Surface Mount Ceramic Capacitor Products
# Ceramic Chip Capacitors

## Table of Contents

<table>
<thead>
<tr>
<th>Section</th>
<th>Pages</th>
</tr>
</thead>
<tbody>
<tr>
<td>How to Order - AVX Part Number Explanation</td>
<td>2-3</td>
</tr>
<tr>
<td><strong>C0G (NP0) Dielectric</strong></td>
<td>4</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>5</td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>6-7</td>
</tr>
<tr>
<td><strong>U Dielectric</strong></td>
<td></td>
</tr>
<tr>
<td>RF/Microwave C0G (NP0) Capacitors (RoHS)</td>
<td>8-10</td>
</tr>
<tr>
<td>General Information and Capacitance Range</td>
<td></td>
</tr>
<tr>
<td>RF/Microwave C0G (NP0) Capacitors (Sn/Pb)</td>
<td>11-12</td>
</tr>
<tr>
<td>General Information and Capacitance Range</td>
<td></td>
</tr>
<tr>
<td>RF/Microwave C0G (NP0) Capacitors (RoHS)</td>
<td>13-14</td>
</tr>
<tr>
<td>AEC-Q200 Qualified</td>
<td></td>
</tr>
<tr>
<td>Designer Kits</td>
<td>15</td>
</tr>
<tr>
<td><strong>X8R/X8L Dielectric</strong></td>
<td>16-17</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>18</td>
</tr>
<tr>
<td><strong>X7R Dielectric</strong></td>
<td>19</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>20</td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>21-22</td>
</tr>
<tr>
<td><strong>X7S Dielectric</strong></td>
<td>23</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>24</td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>25</td>
</tr>
<tr>
<td><strong>X5R Dielectric</strong></td>
<td>26</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>27</td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>28-29</td>
</tr>
<tr>
<td><strong>Y5V Dielectric</strong></td>
<td>30</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>31</td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>32</td>
</tr>
<tr>
<td><strong>MLCC Gold Termination (AU Series)</strong></td>
<td>33</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>34-39</td>
</tr>
<tr>
<td><strong>MLCC Tin/Lead Termination (LD Series)</strong></td>
<td></td>
</tr>
<tr>
<td>C0G (NP0) General Specifications</td>
<td>40</td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>41</td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>42-43</td>
</tr>
<tr>
<td><strong>X8R</strong></td>
<td>44</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>45</td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>46</td>
</tr>
<tr>
<td><strong>X7R</strong></td>
<td>47</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>48</td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>49-50</td>
</tr>
<tr>
<td><strong>X5R</strong></td>
<td>51</td>
</tr>
<tr>
<td>General Specifications</td>
<td></td>
</tr>
<tr>
<td>Specifications and Test Methods</td>
<td>52</td>
</tr>
<tr>
<td>Capacitance Range</td>
<td>53</td>
</tr>
</tbody>
</table>

**Automotive MLCC**
- General Specifications .................................................................. 55-56
- Capacitance Range ....................................................................... 57-59
- APS for COTS+ Applications....................................................... 60
- Capacitance Range ....................................................................... 61-63
- **MLCC with FLEXITERM®**                                          | 64    |
- General Description ................................................................... 65-66
- Specifications and Test Methods.................................................. 67-68
- **FLEXISAFE MLC Chips**                                           | 69    |
- General Specifications and Capacitance Range.............................. 69

**Capacitor Array**
- Capacitor Array (IPC) .................................................................. 70-73
- Automotive Capacitor Array (IPC)................................................ 74
- Part and Pad Layout Dimensions .................................................. 75

**Low Inductance Capacitors**
- Introduction .................................................................................. 76-77
- LICC (Low Inductance Chip Capacitors) ........................................ 78-81
- IDC (InterDigitated Capacitors)..................................................... 82-85
- LGA (Low Inductance Capacitors).................................................... 86-88

**High Temperature MLCCs**
- AT Series 200ºC & 250ºC Rated MLCCs .......................................... 89-94

**High Voltage MLC Chips**
- 600V to 5000V Applications.......................................................... 95-99
- Tin/Lead Termination “B” - 600V to 5000V Applications .................. 100-101
- FLEXITERM® - 600V to 5000V Applications...................................... 102-106
- 600V to 3000V Automotive Applications – AEC-Q200 ...................... 107-108

**MIL-PRF-55681/Chips**
- CDR01 thru CDR06 .......................................................................... 109-110
- CDR31 thru CDR35 .......................................................................... 111-114
- **MLCC Medical**
  - MO Series .................................................................................. 115-123
  - MM Series .................................................................................. 124-128

**Packaging of Chip Components**..................................................... 129
- Embossed Carrier Configuration - 8 & 12mm Tape ........................... 130
- Paper Carrier Configuration - 8 & 12mm Tape ................................. 131

**Basic Capacitor Formulas**............................................................. 132
- General Description ...................................................................... 133-137

**Surface Mounting Guide**............................................................... 138-142
### How to Order

#### Part Number Explanation

**Commercial Surface Mount Chips**

**EXAMPLE: 08055A101JAT2A**

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance</th>
<th>Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0805</td>
<td>5</td>
<td>A</td>
<td>101</td>
<td>J*</td>
<td>A</td>
<td>T</td>
<td>2</td>
<td>A**</td>
</tr>
<tr>
<td></td>
<td>4 = 4V</td>
<td>A = NP0(C0G)</td>
<td>2 Sig, Fig + No. of Zeros</td>
<td>B = ±10 pF</td>
<td>A = N/A</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>A = Std</td>
</tr>
<tr>
<td></td>
<td>6 = 6.3V</td>
<td>C = X7R</td>
<td></td>
<td>C = ±25 pF</td>
<td>4 = Automotive</td>
<td>4 = Gold Plated</td>
<td>4 = 13” Reel</td>
<td>K = 30K (0603 2mm pitch)</td>
</tr>
<tr>
<td></td>
<td>201</td>
<td>D = X5R</td>
<td></td>
<td>D = ±50 pF</td>
<td>T = Conductive Exposy for Hybrid Applications</td>
<td>U = 4mm TR</td>
<td>22K (0805/1206 &lt;0.030” / 0.76mm)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0402</td>
<td>F = X8R</td>
<td></td>
<td>F = ±1%</td>
<td>Z = FLEXITERM® with 5% min lead (X7R &amp; X8R only)</td>
<td></td>
<td>18K (0603/0805/1206 &lt;0.037” / 0.94mm)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0603</td>
<td>G = Y5V</td>
<td></td>
<td>G = ±2%</td>
<td></td>
<td>J = 15K (0805/1206 &lt;0.050” / 1.27mm)</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0805</td>
<td>U = U Series</td>
<td></td>
<td>J = ±5%</td>
<td></td>
<td>1 = 12K (0805/1206 &lt;0.065 / 1.4mm)</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1206</td>
<td>W = X6S</td>
<td></td>
<td>J = ±10%</td>
<td></td>
<td><strong>Non std options upon approval from the factory</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1210</td>
<td>Z = X7S</td>
<td></td>
<td>M = ±20%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1812</td>
<td></td>
<td></td>
<td>Z = +80%,</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1825</td>
<td></td>
<td></td>
<td>-20%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2220</td>
<td></td>
<td></td>
<td>P = +100%,</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2225</td>
<td></td>
<td></td>
<td>-0%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Contact Factory for Special Voltages**

- F = 63V
- 9 = 300V
- * = 75V
- X = 350V
- E = 150V
- 8 = 400V
- V = 250V

**NOTE:** Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. For Tin/Lead Terminations, please refer to LD Series.

### High Voltage MLC Chips

**EXAMPLE: 1808AA271KA11A**

<table>
<thead>
<tr>
<th>AVX Style</th>
<th>Voltage</th>
<th>Temperature Coefficient</th>
<th>Capacitance Code</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Termination</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0805</td>
<td>A</td>
<td>A</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
<td>A = Standard</td>
</tr>
<tr>
<td>1206</td>
<td>A</td>
<td>A</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
<td>A = Standard</td>
</tr>
<tr>
<td>1210</td>
<td>A</td>
<td>A</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
<td>A = Standard</td>
</tr>
<tr>
<td>1808</td>
<td>A</td>
<td>A</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
<td>A = Standard</td>
</tr>
<tr>
<td>1812</td>
<td>A</td>
<td>A</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
<td>A = Standard</td>
</tr>
<tr>
<td>1825</td>
<td>A</td>
<td>A</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
<td>A = Standard</td>
</tr>
<tr>
<td>2220</td>
<td>A</td>
<td>A</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
<td>A = Standard</td>
</tr>
<tr>
<td>2225</td>
<td>A</td>
<td>A</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
<td>A = Standard</td>
</tr>
<tr>
<td>3640</td>
<td>A</td>
<td>A</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
<td>A = Standard</td>
</tr>
</tbody>
</table>

**Contact Factory for Special Voltages**

<table>
<thead>
<tr>
<th>AVX Style</th>
<th>Voltage</th>
<th>Temperature Coefficient</th>
<th>Capacitance Code</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Termination</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0805</td>
<td>A</td>
<td>A</td>
<td>271</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
</tr>
<tr>
<td>1206</td>
<td>A</td>
<td>A</td>
<td>271</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
</tr>
<tr>
<td>1210</td>
<td>A</td>
<td>A</td>
<td>271</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
</tr>
<tr>
<td>1808</td>
<td>A</td>
<td>A</td>
<td>271</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
</tr>
<tr>
<td>1812</td>
<td>A</td>
<td>A</td>
<td>271</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
</tr>
<tr>
<td>1825</td>
<td>A</td>
<td>A</td>
<td>271</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
</tr>
<tr>
<td>2220</td>
<td>A</td>
<td>A</td>
<td>271</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
</tr>
<tr>
<td>2225</td>
<td>A</td>
<td>A</td>
<td>271</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
</tr>
<tr>
<td>3640</td>
<td>A</td>
<td>A</td>
<td>271</td>
<td>K</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>4 = 13” Reel</td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. For Tin/Lead Terminations, please refer to LD Series.

---

* B, C & D tolerance for ≤10 pF values.

Standard Tape and Reel material (Paper/Embossed) depends upon chip size and thickness. See individual part tables for tape material type for each capacitance value.

**NOTE:** Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. For Tin/Lead Terminations, please refer to LD Series.

For RoHS compliant products, please select correct termination style.
How to Order

Part Number Explanation

**Capacitor Array**

**EXAMPLE: W2A43C103MAT2A**

<table>
<thead>
<tr>
<th>W</th>
<th>2</th>
<th>A</th>
<th>4</th>
<th>3</th>
<th>C</th>
<th>103</th>
<th>M</th>
<th>A</th>
<th>T</th>
<th>2A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Style</td>
<td>Case Size</td>
<td>Array</td>
<td>Number of Caps</td>
<td>Voltage</td>
<td>Dielectric</td>
<td>Capacitance Code (in pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Termination Code</td>
<td>Packaging &amp; Quantity Code</td>
</tr>
<tr>
<td>W = RoHS</td>
<td>1 = 0402</td>
<td>2 = 0508</td>
<td>3 = 0612</td>
<td>Z = 10V</td>
<td>A = NPO</td>
<td>2 Sig Digits + Number of Zeros</td>
<td>K = ±10%</td>
<td>A = Commercial</td>
<td>T = Plated Ni and Sn</td>
<td>2A = 7” Reel (4000)</td>
</tr>
<tr>
<td>L = SnPb</td>
<td>0 = 0306</td>
<td>1 = 0402</td>
<td>2 = 0508</td>
<td>3 = 0612</td>
<td>C = X7R</td>
<td>D = X5R</td>
<td>M = ±20%</td>
<td></td>
<td></td>
<td>4A = 13” Reel (10000)</td>
</tr>
</tbody>
</table>

**Low Inductance Capacitors (LICC)**

**EXAMPLE: 0612ZD105MAT2A**

<table>
<thead>
<tr>
<th>0612</th>
<th>Z</th>
<th>105</th>
<th>M</th>
<th>A</th>
<th>T</th>
<th>2</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>Voltage</td>
<td>Capacitance Code (in pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Terminations</td>
<td>Packaging Available</td>
<td>Thickness</td>
</tr>
<tr>
<td>0306</td>
<td>6 = 6.3V</td>
<td>2 Sig Digits + Number of Zeros</td>
<td>K = ±10%</td>
<td>A = N/A</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>See Page 81 for Codes</td>
</tr>
<tr>
<td>0508</td>
<td>Z = 10V</td>
<td></td>
<td>M = ±20%</td>
<td></td>
<td></td>
<td>4 = 13” Reel</td>
<td></td>
</tr>
<tr>
<td>0612</td>
<td>3 = 25V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD16</td>
<td>5 = 50V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD17</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD18</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Interdigitated Capacitors (IDC)**

**EXAMPLE: W3L16D225MAT3A**

<table>
<thead>
<tr>
<th>W</th>
<th>3</th>
<th>L</th>
<th>1</th>
<th>6</th>
<th>D</th>
<th>225</th>
<th>M</th>
<th>A</th>
<th>T</th>
<th>3</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Style</td>
<td>Case Size</td>
<td>Low Inductance</td>
<td>Number of Terminals</td>
<td>Voltage</td>
<td>Dielectric</td>
<td>Capacitance Code (in pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Terminations</td>
<td>Packaging Available</td>
<td>Thickness</td>
</tr>
<tr>
<td>W = RoHS</td>
<td>1 = 0402</td>
<td>2 = 0508</td>
<td>3 = 0612</td>
<td>Z = 10V</td>
<td>A = NPO</td>
<td>2 Sig Digits + Number of Zeros</td>
<td>M = ±20%</td>
<td>A = Commercial</td>
<td>T = Plated Ni and Sn</td>
<td>3 = 13” Reel</td>
<td>Max. Thickness (mm)</td>
</tr>
<tr>
<td>L = SnPb</td>
<td></td>
<td></td>
<td></td>
<td>6 = 6.3V</td>
<td>C = X7R</td>
<td>D = X5R</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>A=0.95 (0.037)</td>
</tr>
</tbody>
</table>

**Low Inductance Decoupling Capacitor Arrays (LICA)**

**EXAMPLE: LICA3T183M3FC4AA**

<table>
<thead>
<tr>
<th>LICA</th>
<th>3</th>
<th>T</th>
<th>102</th>
<th>M</th>
<th>3</th>
<th>F</th>
<th>C</th>
<th>4</th>
<th>A</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>5V = 9</td>
<td>10V = Z</td>
<td>D = X5R</td>
<td>M = ±20%</td>
<td>6 = 0.500mm</td>
<td>M = 7” Reel</td>
<td>A = Standard</td>
<td>A = Bar</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>25V = 3</td>
<td></td>
<td>T = SS5S</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*Not RoHS Compliant*

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.
C0G (NP0) Dielectric

General Specifications

C0G (NP0) is the most popular formulation of the “temperature-compensating,” EIA Class I ceramic materials. Modern C0G (NP0) formulations contain neodymium, samarium and other rare earth oxides.

C0G (NP0) ceramics offer one of the most stable capacitor dielectrics available. Capacitance change with temperature is 0 ±30ppm/°C which is less than ±0.3% C from -55°C to +125°C. Capacitance drift or hysteresis for C0G (NP0) ceramics is negligible at less than ±0.05% versus up to ±2% for films. Typical capacitance change with life is less than ±0.1% for C0G (NP0), one-fifth that shown by most other dielectrics. C0G (NP0) formulations show no aging characteristics.

PART NUMBER (see page 2 for complete part number explanation)

0805 5 A 101 J A T 2 A

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.
## C0G (NP0) Dielectric Specifications and Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>NP0 Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55°C to +125°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td>Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF 1.0 kHz ± 10% for cap &gt; 1000 pF Voltage: 1.0Vrms ± .2V</td>
</tr>
<tr>
<td>Q</td>
<td>&lt;30 pF: Q ≥ 400+20 x Cap Value ≥30 pF: Q ≥ 1000</td>
<td>Charge device with rated voltage for 60 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>100,000MΩ or 1000MΩ - µF, whichever is less</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>No breakdown or visual defects</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Resistance to Flexure Stresses</th>
<th>Appearance</th>
<th>No defects</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Variation</td>
<td>±5% or ±5 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Solderability</th>
<th>Appearance</th>
<th>≥ 95% of each terminal should be covered with fresh solder</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resistance to Solder Heat</td>
<td>Appearance</td>
<td>No defects, &lt;25% leaching of either terminal</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±2.5% or ±25 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Thermal Shock</th>
<th>Appearance</th>
<th>No visual defects</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±2.5% or ±25 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Load Life</th>
<th>Appearance</th>
<th>No visual defects</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±3.0% or ±3 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q (C=Nominal Cap)</td>
<td>≥ 30 pF: Q ≥ 350 ≥10 pF, &lt;30 pF: Q ≥ 275 +5C/2 &lt;10 pF: Q ≥ 200 +10C</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Load Humidity</th>
<th>Appearance</th>
<th>No visual defects</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±5.0% or ±5 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>≥ 30 pF: Q ≥ 350 ≥10 pF, &lt;30 pF: Q ≥ 275 +5C/2 &lt;10 pF: Q ≥ 200 +10C</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
# C0G (NP0) Dielectric Capacitance Range

## Preferred Sizes Are Shaded

<table>
<thead>
<tr>
<th>Size</th>
<th>0101*</th>
<th>0201</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
</tr>
<tr>
<td>Packaging</td>
<td>All Paper</td>
<td>All Paper</td>
<td>All Paper</td>
<td>All Paper</td>
<td>All Paper</td>
<td>Paper/Embosed</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>(L) Length (in.)</th>
<th>0.40 ± 0.02</th>
<th>0.60 ± 0.09</th>
<th>1.00 ± 0.10</th>
<th>1.60 ± 0.15</th>
<th>2.01 ± 0.20</th>
<th>3.20 ± 0.20</th>
</tr>
</thead>
<tbody>
<tr>
<td>(W) Width (in.)</td>
<td>0.30 ± 0.09</td>
<td>0.60 ± 0.10</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
<td>1.60 ± 0.20</td>
<td>(0.063 ± 0.008)</td>
</tr>
<tr>
<td>(T) Terminal (in.)</td>
<td>0.10 ± 0.04</td>
<td>0.15 ± 0.05</td>
<td>0.25 ± 0.15</td>
<td>0.35 ± 0.15</td>
<td>0.50 ± 0.25</td>
<td>0.50 ± 0.25</td>
</tr>
</tbody>
</table>

## Capacitance Range

<table>
<thead>
<tr>
<th>Size</th>
<th>0101*</th>
<th>0201</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cap (µF)</td>
<td>0.047</td>
<td>0.027</td>
<td>0.010</td>
<td>0.047</td>
<td>0.100</td>
<td>0.201</td>
</tr>
<tr>
<td></td>
<td>(0.008 ± 0.0008)</td>
<td>(0.022)</td>
<td>(0.006 ± 0.004)</td>
<td>(0.011 ± 0.004)</td>
<td>(0.037)</td>
<td>(0.100)</td>
</tr>
<tr>
<td></td>
<td>0.047</td>
<td>0.027</td>
<td>0.010</td>
<td>0.047</td>
<td>0.100</td>
<td>0.201</td>
</tr>
<tr>
<td></td>
<td>(0.008 ± 0.0008)</td>
<td>(0.022)</td>
<td>(0.006 ± 0.004)</td>
<td>(0.011 ± 0.004)</td>
<td>(0.037)</td>
<td>(0.100)</td>
</tr>
</tbody>
</table>

## Max. Thickness

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>B</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.33</td>
<td>(0.013)</td>
<td>(0.015)</td>
<td>(0.015)</td>
<td>(0.021)</td>
<td>(0.027)</td>
<td>(0.035)</td>
<td>(0.035)</td>
<td>(0.047)</td>
<td>(0.065)</td>
<td>(0.065)</td>
<td>(0.070)</td>
<td>(0.070)</td>
<td>(0.08)</td>
<td>(0.10)</td>
</tr>
</tbody>
</table>

## Enlarged View

![Image of capacitor][1]

---

[1]: #134x201 to 239x259

---
### Capacitance Range

#### C0G (NP0) Dielectric

<table>
<thead>
<tr>
<th>Capacitance Range</th>
<th>Preferred Sizes Are Shaded</th>
</tr>
</thead>
</table>

**Soldering**
- Reflow Only

**Packaging**
- Paper/Embossed
- All Embossed

<table>
<thead>
<tr>
<th>Size</th>
<th>1210</th>
<th>1812</th>
<th>1825</th>
<th>2220</th>
<th>2225</th>
</tr>
</thead>
<tbody>
<tr>
<td>Paper/Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Packaging</th>
<th>WVDC</th>
<th>WVDC</th>
<th>WVDC</th>
<th>WVDC</th>
<th>WVDC</th>
</tr>
</thead>
<tbody>
<tr>
<td>(µF) 0.012</td>
<td>0.015</td>
<td>0.018</td>
<td>0.022</td>
<td>0.027</td>
<td>0.035</td>
</tr>
<tr>
<td>Thickness</td>
<td>(pF) 1.0</td>
<td>1.2</td>
<td>1.4</td>
<td>1.5</td>
<td>1.7</td>
</tr>
</tbody>
</table>

**Letter**
- A
- B
- C
- E
- G
- J
- K
- M
- N
- P
- Q
- Z

**Max.**
- 0.178
- 0.33
- 0.33
- 0.68
- 0.1
- 0.082
- 0.071
- 0.062
- 0.050
- 0.040
- 0.030
- 0.027

**Thickness**
- 0.003 (µin.)
- 0.003 (µin.)
- 0.003 (µin.)
- 0.003 (µin.)
- 0.003 (µin.)
- 0.003 (µin.)
- 0.003 (µin.)

**Paper/Embossed**
- Reflow Only

**All Embossed**
- Reflow Only

**EMBOSSED**
- Reflow Only
GENERAL INFORMATION

“U” Series capacitors are C0G (NP0) chip capacitors specially designed for “Ultra” low ESR for applications in the communications market. Max ESR and effective capacitance are met on each value producing lot to lot uniformity. Sizes available are EIA chip sizes 0402, 0603, 0805, and 1210.

DIMENSIONS: inches (millimeters)

<table>
<thead>
<tr>
<th>Size</th>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
<th>E</th>
</tr>
</thead>
<tbody>
<tr>
<td>0402</td>
<td>0.039±0.004 (1.00±0.1)</td>
<td>0.020±0.004 (0.50±0.1)</td>
<td>0.022 (0.55mm) max</td>
<td>N/A</td>
<td>N/A</td>
</tr>
<tr>
<td>0603</td>
<td>0.060±0.010 (1.52±0.25)</td>
<td>0.030±0.010 (0.76±0.25)</td>
<td>0.036 (0.91mm) max</td>
<td>0.010±0.005 (0.25±0.13)</td>
<td>0.030 (0.76) min</td>
</tr>
<tr>
<td>0805</td>
<td>0.079±0.008 (2.01±0.2)</td>
<td>0.049±0.008 (1.25±0.2)</td>
<td>0.040±0.005 (1.02±0.127)</td>
<td>0.020±0.010 (0.51±0.255)</td>
<td>0.020 (0.51) min</td>
</tr>
<tr>
<td>1210</td>
<td>0.126±0.008 (3.2±0.2)</td>
<td>0.098±0.008 (2.49±0.2)</td>
<td>0.050±0.005 (1.27±0.127)</td>
<td>0.025±0.015 (0.635±0.381)</td>
<td>0.040 (1.02) min</td>
</tr>
</tbody>
</table>

ELECTRICAL CHARACTERISTICS

Capacitance Values and Tolerances:
- Size 0402 - 0.2 pF to 30 pF @ 1 MHz
- Size 0603 - 1.0 pF to 100 pF @ 1 MHz
- Size 0805 - 1.6 pF to 160 pF @ 1 MHz
- Size 1210 - 2.4 pF to 1000 pF @ 1 MHz

Temperature Coefficient of Capacitance (TC):
0±30 ppm/°C (-55° to +125°C)

Insulation Resistance (IR):
- 10¹² Ω min. @ 25°C and rated WVDC
- 10¹¹ Ω min. @ 125°C and rated WVDC

Working Voltage (WVDC):
- Size 0402 - 100, 50, 25 WVDC
- Size 0603 - 200, 100, 50 WVDC
- Size 0805 - 200, 100 WVDC
- Size 1210 - 200, 100 WVDC

Dielectric Working Voltage (DWV):
250% of rated WVDC

Equivalent Series Resistance Typical (ESR):
- 0402 - See Performance Curve, page 9
- 0603 - See Performance Curve, page 9
- 0805 - See Performance Curve, page 9
- 1210 - See Performance Curve, page 9

Marking:
- Laser marking EIA J marking standard (except 0603) (capacitance code and tolerance upon request).

MILITARY SPECIFICATIONS
- Meets or exceeds the requirements of MIL-C-55681
RF/Microwave C0G (NP0) Capacitors (RoHS)

Ultra Low ESR, “U” Series, C0G (NP0) Chip Capacitors

**CAPACITANCE RANGE**

<table>
<thead>
<tr>
<th>Cap (pF)</th>
<th>Available Size</th>
<th>Tolerance</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1210</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.2</td>
<td>B,C</td>
<td>100V</td>
<td>N/A</td>
<td>N/A</td>
<td>N/A</td>
<td></td>
</tr>
<tr>
<td>0.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.5</td>
<td>B,C</td>
<td>100V</td>
<td>N/A</td>
<td>N/A</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.6</td>
<td>B,C,D</td>
<td>50V</td>
<td>N/A</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.8</td>
<td>B,C,D</td>
<td>50V</td>
<td>N/A</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**ULTRA LOW ESR, “U” SERIES**

**TYPICAL ESR vs. FREQUENCY**

*0402 “U” SERIES*

**TYPICAL ESR vs. FREQUENCY**

*0603 “U” SERIES*

**TYPICAL ESR vs. FREQUENCY**

*0805 “U” SERIES*

**TYPICAL ESR vs. FREQUENCY**

*1210 “U” SERIES*

ESR Measured on the Boonton 34A
RF/Microwave C0G (NP0) Capacitors (Sn/Pb)

Ultra Low ESR, “U” Series, C0G (NP0) Chip Capacitors

GENERAL INFORMATION

“U” Series capacitors are C0G (NP0) chip capacitors specially designed for “Ultra” low ESR for applications in the communications market. Max ESR and effective capacitance are met on each value producing lot to lot uniformity. Sizes available are EIA chip sizes 0402, 0603, 0805, and 1210.

DIMENSIONS: inches (millimeters)

<table>
<thead>
<tr>
<th>Size</th>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
<th>E</th>
</tr>
</thead>
<tbody>
<tr>
<td>0402</td>
<td>0.039±0.004 (1.00±0.1)</td>
<td>0.020±0.004 (0.50±0.1)</td>
<td>0.022 (0.55mm) max</td>
<td>N/A</td>
<td>N/A</td>
</tr>
<tr>
<td>0603</td>
<td>0.060±0.010 (1.52±0.25)</td>
<td>0.030±0.010 (0.76±0.25)</td>
<td>0.036 (0.91mm) max</td>
<td>0.010±0.005 (0.25±0.13)</td>
<td>0.030 (0.76) min</td>
</tr>
<tr>
<td>0805</td>
<td>0.079±0.008 (2.01±0.2)</td>
<td>0.049±0.008 (1.25±0.2)</td>
<td>0.040±0.005 (1.27±0.127)</td>
<td>0.020±0.010 (0.51±0.254)</td>
<td>0.020 (0.51) min</td>
</tr>
<tr>
<td>1210</td>
<td>0.126±0.008 (3.2±0.2)</td>
<td>0.098±0.008 (2.49±0.2)</td>
<td>0.050±0.005 (1.27±0.127)</td>
<td>0.025±0.015 (0.635±0.381)</td>
<td>0.040 (1.02) min</td>
</tr>
</tbody>
</table>

Capacitance Values and Tolerances:
- Size 0402 - 0.2 pF to 22 pF @ 1 MHz
- Size 0603 - 1.0 pF to 100 pF @ 1 MHz
- Size 0805 - 1.6 pF to 160 pF @ 1 MHz
- Size 1210 - 2.4 pF to 1000 pF @ 1 MHz

Temperature Coefficient of Capacitance (TC):
0±30 ppm/°C (-55° to +125°C)

Insulation Resistance (IR):
10^12 Ω min. @ 25°C and rated WVDC
10^11 Ω min. @ 125°C and rated WVDC

Working Voltage (WVDC):
- Size 0402 - 50, 25 WVDC
- Size 0603 - 200, 100, 50 WVDC
- Size 0805 - 200, 100 WVDC
- Size 1210 - 200, 100 WVDC

ELECTRICAL CHARACTERISTICS

Dielectric Working Voltage (DWV):
250% of rated WVDC

Equivalent Series Resistance Typical (ESR):
0402 - See Performance Curve, page 12
0603 - See Performance Curve, page 12
0805 - See Performance Curve, page 12
1210 - See Performance Curve, page 12

Marking: Laser marking EIA J marking standard (except 0603) (capacitance code and tolerance upon request).

MILITARY SPECIFICATIONS
Meets or exceeds the requirements of MIL-C-55681

HOW TO ORDER

Case Size
- LD02 = 0402
- LD03 = 0603
- LD05 = 0805
- LD10 = 1210

Voltage Code
- 3 = 25V
- 5 = 50V
- 1 = 100V
- 2 = 200V

Dielectric = Ultra Low ESR

Capacitance
- EIA Capacitance Code in pF.
- First two digits = significant figures or "R" for decimal place.
- Third digit = number of zeros or after "R" significant figures.

Capacitance Tolerance Code
- B = ±0.1pF
- C = ±0.2pF
- D = ±0.5pF
- F = ±1%
- G = ±2%
- J = ±5%
- K = ±10%
- M = ±20%

Failure Rate Code
- A = Not Applicable

Termination
- *B = 5% min lead

Packaging Code
- 2 = 7" Reel
- 4 = 13" Reel

Special Code
- A = Standard

* Not RoHS Compliant
RF/Microwave C0G (NP0) Capacitors (Sn/Pb)
Ultra Low ESR, “U” Series, C0G (NP0) Chip Capacitors

CAPACITANCE RANGE

<table>
<thead>
<tr>
<th>Cap (pF)</th>
<th>Available Tolerance</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.2</td>
<td>B,C</td>
<td>50V</td>
</tr>
<tr>
<td>0.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.5</td>
<td>B,C</td>
<td></td>
</tr>
<tr>
<td>0.6</td>
<td>B,C,D</td>
<td></td>
</tr>
<tr>
<td>0.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.8</td>
<td>B,C,D</td>
<td></td>
</tr>
<tr>
<td>0.9</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Cap (pF)</th>
<th>Available Tolerance</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0</td>
<td>B,C,D</td>
<td>50V</td>
</tr>
<tr>
<td>1.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.9</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

ULTRA LOW ESR, “U” SERIES

TYPICAL ESR vs. FREQUENCY
0402 “U” SERIES

TYPICAL ESR vs. FREQUENCY
0603 “U” SERIES

TYPICAL ESR vs. FREQUENCY
0805 “U” SERIES

TYPICAL ESR vs. FREQUENCY
1210 “U” SERIES

ESR Measured on the Boonton 34A
RF/Microwave Automotive C0G (NP0) Capacitors (RoHS), AEC Q200 Qualified

Ultra Low ESR, “U” Series, C0G (NP0) Chip Capacitors

**GENERAL INFORMATION**

Automotive “U” Series capacitors are C0G (NP0) chip capacitors specially designed for “Ultra” low ESR for applications in the automotive market. Max ESR and effective capacitance are met on each value producing lot to lot uniformity. Sizes available are EIA chip sizes 0402 and 0603.

**HOW TO ORDER**

**ELECTRICAL CHARACTERISTICS**

Capacitance Values and Tolerances:
Size 0402: 0.2 pF to 22 pF @ 1 MHz
Size 0603: 1.0 pF to 100 pF @ 1 MHz
Temperature Coefficient of Capacitance (TC):
0±30 ppm/°C (-55° to +125°C)
Insulation Resistance (IR):
10^{12} Ω min. @ 25°C and rated WVDC
10^{11} Ω min. @ 125°C and rated WVDC
Working Voltage (WVDC):
Size Working Voltage
0402 - 50, 25 WVDC
0603 - 200, 100, 50 WVDC

Dielectric Working Voltage (DWV):
250% of rated WVDC
Equivalent Series Resistance Typical (ESR):
0402 - See Performance Curve
0603 - See Performance Curve
Automotive Specifications
Meets or exceeds the requirements of AEC Q200
RF/Microwave Automotive C0G (NP0) Capacitors (RoHS), AEC Q200 Qualified
Ultra Low ESR, “U” Series, C0G (NP0) Chip Capacitors

CAPACITANCE RANGE

<table>
<thead>
<tr>
<th>Cap (pF)</th>
<th>Tolerance</th>
<th>Size 0402</th>
<th>Size 0603</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.2</td>
<td>B, C</td>
<td>0.3</td>
<td>B, C</td>
</tr>
<tr>
<td>0.3</td>
<td>B, C</td>
<td>0.4</td>
<td>B, C</td>
</tr>
<tr>
<td>0.4</td>
<td>B, C</td>
<td>0.5</td>
<td>B, C</td>
</tr>
<tr>
<td>0.5</td>
<td>B, C</td>
<td>0.6</td>
<td>B, C</td>
</tr>
<tr>
<td>0.6</td>
<td>B, C</td>
<td>0.7</td>
<td>B, C</td>
</tr>
<tr>
<td>0.7</td>
<td>B, C</td>
<td>0.8</td>
<td>B, C</td>
</tr>
<tr>
<td>0.8</td>
<td>B, C</td>
<td>0.9</td>
<td>B, C</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Cap (pF)</th>
<th>Tolerance</th>
<th>Size 0402</th>
<th>Size 0603</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0</td>
<td>B, C, D</td>
<td>1.1</td>
<td>B, C, D</td>
</tr>
<tr>
<td>1.1</td>
<td>B, C, D</td>
<td>1.2</td>
<td>B, C, D</td>
</tr>
<tr>
<td>1.2</td>
<td>B, C, D</td>
<td>1.3</td>
<td>B, C, D</td>
</tr>
<tr>
<td>1.3</td>
<td>B, C, D</td>
<td>1.4</td>
<td>B, C, D</td>
</tr>
<tr>
<td>1.4</td>
<td>B, C, D</td>
<td>1.5</td>
<td>B, C, D</td>
</tr>
<tr>
<td>1.5</td>
<td>B, C, D</td>
<td>1.6</td>
<td>B, C, D</td>
</tr>
<tr>
<td>1.6</td>
<td>B, C, D</td>
<td>1.7</td>
<td>B, C, D</td>
</tr>
<tr>
<td>1.7</td>
<td>B, C, D</td>
<td>1.8</td>
<td>B, C, D</td>
</tr>
<tr>
<td>1.8</td>
<td>B, C, D</td>
<td>1.9</td>
<td>B, C, D</td>
</tr>
<tr>
<td>1.9</td>
<td>B, C, D</td>
<td>2.0</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.0</td>
<td>B, C, D</td>
<td>2.1</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.1</td>
<td>B, C, D</td>
<td>2.2</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.2</td>
<td>B, C, D</td>
<td>2.3</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.3</td>
<td>B, C, D</td>
<td>2.4</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.4</td>
<td>B, C, D</td>
<td>2.5</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.5</td>
<td>B, C, D</td>
<td>2.6</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.6</td>
<td>B, C, D</td>
<td>2.7</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.7</td>
<td>B, C, D</td>
<td>2.8</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.8</td>
<td>B, C, D</td>
<td>2.9</td>
<td>B, C, D</td>
</tr>
<tr>
<td>2.9</td>
<td>B, C, D</td>
<td>3.0</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.0</td>
<td>B, C, D</td>
<td>3.1</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.1</td>
<td>B, C, D</td>
<td>3.2</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.2</td>
<td>B, C, D</td>
<td>3.3</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.3</td>
<td>B, C, D</td>
<td>3.4</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.4</td>
<td>B, C, D</td>
<td>3.5</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.5</td>
<td>B, C, D</td>
<td>3.6</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.6</td>
<td>B, C, D</td>
<td>3.7</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.7</td>
<td>B, C, D</td>
<td>3.8</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.8</td>
<td>B, C, D</td>
<td>3.9</td>
<td>B, C, D</td>
</tr>
<tr>
<td>3.9</td>
<td>B, C, D</td>
<td>4.0</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.0</td>
<td>B, C, D</td>
<td>4.1</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.1</td>
<td>B, C, D</td>
<td>4.2</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.2</td>
<td>B, C, D</td>
<td>4.3</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.3</td>
<td>B, C, D</td>
<td>4.4</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.4</td>
<td>B, C, D</td>
<td>4.5</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.5</td>
<td>B, C, D</td>
<td>4.6</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.6</td>
<td>B, C, D</td>
<td>4.7</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.7</td>
<td>B, C, D</td>
<td>4.8</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.8</td>
<td>B, C, D</td>
<td>4.9</td>
<td>B, C, D</td>
</tr>
<tr>
<td>4.9</td>
<td>B, C, D</td>
<td>5.0</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.0</td>
<td>B, C, D</td>
<td>5.1</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.1</td>
<td>B, C, D</td>
<td>5.2</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.2</td>
<td>B, C, D</td>
<td>5.3</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.3</td>
<td>B, C, D</td>
<td>5.4</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.4</td>
<td>B, C, D</td>
<td>5.5</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.5</td>
<td>B, C, D</td>
<td>5.6</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.6</td>
<td>B, C, D</td>
<td>5.7</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.7</td>
<td>B, C, D</td>
<td>5.8</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.8</td>
<td>B, C, D</td>
<td>5.9</td>
<td>B, C, D</td>
</tr>
<tr>
<td>5.9</td>
<td>B, C, D</td>
<td>6.0</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.0</td>
<td>B, C, D</td>
<td>6.1</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.1</td>
<td>B, C, D</td>
<td>6.2</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.2</td>
<td>B, C, D</td>
<td>6.3</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.3</td>
<td>B, C, D</td>
<td>6.4</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.4</td>
<td>B, C, D</td>
<td>6.5</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.5</td>
<td>B, C, D</td>
<td>6.6</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.6</td>
<td>B, C, D</td>
<td>6.7</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.7</td>
<td>B, C, D</td>
<td>6.8</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.8</td>
<td>B, C, D</td>
<td>6.9</td>
<td>B, C, D</td>
</tr>
<tr>
<td>6.9</td>
<td>B, C, D</td>
<td>7.0</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.0</td>
<td>B, C, D</td>
<td>7.1</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.1</td>
<td>B, C, D</td>
<td>7.2</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.2</td>
<td>B, C, D</td>
<td>7.3</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.3</td>
<td>B, C, D</td>
<td>7.4</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.4</td>
<td>B, C, D</td>
<td>7.5</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.5</td>
<td>B, C, D</td>
<td>7.6</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.6</td>
<td>B, C, D</td>
<td>7.7</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.7</td>
<td>B, C, D</td>
<td>7.8</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.8</td>
<td>B, C, D</td>
<td>7.9</td>
<td>B, C, D</td>
</tr>
<tr>
<td>7.9</td>
<td>B, C, D</td>
<td>8.0</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.0</td>
<td>B, C, D</td>
<td>8.1</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.1</td>
<td>B, C, D</td>
<td>8.2</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.2</td>
<td>B, C, D</td>
<td>8.3</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.3</td>
<td>B, C, D</td>
<td>8.4</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.4</td>
<td>B, C, D</td>
<td>8.5</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.5</td>
<td>B, C, D</td>
<td>8.6</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.6</td>
<td>B, C, D</td>
<td>8.7</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.7</td>
<td>B, C, D</td>
<td>8.8</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.8</td>
<td>B, C, D</td>
<td>8.9</td>
<td>B, C, D</td>
</tr>
<tr>
<td>8.9</td>
<td>B, C, D</td>
<td>9.0</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.0</td>
<td>B, C, D</td>
<td>9.1</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.1</td>
<td>B, C, D</td>
<td>9.2</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.2</td>
<td>B, C, D</td>
<td>9.3</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.3</td>
<td>B, C, D</td>
<td>9.4</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.4</td>
<td>B, C, D</td>
<td>9.5</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.5</td>
<td>B, C, D</td>
<td>9.6</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.6</td>
<td>B, C, D</td>
<td>9.7</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.7</td>
<td>B, C, D</td>
<td>9.8</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.8</td>
<td>B, C, D</td>
<td>9.9</td>
<td>B, C, D</td>
</tr>
<tr>
<td>9.9</td>
<td>B, C, D</td>
<td>10.0</td>
<td>B, C, D</td>
</tr>
</tbody>
</table>

ULTRA LOW ESR, “U” SERIES

TYPICAL ESR vs. FREQUENCY
0402 “U” SERIES

TYPICAL ESR vs. FREQUENCY
0603 “U” SERIES

TYPICAL SERIES RESONANT FREQUENCY
“U” SERIES CHIP
## Designer Kits

### Communication Kits “U” Series

#### “U” SERIES KITS

<table>
<thead>
<tr>
<th></th>
<th>0402 Kit 5000 UZ</th>
<th></th>
<th>0603 Kit 4000 UZ</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Cap. Value pF</strong></td>
<td><strong>Tolerance</strong></td>
<td>**Cap.</td>
<td><strong>Cap. Value pF</strong></td>
<td><strong>Tolerance</strong></td>
</tr>
<tr>
<td></td>
<td><strong>Value Tolerance</strong></td>
<td><strong>Value</strong></td>
<td></td>
<td><strong>Value Tolerance</strong></td>
</tr>
<tr>
<td>0.5</td>
<td>4.7</td>
<td>B (±0.1pF)</td>
<td>1.0</td>
<td>6.8</td>
</tr>
<tr>
<td>1.0</td>
<td>5.6</td>
<td>6.8</td>
<td>1.2</td>
<td>7.5</td>
</tr>
<tr>
<td>1.5</td>
<td>6.8</td>
<td>8.2</td>
<td>1.8</td>
<td>10.0</td>
</tr>
<tr>
<td>1.8</td>
<td>10.0</td>
<td></td>
<td>2.0</td>
<td>12.0</td>
</tr>
<tr>
<td>2.2</td>
<td>12.0</td>
<td></td>
<td>2.4</td>
<td>15.0</td>
</tr>
<tr>
<td>2.4</td>
<td>15.0</td>
<td></td>
<td>2.7</td>
<td>18.0</td>
</tr>
<tr>
<td>3.0</td>
<td>B (±0.1pF)</td>
<td></td>
<td>3.0</td>
<td>22.0</td>
</tr>
<tr>
<td>3.3</td>
<td>36.0</td>
<td></td>
<td>3.3</td>
<td>27.0</td>
</tr>
<tr>
<td>3.6</td>
<td>39.0</td>
<td></td>
<td>3.9</td>
<td>33.0</td>
</tr>
<tr>
<td></td>
<td>47.0</td>
<td></td>
<td>4.7</td>
<td>47.0</td>
</tr>
<tr>
<td></td>
<td>56.0</td>
<td></td>
<td>5.1</td>
<td>56.0</td>
</tr>
<tr>
<td></td>
<td>68.0</td>
<td></td>
<td>5.6</td>
<td>68.0</td>
</tr>
<tr>
<td></td>
<td>82.0</td>
<td></td>
<td>6.8</td>
<td>82.0</td>
</tr>
<tr>
<td></td>
<td>B (±0.1pF)</td>
<td></td>
<td>8.2</td>
<td>82.0</td>
</tr>
<tr>
<td></td>
<td>9.1</td>
<td></td>
<td>9.1</td>
<td>92.0</td>
</tr>
<tr>
<td></td>
<td>10.0</td>
<td></td>
<td>10.0</td>
<td>100.0</td>
</tr>
<tr>
<td></td>
<td>12.0</td>
<td></td>
<td>12.0</td>
<td>130.0</td>
</tr>
<tr>
<td></td>
<td>130.0</td>
<td></td>
<td>130.0</td>
<td>130.0</td>
</tr>
<tr>
<td></td>
<td>160.0</td>
<td></td>
<td>160.0</td>
<td>160.0</td>
</tr>
</tbody>
</table>

***25 each of 15 values

<table>
<thead>
<tr>
<th></th>
<th>0805 Kit 3000 UZ</th>
<th></th>
<th>1210 Kit 3500 UZ</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Cap. Value pF</strong></td>
<td><strong>Tolerance</strong></td>
<td>**Cap.</td>
<td><strong>Cap. Value pF</strong></td>
<td><strong>Tolerance</strong></td>
</tr>
<tr>
<td></td>
<td><strong>Value Tolerance</strong></td>
<td><strong>Value</strong></td>
<td></td>
<td><strong>Value Tolerance</strong></td>
</tr>
<tr>
<td>1.0</td>
<td>15.0</td>
<td>B (±0.1pF)</td>
<td>2.2</td>
<td>36.0</td>
</tr>
<tr>
<td>1.5</td>
<td>18.0</td>
<td></td>
<td>2.7</td>
<td>39.0</td>
</tr>
<tr>
<td>2.2</td>
<td>22.0</td>
<td></td>
<td>4.7</td>
<td>47.0</td>
</tr>
<tr>
<td>2.4</td>
<td>24.0</td>
<td></td>
<td>5.1</td>
<td>51.0</td>
</tr>
<tr>
<td>2.7</td>
<td>27.0</td>
<td></td>
<td>6.8</td>
<td>56.0</td>
</tr>
<tr>
<td>3.0</td>
<td>33.0</td>
<td></td>
<td>8.2</td>
<td>68.0</td>
</tr>
<tr>
<td>3.3</td>
<td>36.0</td>
<td></td>
<td>9.1</td>
<td>82.0</td>
</tr>
<tr>
<td>3.6</td>
<td>B (±0.1pF)</td>
<td></td>
<td></td>
<td>B (±0.1pF)</td>
</tr>
<tr>
<td>3.9</td>
<td>39.0</td>
<td></td>
<td></td>
<td>10.0</td>
</tr>
<tr>
<td>4.7</td>
<td>47.0</td>
<td></td>
<td></td>
<td>13.0</td>
</tr>
<tr>
<td>5.1</td>
<td>51.0</td>
<td></td>
<td></td>
<td>15.0</td>
</tr>
<tr>
<td>5.6</td>
<td>56.0</td>
<td></td>
<td></td>
<td>18.0</td>
</tr>
<tr>
<td>7.5</td>
<td>68.0</td>
<td></td>
<td></td>
<td>20.0</td>
</tr>
<tr>
<td>8.2</td>
<td>82.0</td>
<td></td>
<td></td>
<td>24.0</td>
</tr>
<tr>
<td>9.1</td>
<td>100.0</td>
<td></td>
<td></td>
<td>27.0</td>
</tr>
<tr>
<td>10.0</td>
<td>J (±5%)</td>
<td></td>
<td></td>
<td>30.0</td>
</tr>
<tr>
<td>12.0</td>
<td>130.0</td>
<td></td>
<td></td>
<td>160.0</td>
</tr>
</tbody>
</table>

***25 each of 30 values

<table>
<thead>
<tr>
<th></th>
<th>0603 Kit 4000 UZ</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Cap. Value pF</strong></td>
<td><strong>Tolerance</strong></td>
<td><strong>Cap. Value pF</strong></td>
</tr>
<tr>
<td></td>
<td><strong>Value Tolerance</strong></td>
<td><strong>Value</strong></td>
</tr>
<tr>
<td>1.0</td>
<td>6.8</td>
<td>B (±0.1pF)</td>
</tr>
<tr>
<td>1.5</td>
<td>8.2</td>
<td></td>
</tr>
<tr>
<td>2.0</td>
<td>12.0</td>
<td></td>
</tr>
<tr>
<td>2.7</td>
<td>18.0</td>
<td></td>
</tr>
<tr>
<td>3.3</td>
<td>27.0</td>
<td></td>
</tr>
<tr>
<td>4.7</td>
<td>39.0</td>
<td></td>
</tr>
<tr>
<td>8.2</td>
<td>68.0</td>
<td></td>
</tr>
<tr>
<td>9.1</td>
<td>130.0</td>
<td></td>
</tr>
</tbody>
</table>

***25 each of 24 values

<table>
<thead>
<tr>
<th></th>
<th>0805 Kit 3000 UZ</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Cap. Value pF</strong></td>
<td><strong>Tolerance</strong></td>
<td><strong>Cap. Value pF</strong></td>
</tr>
<tr>
<td></td>
<td><strong>Value Tolerance</strong></td>
<td><strong>Value</strong></td>
</tr>
<tr>
<td>1.0</td>
<td>15.0</td>
<td>B (±0.1pF)</td>
</tr>
<tr>
<td>1.5</td>
<td>18.0</td>
<td></td>
</tr>
<tr>
<td>2.2</td>
<td>22.0</td>
<td></td>
</tr>
<tr>
<td>2.4</td>
<td>24.0</td>
<td></td>
</tr>
<tr>
<td>2.7</td>
<td>27.0</td>
<td></td>
</tr>
<tr>
<td>3.0</td>
<td>33.0</td>
<td></td>
</tr>
<tr>
<td>3.3</td>
<td>36.0</td>
<td></td>
</tr>
<tr>
<td>3.6</td>
<td>B (±0.1pF)</td>
<td></td>
</tr>
<tr>
<td>3.9</td>
<td>39.0</td>
<td></td>
</tr>
<tr>
<td>4.7</td>
<td>47.0</td>
<td></td>
</tr>
<tr>
<td>5.1</td>
<td>51.0</td>
<td></td>
</tr>
<tr>
<td>5.6</td>
<td>56.0</td>
<td></td>
</tr>
<tr>
<td>7.5</td>
<td>68.0</td>
<td></td>
</tr>
<tr>
<td>8.2</td>
<td>82.0</td>
<td></td>
</tr>
<tr>
<td>9.1</td>
<td>100.0</td>
<td></td>
</tr>
<tr>
<td>10.0</td>
<td>J (±5%)</td>
<td></td>
</tr>
<tr>
<td>12.0</td>
<td>130.0</td>
<td></td>
</tr>
</tbody>
</table>

***25 each of 30 values

<table>
<thead>
<tr>
<th></th>
<th>1210 Kit 3500 UZ</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Cap. Value pF</strong></td>
<td><strong>Tolerance</strong></td>
<td><strong>Cap. Value pF</strong></td>
</tr>
<tr>
<td></td>
<td><strong>Value Tolerance</strong></td>
<td><strong>Value</strong></td>
</tr>
<tr>
<td>2.2</td>
<td>36.0</td>
<td></td>
</tr>
<tr>
<td>4.7</td>
<td>47.0</td>
<td></td>
</tr>
<tr>
<td>5.6</td>
<td>56.0</td>
<td></td>
</tr>
<tr>
<td>7.5</td>
<td>82.0</td>
<td></td>
</tr>
<tr>
<td>9.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>10.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>13.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>15.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>20.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>24.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>27.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>30.0</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

***25 each of 30 values

---

** REV 1**

**AVX**
# X8R/X8L Dielectric

AVX has developed a range of multilayer ceramic capacitors designed for use in applications up to 150°C. These capacitors are manufactured with an X8R and an X8L dielectric material. X8R material has capacitance variation of ±15% between -55°C and +150°C. The X8L material has capacitance variation of ±15% between -55°C to 125°C and +15/40% from +125°C to +150°C.

The need for X8R and X8L performance has been driven by customer requirements for parts that operate at elevated temperatures. They provide a highly reliable capacitor with low loss and stable capacitance over temperature.

They are ideal for automotive under the hood sensors, and various industrial applications. Typical industrial application would be drilling monitoring system. They can also be used as bulk capacitors for high temperature camera modules.

Both X8R and X8L dielectric capacitors are automotive AEC-Q200 qualified. Optional termination systems, tin, FLEXITERM® and conductive epoxy for hybrid applications are available. Providing this series with our FLEXITERM® termination system provides further advantage to customers by way of enhanced resistance to both, temperature cycling and mechanical damage.

## X8R

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (in pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0402</td>
<td>10V = Z</td>
<td>X8R = F</td>
<td>2 Sig. Digits + Number of Zeros e.g. 10 F = 106</td>
<td>J = ±5%</td>
<td>K = ±10%</td>
<td>T = Plated Ni and Sn Z = FLEXITERM***</td>
<td>A = Not Applicable</td>
<td></td>
</tr>
<tr>
<td>0603</td>
<td>16V = Y</td>
<td>X8L = L</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0805</td>
<td>25V = Z</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1206</td>
<td>100V = 1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

## X8L

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (in pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0402</td>
<td>10V = Z</td>
<td>X8R = F</td>
<td>2 Sig. Digits + Number of Zeros e.g. 10 F = 106</td>
<td>J = ±5%</td>
<td>K = ±10%</td>
<td>T = Plated Ni and Sn Z = FLEXITERM***</td>
<td>A = Not Applicable</td>
<td></td>
</tr>
<tr>
<td>0603</td>
<td>16V = Y</td>
<td>X8L = L</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0805</td>
<td>25V = Z</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1206</td>
<td>100V = 1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

### Packing Information

<table>
<thead>
<tr>
<th>Size</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
</tr>
<tr>
<td>WVDC</td>
<td>25V</td>
<td>50V</td>
<td>25V</td>
<td>50V</td>
</tr>
<tr>
<td>271 Cap</td>
<td>270 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>331 (µF)</td>
<td>330 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>471</td>
<td>470 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>981</td>
<td>980 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>102</td>
<td>1000 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>152</td>
<td>1500 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>222</td>
<td>2200 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>332</td>
<td>3300 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>472</td>
<td>4700 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>682</td>
<td>6800 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>103 Cap</td>
<td>100 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>153 (µF)</td>
<td>150 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>223</td>
<td>220 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>333</td>
<td>330 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>473</td>
<td>470 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>683</td>
<td>680 G</td>
<td>G</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>104</td>
<td>10 G</td>
<td>N</td>
<td>N</td>
<td>M</td>
</tr>
<tr>
<td>154</td>
<td>15 G</td>
<td>N</td>
<td>N</td>
<td>M</td>
</tr>
<tr>
<td>224</td>
<td>22 G</td>
<td>N</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>334</td>
<td>33 G</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>474</td>
<td>47 G</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>684</td>
<td>68 G</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>105</td>
<td>10</td>
<td>N</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>155</td>
<td>15</td>
<td>N</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>225</td>
<td>22</td>
<td>N</td>
<td>M</td>
<td>M</td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

### Letter Grade Information

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max. Thickness</td>
<td>(-0.013)</td>
<td>(-0.022)</td>
<td>(-0.028)</td>
<td>(-0.035)</td>
<td>(-0.037)</td>
<td>(-0.04)</td>
<td>(-0.05)</td>
<td>(-0.055)</td>
<td>(-0.06)</td>
<td>(-0.07)</td>
<td>(-0.09)</td>
<td>(-0.1)</td>
<td>(-0.11)</td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.
X8R/X8L Dielectric

General Specifications

APPLICATIONS FOR X8R AND X8L CAPACITORS

- All market sectors with a 150°C requirement
- Automotive on engine applications
- Oil exploration applications
- Hybrid automotive applications
  - Battery control
  - Inverter / converter circuits
  - Motor control applications
  - Water pump
- Hybrid commercial applications
  - Emergency circuits
  - Sensors
  - Temperature regulation

ADVANTAGES OF X8R AND X8L MLC CAPACITORS

- Both ranges are qualified to the highest automotive AEC-Q200 standards
- Excellent reliability compared to other capacitor technologies
- RoHS compliant
- Low ESR / ESL compared to other technologies
- Tin solder finish
- FLEXITERM® available
- Epoxy termination for hybrid available
- 100V range available

ENGINEERING TOOLS FOR HIGH VOLTAGE MLC CAPACITORS

- Samples
- Technical Articles
- Application Engineering
- Application Support

![Graph of X8R/X8L Dielectric 0805, 50V, X8R/X8L Typical Temperature Coefficient](image)
# X8R/X8L Dielectric Specifications and Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X8R/X8L Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating Temperature Range</td>
<td>-55ºC to +150ºC</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td>Freq.: 1.0 kHz ± 10%</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ 2.5% for ≥ 50V DC rating</td>
<td>Voltage: 1.0Vrms ± .2V</td>
</tr>
<tr>
<td></td>
<td>≤ 3.5% for 25V DC and 16V DC rating</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>100,000MΩ or 1000MΩ - μF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>No breakdown or visual defects</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Resistance to Flexure Stresses</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects</td>
<td>Dip device in eutectic solder at 230 ± 5ºC for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12%</td>
<td>Dip device in eutectic solder at 260ºC for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
<tr>
<td>Solderability</td>
<td>≥ 95% of each terminal should be covered with fresh solder</td>
<td></td>
</tr>
<tr>
<td>Resistance to Solder Heat</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects, &lt;25% leaching of either end terminal</td>
<td>Dip device in eutectic solder at 230 ± 5ºC for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td>Dip device in eutectic solder at 260ºC for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Thermal Shock</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Step 1: -55ºC ± 2º</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td>Step 2: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 3: +125ºC ± 2º</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 4: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td>Repeat for 5 cycles and measure after 24 ± 2 hours at room temperature</td>
</tr>
<tr>
<td>Load Life</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Charge device with 1.5 rated voltage (≤ 10V) in test chamber set at 150ºC ± 2º for 1000 hours (+48, -0)</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td>Remove from test chamber and stabilize at room temperature for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Load Humidity</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Store in a test chamber set at 85ºC ± 2º/ 85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td>Remove from chamber and stabilize at room temperature and humidity for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
X7R Dielectric
General Specifications

X7R formulations are called “temperature stable” ceramics and fall into EIA Class II materials. X7R is the most popular of these intermediate dielectric constant materials. Its temperature variation of capacitance is within ±15% from -55°C to +125°C. This capacitance change is non-linear.

Capacitance for X7R varies under the influence of electrical operating conditions such as voltage and frequency.

X7R dielectric chip usage covers the broad spectrum of industrial applications where known changes in capacitance due to applied voltages are acceptable.

PART NUMBER (see page 2 for complete part number explanation)

<table>
<thead>
<tr>
<th>0805</th>
<th>5</th>
<th>C</th>
<th>103</th>
<th>M</th>
<th>A</th>
<th>T</th>
<th>2</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size (L” x W”)</td>
<td>Voltage</td>
<td>Dielectric</td>
<td>Capacitance Code (in pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Terminations</td>
<td>Packaging</td>
<td>Special Code</td>
</tr>
<tr>
<td>6.3V = 6</td>
<td>4V = 4</td>
<td>X7R = C</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>J = ± 5%</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>7 = Gold Plated*</td>
<td>A = Std. Product</td>
</tr>
<tr>
<td>10V = Z</td>
<td>6.3V = 6</td>
<td></td>
<td>K = ±10%</td>
<td>Z = FLEXITERM®**</td>
<td>*Optional termination</td>
<td>**See FLEXITERM® X7R section</td>
<td></td>
<td></td>
</tr>
<tr>
<td>16V = Y</td>
<td>10V = Z</td>
<td></td>
<td>M = ± 20%</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>25V = 3</td>
<td>16V = Y</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>50V = 5</td>
<td>25V = 3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>100V = 1</td>
<td>50V = 5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>200V = 2</td>
<td>100V = 1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>500V = 7</td>
<td>200V = 2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.
Contact factory for non-specified capacitance values.
## X7R Dielectric Specifications and Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X7R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55°C to +125°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td><strong>Capacitance</strong></td>
<td>Within specified tolerance</td>
<td></td>
</tr>
<tr>
<td><strong>Dissipation Factor</strong></td>
<td>≤ 10% for ≥ 50V DC ratings; 12.5% for 25V DC rating; ≤ 12.5% for 25V and 16V DC rating</td>
<td>Freq.: 1.0 kHz ± 10%; Voltage: 1.0Vrms ± .2V; For Cap &gt; 10µF, 05Vrm @ 120Hz</td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>100,000MΩ or 1000MΩ - µF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 seconds @ room temp/humidity</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>No breakdown or visual defects</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, with charge and discharge current limited to 50 mA (max). Note: Charge device with 150% of rated voltage for 500V devices.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X7R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Resistance to Flexure Stresses</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects</td>
<td>Deflection: 2mm</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12%</td>
<td>Test Time: 30 seconds</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
<tr>
<td><strong>Solderability</strong></td>
<td></td>
<td>Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects, &lt;25% leaching of either end terminal</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ 7.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X7R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Resistance to Solder Heat</strong></td>
<td></td>
<td>Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td>Step 1: -55°C ± 2°C for 30 ± 3 minutes</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 2: Room Temp for ≤ 3 minutes</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 3: +125°C ± 2°C for 30 ± 3 minutes</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 4: Room Temp for ≤ 3 minutes</td>
</tr>
<tr>
<td><strong>Thermal Shock</strong></td>
<td></td>
<td>Repeat for 5 cycles and measure after 24 ± 2 hours at room temperature</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X7R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Load Life</strong></td>
<td></td>
<td>Charge device with 1.5 rated voltage (≤ 10V) in test chamber set at 125°C ± 2°C for 1000 hours (+48, -0)</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X7R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Load Humidity</strong></td>
<td></td>
<td>Store in a test chamber set at 85°C ± 2°C/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied. Remove from chamber and stabilize at room temperature and humidity for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
## Capacitor Sizing Information

### X7R Dielectric Capacitance Range

#### Preferred Sizes are Shaded

<table>
<thead>
<tr>
<th>Size</th>
<th>0101*</th>
<th>0201</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>0.40 ± 0.02 (0.016 ± 0.008)</td>
<td>0.60 ± 0.03 (0.024 ± 0.001)</td>
<td>1.00 ± 0.10 (0.040 ± 0.004)</td>
<td>1.60 ± 0.15 (0.063 ± 0.006)</td>
<td>2.01 ± 0.20 (0.079 ± 0.008)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>0.20 ± 0.02 (0.008 ± 0.0008)</td>
<td>0.30 ± 0.03 (0.011 ± 0.001)</td>
<td>0.50 ± 0.10 (0.020 ± 0.004)</td>
<td>0.81 ± 0.15 (0.032 ± 0.006)</td>
<td>1.25 ± 0.20 (0.049 ± 0.008)</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
</tr>
<tr>
<td>(t) Terminal (mm)</td>
<td>0.10a ± 0.04 (0.004 ± 0.0016)</td>
<td>0.15 ± 0.05 (0.006 ± 0.002)</td>
<td>0.25 ± 0.16 (0.010 ± 0.006)</td>
<td>0.35 ± 0.15 (0.014 ± 0.006)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
</tr>
<tr>
<td>WVDC (V)</td>
<td>10</td>
<td>6.3</td>
<td>10</td>
<td>16</td>
<td>25</td>
<td>50</td>
</tr>
</tbody>
</table>

### Capacitance (µF) and Tolerances

<table>
<thead>
<tr>
<th>Size</th>
<th>0101*</th>
<th>0201</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cap (µF)</td>
<td>0.010 ± 0.03</td>
<td>0.022 ± 0.06</td>
<td>0.047 ± 0.14</td>
<td>0.094 ± 0.28</td>
<td>0.188 ± 0.56</td>
<td>0.376 ± 1.12</td>
</tr>
<tr>
<td>(µF) 0.015 ± 0.04</td>
<td>0.022 ± 0.06</td>
<td>0.047 ± 0.14</td>
<td>0.094 ± 0.28</td>
<td>0.188 ± 0.56</td>
<td>0.376 ± 1.12</td>
<td></td>
</tr>
</tbody>
</table>

### Soldering Information

- **PAPER**
- **EMBOSS**

**NOTE:** Contact factory for non-specified capacitance values

*EIA 01005

**Contact Factory for Specifications**
## PREFERRED SIZES ARE SHaded

<table>
<thead>
<tr>
<th>SIZE</th>
<th>1210</th>
<th>1812</th>
<th>1825</th>
<th>2220</th>
<th>2225</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Soldering</strong></td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td><strong>Packaging</strong></td>
<td>Paper/Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
</tr>
<tr>
<td>(L) Length (in.)</td>
<td>3.00 ± 0.4 (0.130 ± 0.016)</td>
<td>4.50 ± 0.30 (0.177 ± 0.012)</td>
<td>4.50 ± 0.30 (0.177 ± 0.012)</td>
<td>5.70 ± 0.40 (0.225 ± 0.016)</td>
<td>5.72 ± 0.25 (0.225 ± 0.010)</td>
</tr>
<tr>
<td>(W) Width (in.)</td>
<td>2.50 ± 0.30 (0.088 ± 0.012)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>6.40 ± 0.40 (0.252 ± 0.016)</td>
<td>6.00 ± 0.40 (0.197 ± 0.016)</td>
<td>6.35 ± 0.25 (0.250 ± 0.010)</td>
</tr>
<tr>
<td>(I) Terminal (in.)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
<td>0.61 ± 0.36 (0.024 ± 0.014)</td>
<td>0.61 ± 0.36 (0.024 ± 0.014)</td>
<td>0.64 ± 0.39 (0.025 ± 0.015)</td>
<td>0.64 ± 0.39 (0.025 ± 0.015)</td>
</tr>
</tbody>
</table>

**Cap**

<table>
<thead>
<tr>
<th>WVDC</th>
<th>10</th>
<th>16</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>100</th>
<th>200</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>16</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>25</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>50</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>100</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>200</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>500</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>1000</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
</tbody>
</table>

**Cap (µF)**

<table>
<thead>
<tr>
<th>WVDC</th>
<th>10</th>
<th>16</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>100</th>
<th>200</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>16</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>25</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>50</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>100</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>200</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>500</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>1000</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for non-specified capacitance values.
X7S formulations are called “temperature stable” ceramics and fall into EIA Class II materials. Its temperature variation of capacitance s within ±22% from –55°C to +125°C. This capacitance change is non-linear.

Capacitance for X7S varies under the influence of electrical operating conditions such as voltage and frequency.

X7S dielectric chip usage covers the broad spectrum of industrial applications where known changes in capacitance due to applied voltages are acceptable.

**PART NUMBER (see page 2 for complete part number explanation)**

<table>
<thead>
<tr>
<th>Size (L” x W”)</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (in pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>1206</td>
<td>Z</td>
<td>Z</td>
<td>Z = X7S</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>K = ±10%</td>
<td>A = N/A</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
</tr>
<tr>
<td>4 = 4V</td>
<td>6 = 6.3V</td>
<td>10V</td>
<td>16V</td>
<td>25V</td>
<td>50V</td>
<td>100V</td>
<td>200V</td>
<td></td>
</tr>
</tbody>
</table>

NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers.

**TYPICAL ELECTRICAL CHARACTERISTICS**
## X7S Dielectric Specifications and Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X7S Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55°C to +125°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td></td>
</tr>
<tr>
<td><strong>Dissipation Factor</strong></td>
<td>≤ 5.0% for ≥ 100V DC rating</td>
<td>Freq.: 1.0 kHz ± 10%</td>
</tr>
<tr>
<td></td>
<td>≤ 5.0% for ≥ 25V DC rating</td>
<td>Voltage: 1.0Vrms ± .2V</td>
</tr>
<tr>
<td></td>
<td>≤ 10.0% for ≥ 10V DC rating</td>
<td>For Cap &gt; 10 μF, 0.5Vrms @ 120Hz</td>
</tr>
<tr>
<td></td>
<td>≤ 10.0% for ≤ 10V DC rating</td>
<td></td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>≥ 100,000MΩ or 1000MΩ - μF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>No breakdown or visual defects</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max)</td>
</tr>
<tr>
<td><strong>Resistance to Flexure Stresses</strong></td>
<td>Appearance: No defects</td>
<td>Deflection: 2mm</td>
</tr>
<tr>
<td></td>
<td>Capacitance Variation: ≤ ±12%</td>
<td>Test Time: 30 seconds</td>
</tr>
<tr>
<td></td>
<td>Dissipation Factor: Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Insulation Resistance: ≥ Initial Value x 0.3</td>
<td></td>
</tr>
<tr>
<td><strong>Solderability</strong></td>
<td>≥ 95% of each terminal should be covered with fresh solder</td>
<td>Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td><strong>Resistance to Solder Heat</strong></td>
<td>Appearance: No defects, &lt;25% leaching of either end terminal</td>
<td>Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.</td>
</tr>
<tr>
<td></td>
<td>Capacitance Variation: ≤ ±7.5%</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Dissipation Factor: Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Insulation Resistance: Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Dielectric Strength: Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Thermal Shock</strong></td>
<td>Appearance: No visual defects</td>
<td>Step 1: -55°C ± 2°C</td>
</tr>
<tr>
<td></td>
<td>Capacitance Variation: ≤ ±7.5%</td>
<td>Step 2: Room Temp</td>
</tr>
<tr>
<td></td>
<td>Dissipation Factor: Meets Initial Values (As Above)</td>
<td>Step 3: +125°C ± 2°C</td>
</tr>
<tr>
<td></td>
<td>Insulation Resistance: Meets Initial Values (As Above)</td>
<td>Step 4: Room Temp</td>
</tr>
<tr>
<td></td>
<td>Dielectric Strength: Meets Initial Values (As Above)</td>
<td>Repeat for 5 cycles and measure after 24 ± 2 hours at room temperature</td>
</tr>
<tr>
<td><strong>Load Life</strong></td>
<td>Appearance: No visual defects</td>
<td>Charge device with 1.5 rated voltage (≤ 10V) in test chamber set at 125°C ± 2°C for 1000 hours (+48, -0)</td>
</tr>
<tr>
<td></td>
<td>Capacitance Variation: ≤ ±12.5%</td>
<td>Remove from test chamber and stabilize at room temperature for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td></td>
<td>Dissipation Factor: ≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Insulation Resistance: ≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Dielectric Strength: Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Load Humidity</strong></td>
<td>Appearance: No visual defects</td>
<td>Store in a test chamber set at 85°C ± 2°C/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.</td>
</tr>
<tr>
<td></td>
<td>Capacitance Variation: ≤ ±12.5%</td>
<td>Remove from chamber and stabilize at room temperature and humidity for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td></td>
<td>Dissipation Factor: ≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Insulation Resistance: ≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Dielectric Strength: Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
## X7S Dielectric Capacitance Range

### Preferred Sizes are Shaded

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Soldering</strong></td>
<td>All Paper</td>
<td>All Paper</td>
<td>All Paper</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td><strong>Packaging</strong></td>
<td></td>
<td></td>
<td></td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td>(L) Length (in)</td>
<td>mm</td>
<td>1.00 ± 0.10</td>
<td>1.60 ± 0.15</td>
<td>2.01 ± 0.20</td>
<td>3.20 ± 0.20</td>
</tr>
<tr>
<td>(W) Width (in)</td>
<td>mm</td>
<td>0.50 ± 0.10</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
<td>1.80 ± 0.20</td>
</tr>
<tr>
<td>(T) Terminal (in)</td>
<td>mm</td>
<td>0.25 ± 0.15</td>
<td>0.35 ± 0.15</td>
<td>0.50 ± 0.20</td>
<td>0.50 ± 0.20</td>
</tr>
<tr>
<td>WVDC</td>
<td>6.3</td>
<td>6.3</td>
<td>4</td>
<td>10</td>
<td>50</td>
</tr>
</tbody>
</table>

- **Cap** (pF)
  - 0.010
  - 0.015
  - 0.022
  - 0.068
  - 0.15
  - 0.22
  - 0.33
  - 0.47
  - 0.68
  - 1.0
  - 1.5
  - 2.2
  - 3.3
  - 4.7
  - 10

- **Cap** (μF)
  - 0.001
  - 0.003
  - 0.047
  - 0.050
  - 0.15
  - 0.33
  - 0.47
  - 0.68
  - 1.0
  - 1.5
  - 2.2
  - 3.3
  - 4.7
  - 10

### WVDC

- 6.3
- 6.3
- 4
- 10
- 50
- 100
- 6.3

### SIZE

<table>
<thead>
<tr>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
</tr>
</thead>
<tbody>
<tr>
<td>Letter</td>
<td>A</td>
<td>C</td>
<td>E</td>
<td>G</td>
</tr>
<tr>
<td>Max. Thickness</td>
<td>0.33 (0.013)</td>
<td>0.56 (0.022)</td>
<td>0.71 (0.028)</td>
<td>0.90 (0.035)</td>
</tr>
</tbody>
</table>

*Contact Factory for Specifications*
X5R Dielectric
General Specifications

GENERAL DESCRIPTION
• General Purpose Dielectric for Ceramic Capacitors
• EIA Class II Dielectric
• Temperature variation of capacitance is within ±15% from -55°C to +85°C
• Well suited for decoupling and filtering applications
• Available in High Capacitance values (up to 100µF)

PART NUMBER (see page 2 for complete part number explanation)

<table>
<thead>
<tr>
<th>Size (L'' x W'')</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (in pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0101**</td>
<td>4</td>
<td>D = X5R</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>K = ±10% M = ±20%</td>
<td>A = N/A</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7&quot; Reel</td>
<td>A = Std.</td>
</tr>
<tr>
<td>0201</td>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>4 = 13&quot; Reel</td>
<td></td>
</tr>
<tr>
<td>0402</td>
<td>6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>U = 4mm TR</td>
<td></td>
</tr>
<tr>
<td>0603</td>
<td>6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>405 (01005)</td>
<td></td>
</tr>
<tr>
<td>0805</td>
<td>Z</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1206</td>
<td>Y</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1812</td>
<td>5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**EIA 01005

NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.

TYPICAL ELECTRICAL CHARACTERISTICS

Temperature Coefficient

% Capacitance vs Temperature

Insulation Resistance vs Temperature

Insulation Resistance (Ohm-Farads) vs Temperature
# X5R Dielectric

## Specifications and Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X5R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55°C to +85°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ 2.5% for ≥ 50V DC rating, ≤ 12.5% for 25V, 35V DC rating, ≤ 12.5% Max. for 16V DC rating and lower</td>
<td>Freq.: 1.0 kHz ± 10% Voltage: 1.0Vrms ± .2V For Cap &gt; 10 µF, 0.5Vrms @ 120Hz</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>10,000MΩ or 500MΩ - µF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>No breakdown or visual defects</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max)</td>
</tr>
</tbody>
</table>

### Resistance to Flexure Stresses

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X5R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No defects</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Capacitance</td>
<td>≤ ±12%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
</tbody>
</table>

### Solderability

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X5R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>≥ 95% of each terminal should be covered with fresh solder</td>
<td>Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±5% or ± .5 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

### Resistance to Solder Heat

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X5R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No defects, &lt;25% leaching of either end terminal</td>
<td>Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.</td>
</tr>
<tr>
<td>Capacitance</td>
<td>≤ ±7.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

### Thermal Shock

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X5R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Step 1: -55°C ± 2°C 30 ± 3 minutes</td>
</tr>
<tr>
<td>Capacitance</td>
<td>≤ ±7.5%</td>
<td>Step 2: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 3: +85°C ± 2°C 30 ± 3 minutes</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 4: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td>Repeat for 5 cycles and measure after 24 ± 2 hours at room temperature</td>
</tr>
</tbody>
</table>

### Load Life

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X5R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Charge device with 1.5X rated voltage in test chamber set at 85°C ± 2°C for 1000 hours (+48, -0). Note: Contact factory for *optional specification part numbers that are tested at &lt; 1.5X rated voltage. Remove from test chamber and stabilize at room temperature for 24 ± 2 hours</td>
</tr>
<tr>
<td>Capacitance</td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

### Load Humidity

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X5R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Store in a test chamber set at 85°C ± 2°C/ 85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied. Remove from chamber and stabilize at room temperature and humidity for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Capacitance</td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
## X5R Dielectric

### Capacitance Range

#### Preferred Sizes are Shaded

<table>
<thead>
<tr>
<th>Case Size</th>
<th>0101*</th>
<th>0201</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
</tr>
<tr>
<td>Packaging</td>
<td>Paper/Embossed</td>
<td>All Paper</td>
<td>All Paper</td>
<td>All Paper</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td>(L) Length (in.)</td>
<td>0.40 ± 0.02</td>
<td>0.89 ± 0.09</td>
<td>1.00 ± 0.15</td>
<td>1.60 ± 0.15</td>
<td>2.01 ± 0.20</td>
</tr>
<tr>
<td>(W) Width (in.)</td>
<td>0.20 ± 0.02</td>
<td>0.30 ± 0.09</td>
<td>0.50 ± 0.15</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
</tr>
<tr>
<td>(t) Terminal (in.)</td>
<td>0.10 ± 0.04</td>
<td>0.15 ± 0.05</td>
<td>0.25 ± 0.15</td>
<td>0.35 ± 0.15</td>
<td>0.50 ± 0.25</td>
</tr>
<tr>
<td>Voltage:</td>
<td>6.3 16 4</td>
<td>6.3 16 25</td>
<td>6.3 10 16 25</td>
<td>4 6.3 10 16 25</td>
<td>4 6.3 10 16 25</td>
</tr>
<tr>
<td>Capacitance (pF)</td>
<td>100 101</td>
<td>200 221</td>
<td>300 332</td>
<td>400 472</td>
<td>600 682</td>
</tr>
<tr>
<td></td>
<td>150 151</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>220 221</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>330 331</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>470 471</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>680 681</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1500 151</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2290 222</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>3300 332</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>4700 472</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>6800 682</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Voltage:</td>
<td>6.3 16 4</td>
<td>6.3 16 25</td>
<td>6.3 10 16 25</td>
<td>4 6.3 10 16 25</td>
<td>4 6.3 10 16 25</td>
</tr>
<tr>
<td>Capacitance (µF)</td>
<td>100 101</td>
<td>200 221</td>
<td>300 332</td>
<td>400 472</td>
<td>600 682</td>
</tr>
<tr>
<td></td>
<td>150 151</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>220 221</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>330 331</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>470 471</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>680 681</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1500 151</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2290 222</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>3300 332</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>4700 472</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>6800 682</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Voltage:</td>
<td>6.3 16 4</td>
<td>6.3 16 25</td>
<td>6.3 10 16 25</td>
<td>4 6.3 10 16 25</td>
<td>4 6.3 10 16 25</td>
</tr>
</tbody>
</table>

NOTE: Contact factory for non-specified capacitance values

*EIA 01005

---

**PAPER and EMBOSSED available for 01005**
## X5R Dielectric Capacitance Range

### PREFERRED SIZES ARE SHADED

<table>
<thead>
<tr>
<th>Case Size</th>
<th>1206</th>
<th>1210</th>
<th>1812</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>Packaging</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
<td>All Embossed</td>
</tr>
<tr>
<td>(L) Length</td>
<td>mm</td>
<td>3.20 ± 0.20</td>
<td>3.20 ± 0.20</td>
</tr>
<tr>
<td></td>
<td>(in.)</td>
<td>(0.126 ± 0.008)</td>
<td>(0.126 ± 0.008)</td>
</tr>
<tr>
<td>(W) Width</td>
<td>mm</td>
<td>1.60 ± 0.20</td>
<td>2.50 ± 0.20</td>
</tr>
<tr>
<td></td>
<td>(in.)</td>
<td>(0.063 ± 0.008)</td>
<td>(0.098 ± 0.008)</td>
</tr>
<tr>
<td>(t) Terminal</td>
<td>mm</td>
<td>0.50 ± 0.25</td>
<td>0.50 ± 0.25</td>
</tr>
<tr>
<td></td>
<td>(in.)</td>
<td>(0.020 ± 0.010)</td>
<td>(0.020 ± 0.010)</td>
</tr>
<tr>
<td>Voltage</td>
<td>4</td>
<td>6.3</td>
<td>10</td>
</tr>
<tr>
<td>Cap(pF)</td>
<td>100</td>
<td>101</td>
<td></td>
</tr>
<tr>
<td>150</td>
<td>151</td>
<td></td>
<td></td>
</tr>
<tr>
<td>220</td>
<td>221</td>
<td></td>
<td></td>
</tr>
<tr>
<td>330</td>
<td>331</td>
<td></td>
<td></td>
</tr>
<tr>
<td>470</td>
<td>471</td>
<td></td>
<td></td>
</tr>
<tr>
<td>680</td>
<td>681</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1000</td>
<td>102</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1500</td>
<td>152</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2200</td>
<td>222</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3300</td>
<td>332</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4700</td>
<td>472</td>
<td></td>
<td></td>
</tr>
<tr>
<td>6800</td>
<td>682</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Cap(µF)</td>
<td>0.01</td>
<td>0103</td>
<td></td>
</tr>
<tr>
<td>0.015</td>
<td>150</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.022</td>
<td>223</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.033</td>
<td>333</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.047</td>
<td>473</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.068</td>
<td>689</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.1</td>
<td>104</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.15</td>
<td>154</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.22</td>
<td>224</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.33</td>
<td>334</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.47</td>
<td>474</td>
<td>Q</td>
<td>Q</td>
</tr>
<tr>
<td>0.68</td>
<td>684</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.0</td>
<td>105</td>
<td>Q</td>
<td>Q</td>
</tr>
<tr>
<td>1.5</td>
<td>155</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.2</td>
<td>225</td>
<td>Q</td>
<td>Q</td>
</tr>
<tr>
<td>3.3</td>
<td>335</td>
<td>Q</td>
<td>Q</td>
</tr>
<tr>
<td>4.7</td>
<td>475</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>10</td>
<td>106</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>22</td>
<td>226</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>47</td>
<td>476</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>100</td>
<td>107</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>Voltage</td>
<td>4</td>
<td>6.3</td>
<td>10</td>
</tr>
<tr>
<td>Case Size</td>
<td>1206</td>
<td>1210</td>
<td>1812</td>
</tr>
</tbody>
</table>

NOTE: Contact factory for non-specified capacitance values

*EIA 01005
Y5V Dielectric
General Specifications

Y5V formulations are for general-purpose use in a limited temperature range. They have a wide temperature characteristic of +22% –82% capacitance change over the operating temperature range of –30°C to +85°C. These characteristics make Y5V ideal for decoupling applications within limited temperature range.

PART NUMBER (see page 2 for complete part number explanation)

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric Code</th>
<th>Capacitance Code (in pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>(L x W”)</td>
<td>6.3V = 6</td>
<td>YSV = G</td>
<td>2 Sig. Digits +</td>
<td>Z = +80 –20%</td>
<td>A = Not Applicable</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>A = Std. Product</td>
</tr>
<tr>
<td>3</td>
<td>10V = Z</td>
<td>16V = Y</td>
<td>Number of Zeros</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>104</td>
<td>25V = 3</td>
<td>50V = 5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Temperature Coefficient

Capacitance Change vs. DC Bias Voltage

Insulation Resistance vs. Temperature

Impedance vs. Frequency
# Y5V Dielectric Specifications and Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>Y5V Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating Temperature Range</td>
<td>-30°C to +85°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ 5.0% for ≥ 50V DC rating</td>
<td>Freq.: 1.0 kHz ± 10%</td>
</tr>
<tr>
<td></td>
<td>≤ 7.0% for 25V DC rating</td>
<td>Voltage: 1.0Vrms ± 0.2V</td>
</tr>
<tr>
<td></td>
<td>≤ 9.0% for 16V DC rating</td>
<td>For Cap &gt; 10 μF, 0.5Vrms @ 120Hz</td>
</tr>
<tr>
<td></td>
<td>≤ 12.5% for ≤ 10V DC rating</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>10,000MΩ or 500MΩ - μF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>No breakdown or visual defects</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max)</td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±30%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.1</td>
<td></td>
</tr>
<tr>
<td>Solderability</td>
<td>≥ 95% of each terminal should be covered with fresh solder</td>
<td>Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects, ≤25% leaching of either end terminal</td>
<td>Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±20%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Thermal Shock</td>
<td>No visual defects</td>
<td>Step 1: -30°C ± 2°C, 30 ± 3 minutes</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Step 2: Room Temp ± 3°C, ≤ 3 minutes</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±20%</td>
<td>Step 3: +85°C ± 2°C, 30 ± 3 minutes</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 4: Room Temp ± 3°C, ≤ 3 minutes</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td>Repeat for 5 cycles and measure after 24 ±2 hours at room temperature</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td>Charge device with twice rated voltage in test chamber set at 85°C ± 2°C for 1000 hours (+48, -0)</td>
</tr>
<tr>
<td>Load Life</td>
<td>No visual defects</td>
<td>Remove from test chamber and stabilize at room temperature for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Store in a test chamber set at 85°C ± 2°C / 85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±30%</td>
<td>Remove from chamber and stabilize at room temperature and humidity for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 1.5 (See above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.1 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
### Y5V Dielectric Capacitance Range

**PREFERRED SIZES ARE SHADEd**

<table>
<thead>
<tr>
<th>Size</th>
<th>0201</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Soldering</strong></td>
<td>Relflow Only</td>
<td>Relflow/Wave</td>
<td>Relflow/Wave</td>
<td>Relflow/Wave</td>
<td>Relflow/Wave</td>
<td>Relflow/Wave</td>
</tr>
<tr>
<td><strong>Packaging</strong></td>
<td>All Paper</td>
<td>All Paper</td>
<td>All Paper</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td><strong>(L) Length</strong> (mm (in.))</td>
<td>0.60 ± 0.09 (0.024 ± 0.004)</td>
<td>1.00 ± 0.10 (0.040 ± 0.004)</td>
<td>1.60 ± 0.15 (0.063 ± 0.006)</td>
<td>2.01 ± 0.20 (0.079 ± 0.008)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
</tr>
<tr>
<td><strong>(W) Width</strong> (mm (in.))</td>
<td>0.30 ± 0.10 (0.011 ± 0.004)</td>
<td>0.50 ± 0.10 (0.020 ± 0.004)</td>
<td>0.81 ± 0.15 (0.032 ± 0.006)</td>
<td>1.25 ± 0.20 (0.049 ± 0.008)</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
<td>2.50 ± 0.20 (0.098 ± 0.008)</td>
</tr>
<tr>
<td><strong>(t) Terminal</strong> (mm (in.))</td>
<td>0.15 ± 0.05 (0.006 ± 0.002)</td>
<td>0.25 ± 0.15 (0.010 ± 0.008)</td>
<td>0.35 ± 0.15 (0.014 ± 0.006)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
</tr>
<tr>
<td><strong>WVDC</strong></td>
<td>6.3</td>
<td>10</td>
<td>6</td>
<td>10</td>
<td>16</td>
<td>25</td>
</tr>
<tr>
<td><strong>Cap (pF)</strong></td>
<td>1000</td>
<td>2200</td>
<td>1000</td>
<td>2200</td>
<td>1000</td>
<td>2200</td>
</tr>
<tr>
<td><strong>Cap (µF)</strong></td>
<td>0.010</td>
<td>0.022</td>
<td>0.010</td>
<td>0.022</td>
<td>0.010</td>
<td>0.022</td>
</tr>
<tr>
<td><strong>Max. Thickness</strong> (mm)</td>
<td>0.33</td>
<td>0.47</td>
<td>0.56</td>
<td>0.56</td>
<td>0.66</td>
<td>0.66</td>
</tr>
</tbody>
</table>

- **Max. Thickness** (mm)
  - 0.33
  - 0.47
  - 0.56
  - 0.56
  - 0.66
  - 0.66

- **PAPER**
- **EMBOSSED**
AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of Gold. This termination is indicated by the use of a “7” or “G” in the 12th position of the AVX Catalog Part Number. This fulfills AVX’s commitment to providing a full range of products to our customers. Please contact the factory if you require additional information on our MLCC Gold Termination.

**PART NUMBER**

<table>
<thead>
<tr>
<th>AU03</th>
<th>Y</th>
<th>C</th>
<th>104</th>
<th>K</th>
<th>A</th>
<th>7</th>
<th>2</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>Voltage</td>
<td>Dielectric</td>
<td>Capacitance Code (In pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Terminations</td>
<td>Packaging</td>
<td>Special Code</td>
</tr>
<tr>
<td>AU01 - 0201</td>
<td>6,3V = 6</td>
<td>C0G (NP0) = A</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>B = ±.10 pF (&lt;10pF)</td>
<td>A = Not Applicable</td>
<td>G* = 1.9 μ* to 7.87 μ*</td>
<td>2 = 7&quot; Reel</td>
<td>A = Std. Product</td>
</tr>
<tr>
<td>AU02 - 0402</td>
<td>10V = Z</td>
<td>X7R = C</td>
<td></td>
<td>C = ±.25 pF (&lt;10pF)</td>
<td></td>
<td>7 = 100 μ* minimum</td>
<td>4 = 13&quot; Reel</td>
<td></td>
</tr>
<tr>
<td>AU03 - 0603</td>
<td>16V = Y</td>
<td>X5R = D</td>
<td></td>
<td>D = ±.50 pF (&lt;10pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AU05 - 0805</td>
<td>25V = 3</td>
<td></td>
<td></td>
<td>F = ±1% (≥ 10 pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AU06 - 1206</td>
<td>35V = D</td>
<td></td>
<td></td>
<td>G = ±2% (≥ 10 pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AU10 - 1210</td>
<td>50V = 5</td>
<td></td>
<td></td>
<td>J = ±5%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AU12 - 1812</td>
<td>100V = 1</td>
<td></td>
<td></td>
<td>K = ±10%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AU13 - 1825</td>
<td>200V = 2</td>
<td></td>
<td></td>
<td>M = ±20%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AU14 - 2225</td>
<td>500V = 7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AU16 - 0306</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AU17 - 0508</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AU18 - 0612</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

* Contact factory for availability.
## MLCC Gold Termination – AU Series

**Capacitance Range (NP0 Dielectric)**

### PREFERRED SIZES ARE SHADDED

<table>
<thead>
<tr>
<th>SIZE</th>
<th>AU01</th>
<th>AU02</th>
<th>AU03</th>
<th>AU05</th>
<th>AU06</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Epoxy/</td>
<td>Reflow/Epoxy/</td>
<td>Reflow/Epoxy/</td>
<td>Reflow/Epoxy/</td>
<td>Reflow/Epoxy/</td>
</tr>
<tr>
<td>Packaging</td>
<td>All Paper</td>
<td>All Paper</td>
<td>All Paper</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
</tbody>
</table>

(W) Width (in.)

<table>
<thead>
<tr>
<th>(L) Length (mm)</th>
<th>(W) Width (mm)</th>
<th>(t) Terminal (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.60 ± 0.09 (0.024 ± 0.004)</td>
<td>2.01 ± 0.20 (0.079 ± 0.008)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
</tr>
<tr>
<td>1.00 ± 0.10 (0.040 ± 0.004)</td>
<td>0.81 ± 0.15 (0.032 ± 0.006)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
</tr>
<tr>
<td>1.60 ± 0.15 (0.063 ± 0.006)</td>
<td>1.25 ± 0.20 (0.050 ± 0.010)</td>
<td>(0.063 ± 0.008)</td>
</tr>
<tr>
<td>2.01 ± 0.20 (0.079 ± 0.008)</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
<td>(0.063 ± 0.008)</td>
</tr>
</tbody>
</table>

**Letter**

<table>
<thead>
<tr>
<th>Max. Thickness</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
</tr>
<tr>
<td>0.33 (0.013)</td>
</tr>
</tbody>
</table>

**Contact Factory**

* Contact Factory
## MLCC Gold Termination – AU Series
### Capacitance Range (NP0 Dielectric)

### Preferred Sizes Are Shaded

<table>
<thead>
<tr>
<th>Size</th>
<th>AU10</th>
<th>AU12</th>
<th>AU13</th>
<th>AU14</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Soldering</strong></td>
<td>Rework/Epoxi/Wire Bond*</td>
<td>Rework/Epoxi/Wire Bond*</td>
<td>Rework/Epoxi/Wire Bond*</td>
<td>Rework/Epoxi/Wire Bond*</td>
</tr>
<tr>
<td><strong>Packaging</strong></td>
<td>Paper/Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
</tr>
<tr>
<td>(L) Length (mm) (in.)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>4.50 ± 0.30 (0.177 ± 0.012)</td>
<td>4.50 ± 0.30 (0.177 ± 0.012)</td>
<td>5.72 ± 0.25 (0.225 ± 0.010)</td>
</tr>
<tr>
<td>(W) Width (mm) (in.)</td>
<td>2.50 ± 0.20 (0.098 ± 0.008)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>6.40 ± 0.40 (0.252 ± 0.016)</td>
<td>6.35 ± 0.25 (0.250 ± 0.010)</td>
</tr>
<tr>
<td>(T) Terminal (mm) (in.)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
<td>0.61 ± 0.36 (0.024 ± 0.014)</td>
<td>0.61 ± 0.36 (0.024 ± 0.014)</td>
<td>0.64 ± 0.39 (0.025 ± 0.015)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>WVDC</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cap (pF)</td>
<td>0.5</td>
<td>1.0</td>
<td>1.2</td>
<td>1.5</td>
<td></td>
<td>1.0</td>
<td>1.2</td>
<td>1.5</td>
<td></td>
<td></td>
<td>1.0</td>
<td>1.2</td>
<td>1.5</td>
<td></td>
</tr>
<tr>
<td>1.8</td>
<td></td>
<td>2.2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.7</td>
<td></td>
<td></td>
<td>3.3</td>
<td>3.6</td>
<td>4.7</td>
<td>5.6</td>
<td>6.8</td>
<td>8.2</td>
<td></td>
<td></td>
<td>10</td>
<td>12</td>
<td>15</td>
<td></td>
</tr>
<tr>
<td>18</td>
<td>22</td>
<td>27</td>
<td>33</td>
<td>39</td>
<td>47</td>
<td>56</td>
<td>68</td>
<td>82</td>
<td>100</td>
<td>120</td>
<td>150</td>
<td>180</td>
<td>220</td>
<td>270</td>
</tr>
<tr>
<td>1000</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>1200</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>1500</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>1800</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>2200</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>Q</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>P</td>
<td>K</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>2700</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>Q</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>P</td>
<td>Q</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>3300</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>P</td>
<td>Q</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>3900</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>P</td>
<td>Q</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>4700</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>P</td>
<td>Q</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>5600</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>K</td>
<td>K</td>
<td>M</td>
<td>X</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>6800</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>K</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>8200</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.010</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>K</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.012</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>K</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.015</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>K</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.018</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.022</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.027</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.033</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.039</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.047</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.068</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>0.082</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
<td>M</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>WVDC</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
</tr>
</thead>
<tbody>
<tr>
<td><em>Contact Factor</em></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Letter

<table>
<thead>
<tr>
<th>Max. Thickness</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.33 (0.013)</td>
<td>0.56 (0.022)</td>
<td>0.71 (0.029)</td>
<td>0.90 (0.035)</td>
<td>0.94 (0.037)</td>
<td>1.02 (0.040)</td>
<td>1.27 (0.050)</td>
<td>1.40 (0.055)</td>
<td>1.52 (0.060)</td>
<td>1.78 (0.070)</td>
<td>2.29 (0.090)</td>
<td>2.54 (0.100)</td>
<td>2.79 (0.110)</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Paper</th>
<th>Embossed</th>
</tr>
</thead>
<tbody>
<tr>
<td>041019</td>
<td></td>
</tr>
</tbody>
</table>
### MLCC Gold Termination - AU Series

#### Capacitance Range (X7R Dielectric)

**PREFERRED SIZES ARE SHADED**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>AU02</th>
<th>AU03</th>
<th>AU05</th>
<th>AU06</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Soldering</strong></td>
<td>Reflow/Epoxy Wire Bond*</td>
<td>Reflow/Epoxy Wire Bond*</td>
<td>Reflow/Epoxy Wire Bond*</td>
<td>Reflow/Epoxy Wire Bond*</td>
</tr>
<tr>
<td><strong>Packaging</strong></td>
<td>All Paper</td>
<td>All Paper</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>1.00 ± 0.10 (0.040 ± 0.004)</td>
<td>1.60 ± 0.15 (0.063 ± 0.006)</td>
<td>2.01 ± 0.20 (0.079 ± 0.008)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>0.50 ± 0.10 (0.020 ± 0.004)</td>
<td>0.81 ± 0.15 (0.032 ± 0.006)</td>
<td>1.25 ± 0.20 (0.049 ± 0.008)</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
</tr>
<tr>
<td>(t) Terminal (mm)</td>
<td>0.25 ± 0.15 (0.010 ± 0.006)</td>
<td>0.35 ± 0.15 (0.014 ± 0.006)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
</tr>
<tr>
<td><strong>WVDC</strong></td>
<td>10</td>
<td>16</td>
<td>25</td>
<td>50</td>
</tr>
<tr>
<td>Cap (pF)</td>
<td>100</td>
<td>150</td>
<td>220</td>
<td></td>
</tr>
<tr>
<td>0.010</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.015</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.022</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.033</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.047</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.068</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.10</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.15</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.22</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.33</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.47</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.68</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.5</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.2</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.7</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>10</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>22</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>47</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>100</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>SIZE</strong></td>
<td>AU02</td>
<td>AU03</td>
<td>AU05</td>
<td>AU06</td>
</tr>
<tr>
<td><strong>WVDC</strong></td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>6.3</td>
</tr>
<tr>
<td>10</td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>100</td>
</tr>
<tr>
<td>200</td>
<td>6.3</td>
<td>10</td>
<td>16</td>
<td>25</td>
</tr>
<tr>
<td>50</td>
<td>100</td>
<td>200</td>
<td>6.3</td>
<td>10</td>
</tr>
<tr>
<td>16</td>
<td>25</td>
<td>50</td>
<td>100</td>
<td></td>
</tr>
<tr>
<td><strong>Max. Thickness</strong></td>
<td>0.33</td>
<td>0.56</td>
<td>0.71</td>
<td>0.90</td>
</tr>
<tr>
<td>PAPER</td>
<td>EMBOSSED</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

* Contact Factory
# MLCC Gold Termination - AU Series
## Capacitance Range (X7R Dielectric)

**PREFERRED SIZES ARE SHAPED**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>AU10</th>
<th>AU12</th>
<th>AU13</th>
<th>AU14</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Epoxy/Wire Bond*</td>
<td>Reflow/Epoxy/Wire Bond*</td>
<td>Reflow/Epoxy/Wire Bond*</td>
<td>Reflow/Epoxy/Wire Bond*</td>
</tr>
<tr>
<td>Packaging</td>
<td>Paper/Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
</tr>
<tr>
<td>(L) Length mm (in.)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>4.50 ± 0.30 (0.177 ± 0.012)</td>
<td>4.50 ± 0.30 (0.177 ± 0.012)</td>
<td>5.72 ± 0.25 (0.225 ± 0.010)</td>
</tr>
<tr>
<td>(W) Width mm (in.)</td>
<td>2.50 ± 0.20 (0.098 ± 0.008)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>6.40 ± 0.40 (0.252 ± 0.016)</td>
<td>6.35 ± 0.25 (0.250 ± 0.010)</td>
</tr>
<tr>
<td>(t) Terminal mm (in.)</td>
<td>0.50 ± 0.25 (0.020 ± 0.010)</td>
<td>0.61 ± 0.36 (0.024 ± 0.014)</td>
<td>0.61 ± 0.36 (0.024 ± 0.014)</td>
<td>0.64 ± 0.39 (0.025 ± 0.015)</td>
</tr>
</tbody>
</table>

**WVDC**

<table>
<thead>
<tr>
<th>Cap (pF)</th>
<th>10</th>
<th>16</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>50</th>
<th>100</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.010 J J J J M</td>
<td>K K K K M</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.015 J J J J P</td>
<td>K K K K P</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.022 J J J J Q</td>
<td>K K K K X</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.033 J J J J J</td>
<td>K K K K M</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.047 J J J J J</td>
<td>K K K K Z</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.068 J J J J J</td>
<td>K K K K Z</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.10 J J J J J</td>
<td>K K K K Z</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.15 J J J J M</td>
<td>K K K K Z</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.22 J J J J P</td>
<td>K K K K M</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.33 J J J J Q</td>
<td>K M X</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.47 M M M M Q</td>
<td>K P</td>
<td>M M M</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.68 M M M P X</td>
<td>M Q</td>
<td>M P</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.0 N N N X</td>
<td>M X</td>
<td>M P</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1.5 N N N Z</td>
<td>Z X</td>
<td>M X</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.2 X X Z Z</td>
<td>Z Z</td>
<td>M X</td>
<td>P</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.3 X X Z Z</td>
<td>Z Z</td>
<td>Z M</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.7 X X Z Z</td>
<td>Z Z</td>
<td>Z M</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>10 Z Z Z</td>
<td>Z Z</td>
<td>Z M</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>WVDC</th>
<th>10</th>
<th>16</th>
<th>25</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>50</th>
<th>100</th>
<th>200</th>
<th>500</th>
<th>50</th>
<th>100</th>
</tr>
</thead>
</table>

* Contact Factory

<table>
<thead>
<tr>
<th>SIZE</th>
<th>AU10</th>
<th>AU12</th>
<th>AU13</th>
<th>AU14</th>
</tr>
</thead>
<tbody>
<tr>
<td>Letter</td>
<td>A</td>
<td>C</td>
<td>E</td>
<td>G</td>
</tr>
<tr>
<td>Max. Thickness</td>
<td>0.33 (0.013)</td>
<td>0.56 (0.022)</td>
<td>0.71 (0.028)</td>
<td>0.90 (0.035)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Paper</th>
<th>Embossed</th>
</tr>
</thead>
<tbody>
<tr>
<td>PAPER</td>
<td>EMBOSSED</td>
</tr>
</tbody>
</table>
**PREFERRED SIZES ARE SHADED**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>AU01</th>
<th>AU02</th>
<th>AU03</th>
<th>AU05</th>
<th>AU06</th>
<th>AU10</th>
<th>AU12</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.010</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td>0.015</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td>0.022</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td>0.033</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td>0.044</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td>0.10</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
</tr>
<tr>
<td>0.15</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
</tr>
<tr>
<td>0.22</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
</tr>
<tr>
<td>0.33</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
</tr>
<tr>
<td>0.47</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
</tr>
<tr>
<td>0.68</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
<td>C</td>
</tr>
<tr>
<td>1.0</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td>1.5</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td>2.2</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td>3.3</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>4.7</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>10</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
<td>K</td>
</tr>
<tr>
<td>22</td>
<td>P</td>
<td>P</td>
<td>P</td>
<td>P</td>
<td>P</td>
<td>P</td>
<td>P</td>
</tr>
<tr>
<td>47</td>
<td>Q</td>
<td>Q</td>
<td>Q</td>
<td>Q</td>
<td>Q</td>
<td>Q</td>
<td>Q</td>
</tr>
<tr>
<td>100</td>
<td>Z</td>
<td>Z</td>
<td>Z</td>
<td>Z</td>
<td>Z</td>
<td>Z</td>
<td>Z</td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for non-specified capacitance values

---

**CAPACITANCE RANGE (X5R DIELECTRIC)**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>AU01</th>
<th>AU02</th>
<th>AU03</th>
<th>AU05</th>
<th>AU06</th>
<th>AU10</th>
<th>AU12</th>
</tr>
</thead>
<tbody>
<tr>
<td>WVDC</td>
<td>6.3</td>
<td>10</td>
<td>16</td>
<td>25</td>
<td>4</td>
<td>6.3</td>
<td>10</td>
</tr>
</tbody>
</table>

---

**LETTER THICKNESS**

| Letter | A | B | C | D | E | F | G | H | I | J | K | L | M | N | O | P | Q | R | S | T | U | V | W | X | Y | Z |
| Max.  | 0.33 | 0.56 | 0.71 | 0.90 | 0.94 | 1.02 | 1.27 | 1.40 | 1.52 | 1.78 | 2.29 | 2.54 | 2.79 |
| Thickness | (0.013) | (0.022) | (0.028) | (0.035) | (0.037) | (0.040) | (0.050) | (0.055) | (0.060) | (0.070) | (0.090) | (0.100) | (0.110) |

**Embossed (PAPER)**

---

**OPTIONS**

- Reflow/Epoxy Wire Bond
- Reflow/Epoxy Wire Bond* (Contact Factory)
- Soldering
- Reflow/Epoxy
- Wire Bond
- Reflow/Epoxy
- Wire Bond*
- Wire Bond
- Reflow/Epoxy/ Wire Bond*
- Reflow/Epoxy/ Wire Bond* (Contact Factory)

---

**NOTE:** Contact factory for non-specified capacitance values

---

**= *Optional Specifications – Contact Factory**
MLCC Gold Termination – AU Series

**AU16/AU17/AU18**

### PHYSICAL DIMENSIONS AND PAD LAYOUT

**SIZE**

<table>
<thead>
<tr>
<th>Size</th>
<th>AU16 (0306)</th>
<th>AU17 (0508)</th>
<th>AU18 (0612)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>mm (in.)</td>
<td>mm (in.)</td>
<td>mm (in.)</td>
</tr>
<tr>
<td>Length</td>
<td>0.81 ± 0.15 (0.032 ± 0.006)</td>
<td>1.27 ± 0.25 (0.050 ± 0.010)</td>
<td>1.60 ± 0.25 (0.063 ± 0.010)</td>
</tr>
<tr>
<td>Width</td>
<td>1.60 ± 0.15 (0.063 ± 0.008)</td>
<td>2.00 ± 0.25 (0.080 ± 0.010)</td>
<td>3.20 ± 0.25 (0.126 ± 0.010)</td>
</tr>
</tbody>
</table>

**Packaging**

<table>
<thead>
<tr>
<th>Cap Code</th>
<th>AU16/AU17/AU18</th>
</tr>
</thead>
<tbody>
<tr>
<td>WVDC</td>
<td>AU16/AU17/AU18</td>
</tr>
<tr>
<td>102</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>222</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>332</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>472</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>662</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>103</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>153</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>223</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>333</td>
<td>A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>473</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>663</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>104</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>154</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>224</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>334</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>474</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>664</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>105</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>155</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>225</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>335</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>475</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
<tr>
<td>685</td>
<td>A A A S S S V S S S V S S S V</td>
</tr>
</tbody>
</table>

**Code Thickness**

<table>
<thead>
<tr>
<th>Code</th>
<th>AU16 (0306)</th>
<th>AU17 (0508)</th>
<th>AU18 (0612)</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>0.66 (0.026)</td>
<td>0.66 (0.026)</td>
<td>0.66 (0.026)</td>
</tr>
<tr>
<td>V</td>
<td>0.76 (0.030)</td>
<td>0.76 (0.030)</td>
<td>0.76 (0.030)</td>
</tr>
<tr>
<td>W</td>
<td>1.02 (0.040)</td>
<td>1.02 (0.040)</td>
<td>1.02 (0.040)</td>
</tr>
</tbody>
</table>

**PHYSICAL DIMENSIONS**

<table>
<thead>
<tr>
<th>Dimension</th>
<th>AU16 (0306)</th>
<th>AU17 (0508)</th>
<th>AU18 (0612)</th>
</tr>
</thead>
<tbody>
<tr>
<td>L</td>
<td>0.81 ± 0.15 (0.032 ± 0.006)</td>
<td>1.27 ± 0.25 (0.050 ± 0.010)</td>
<td>1.60 ± 0.25 (0.063 ± 0.010)</td>
</tr>
<tr>
<td>W</td>
<td>1.60 ± 0.15 (0.063 ± 0.008)</td>
<td>2.00 ± 0.25 (0.080 ± 0.010)</td>
<td>3.20 ± 0.25 (0.126 ± 0.010)</td>
</tr>
</tbody>
</table>

**PAD LAYOUT DIMENSIONS**

<table>
<thead>
<tr>
<th>Dimension</th>
<th>A</th>
<th>B</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>AU16 (0306)</td>
<td>0.31 (0.012)</td>
<td>1.52 (0.060)</td>
<td>0.51 (0.020)</td>
</tr>
<tr>
<td>AU17 (0508)</td>
<td>0.51 (0.020)</td>
<td>2.03 (0.080)</td>
<td>0.51 (0.020)</td>
</tr>
<tr>
<td>AU18 (0612)</td>
<td>0.76 (0.030)</td>
<td>3.05 (0.120)</td>
<td>0.635 (0.025)</td>
</tr>
</tbody>
</table>
MLCC Tin/Lead Termination “B”
C0G (NP0) – General Specifications

AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a “B” in the 12th position of the AVX Catalog Part Number. This fulfills AVX’s commitment to providing a full range of products to our customers. AVX has provided in the following pages a full range of values that we are currently offering in this special “B” termination. Please contact the factory if you require additional information on our MLCC Tin/Lead Termination “B” products.

**Not RoHS Compliant**

PART NUMBER (see page 2 for complete part number explanation)

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (In pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
<th>Contact Factory For Multiples</th>
</tr>
</thead>
<tbody>
<tr>
<td>LD05</td>
<td>5</td>
<td>A</td>
<td>101</td>
<td>J</td>
<td>A</td>
<td>B</td>
<td>2</td>
<td>A</td>
<td></td>
</tr>
<tr>
<td>LD02</td>
<td>02</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD03</td>
<td>0603</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD04</td>
<td>0604*</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD05</td>
<td>0805</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD06</td>
<td>1206</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD10</td>
<td>1210</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD12</td>
<td>1812</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD13</td>
<td>1825</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD14</td>
<td>2225</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD20</td>
<td>2220</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*LD04 has the same CV ranges as LD03.

NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers.
Contact factory for non-specified capacitance values.

See FLEXITERM® section for CV options.

*“X7R only

Contact Factory For Multiples

~AVX~
# MLCC Tin/Lead Termination “B”

## C0G (NP0) – Specifications and Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>NP0 Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Q</td>
<td>&lt;30 pF: Q ≥ 400 + 20 x Cap Value  ≥30 pF: Q ≥ 1000</td>
<td>Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF 1 kHz ± 10% for cap &gt; 1000 pF Voltage: 1.0Vrms ± .2V</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>100,000MΩ or 1000MΩ - µF, whichever is less</td>
<td>Charge device with rated voltage for 60 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>No breakdown or visual defects</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max)  Note: Charge device with 150% of rated voltage for 500V devices.</td>
</tr>
</tbody>
</table>

### Resistance to Flexure Stresses
<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Variation</td>
<td>±5% or ±.5 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
</tbody>
</table>

### Solderability
<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No defects, &lt;25% leaching of either end terminal</td>
<td>Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±2.5% or ±.25 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

### Resistance to Solder Heat
<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Step 1: -55°C ± 2°C  30 ± 3 minutes</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±2.5% or ±.25 pF, whichever is greater</td>
<td>Step 2: Room Temp  ≤ 3 minutes</td>
</tr>
<tr>
<td>Q</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 3: +125°C ± 2°C  30 ± 3 minutes</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 4: Room Temp  ≤ 3 minutes</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td>Repeat for 5 cycles and measure after 24 hours at room temperature.</td>
</tr>
</tbody>
</table>

### Thermal Shock
<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Charge device with twice rated voltage in test chamber set at 125°C ± 2°C for 1000 hours (+48, -0).  Remove from test chamber and stabilize at room temperature for 24 hours before measuring.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±3.0% or ±.3 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>≥ 30 pF: Q ≥ 350  ≥10 pF, &lt;30 pF: Q ≥ 275 +5C/2  &lt;10 pF: Q ≥ 200 +10C</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

### Load Life
<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>EDT + 2°C, 1000 hours 40% RH, 100% RH (at 85°C ± 2°C/85% ± 5% relative humidity)</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±5.0% or ±.5 pF, whichever is greater</td>
<td>Store in a test chamber set at 85°C ± 2°C/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.  Remove from chamber and stabilize at room temperature for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Q</td>
<td>≥ 30 pF: Q ≥ 350  ≥10 pF, &lt;30 pF: Q ≥ 275 +5C/2  &lt;10 pF: Q ≥ 200 +10C</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

### Load Humidity
<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>EDT + 2°C, 1000 hours 40% RH, 100% RH (at 85°C ± 2°C/85% ± 5% relative humidity)</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±5.0% or ±.5 pF, whichever is greater</td>
<td>Store in a test chamber set at 85°C ± 2°C/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.  Remove from chamber and stabilize at room temperature for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Q</td>
<td>≥ 30 pF: Q ≥ 350  ≥10 pF, &lt;30 pF: Q ≥ 275 +5C/2  &lt;10 pF: Q ≥ 200 +10C</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
# MLCC Tin/Lead Termination “B”
## Capacitance Range (NP0 Dielectric)

### Preferred Sizes are Shaded

<table>
<thead>
<tr>
<th>Size</th>
<th>LD02</th>
<th>LD03</th>
<th>LD05</th>
<th>LD06</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Soldering</strong></td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
</tr>
<tr>
<td><strong>Packaging</strong></td>
<td>All Paper</td>
<td>All Paper</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td>(L) Length</td>
<td>(mm)</td>
<td>(in.)</td>
<td>(mm)</td>
<td>(in.)</td>
</tr>
<tr>
<td>Max.</td>
<td>0.33</td>
<td>(0.013)</td>
<td>0.56</td>
<td>(0.022)</td>
</tr>
<tr>
<td>(W) Width</td>
<td>(mm)</td>
<td>(in.)</td>
<td>(mm)</td>
<td>(in.)</td>
</tr>
<tr>
<td>Min.</td>
<td>0.33</td>
<td>(0.013)</td>
<td>0.56</td>
<td>(0.022)</td>
</tr>
<tr>
<td>(l) Terminal</td>
<td>(mm)</td>
<td>(in.)</td>
<td>(mm)</td>
<td>(in.)</td>
</tr>
<tr>
<td>Max.</td>
<td>0.25</td>
<td>(0.010)</td>
<td>0.35</td>
<td>(0.014)</td>
</tr>
<tr>
<td>Min.</td>
<td>0.25</td>
<td>(0.010)</td>
<td>0.35</td>
<td>(0.014)</td>
</tr>
<tr>
<td><strong>VWDC</strong></td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>100</td>
</tr>
<tr>
<td>Max</td>
<td>16</td>
<td>(0.63)</td>
<td>25</td>
<td>(1.00)</td>
</tr>
<tr>
<td>Min.</td>
<td>16</td>
<td>(0.63)</td>
<td>25</td>
<td>(1.00)</td>
</tr>
</tbody>
</table>

### Lettering

<table>
<thead>
<tr>
<th>Letter</th>
<th>LD02</th>
<th>LD03</th>
<th>LD05</th>
<th>LD06</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Max. Thickness</strong></td>
<td>0.013</td>
<td>0.019</td>
<td>0.028</td>
<td>0.035</td>
</tr>
<tr>
<td><strong>PAPER</strong></td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td><strong>EMBOSS</strong></td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
# MLCC Tin/Lead Termination “B”

## Capacitance Range (NP0 Dielectric)

### Preferred Sizes are Shaded

<table>
<thead>
<tr>
<th>Size</th>
<th>LD10</th>
<th>LD12</th>
<th>LD13</th>
<th>LD14</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>Packaging</td>
<td>Paper/Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>3.20 ± 0.20</td>
<td>4.50 ± 0.30</td>
<td>4.50 ± 0.30</td>
<td>5.70 ± 0.20</td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>2.00 ± 0.20</td>
<td>2.00 ± 0.20</td>
<td>2.00 ± 0.20</td>
<td>2.00 ± 0.20</td>
</tr>
<tr>
<td>(t) Terminal (mm)</td>
<td>0.50 ± 0.25</td>
<td>0.61 ± 0.35</td>
<td>0.61 ± 0.35</td>
<td>0.64 ± 0.39</td>
</tr>
</tbody>
</table>

### Capacitance Range

<table>
<thead>
<tr>
<th>Size</th>
<th>LD10</th>
<th>LD12</th>
<th>LD13</th>
<th>LD14</th>
</tr>
</thead>
<tbody>
<tr>
<td>WDC 25</td>
<td>0.50</td>
<td>1.00</td>
<td>1.20</td>
<td>1.50</td>
</tr>
<tr>
<td>WDC 50</td>
<td>1.80</td>
<td>2.20</td>
<td>2.70</td>
<td>3.30</td>
</tr>
<tr>
<td>WDC 100</td>
<td>5.60</td>
<td>6.60</td>
<td>8.20</td>
<td>10.00</td>
</tr>
<tr>
<td>WDC 200</td>
<td>10.0</td>
<td>12.0</td>
<td>15.0</td>
<td>18.0</td>
</tr>
<tr>
<td>WDC 500</td>
<td>25.0</td>
<td>30.0</td>
<td>39.0</td>
<td>47.0</td>
</tr>
<tr>
<td>WDC 1000</td>
<td>56.0</td>
<td>69.0</td>
<td>82.0</td>
<td>100.0</td>
</tr>
</tbody>
</table>

### Max. Thickness

<table>
<thead>
<tr>
<th>Letter</th>
<th>LD10</th>
<th>LD12</th>
<th>LD13</th>
<th>LD14</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>0.33</td>
<td>0.36</td>
<td>0.39</td>
<td>0.47</td>
</tr>
<tr>
<td>B</td>
<td>0.36</td>
<td>0.39</td>
<td>0.47</td>
<td>0.56</td>
</tr>
<tr>
<td>C</td>
<td>0.39</td>
<td>0.47</td>
<td>0.56</td>
<td>0.68</td>
</tr>
<tr>
<td>E</td>
<td>0.56</td>
<td>0.68</td>
<td>0.82</td>
<td>1.00</td>
</tr>
<tr>
<td>G</td>
<td>0.82</td>
<td>1.00</td>
<td>1.20</td>
<td>1.50</td>
</tr>
<tr>
<td>Q</td>
<td>2.20</td>
<td>2.70</td>
<td>3.30</td>
<td>4.70</td>
</tr>
<tr>
<td>N</td>
<td>1.00</td>
<td>1.20</td>
<td>1.50</td>
<td>1.80</td>
</tr>
<tr>
<td>M</td>
<td>0.50</td>
<td>0.60</td>
<td>0.70</td>
<td>0.90</td>
</tr>
</tbody>
</table>

### Max. Thickness (μm)

<table>
<thead>
<tr>
<th>Letter</th>
<th>LD10</th>
<th>LD12</th>
<th>LD13</th>
<th>LD14</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>0.013</td>
<td>0.022</td>
<td>0.038</td>
<td>0.063</td>
</tr>
<tr>
<td>B</td>
<td>0.022</td>
<td>0.038</td>
<td>0.063</td>
<td>0.100</td>
</tr>
<tr>
<td>C</td>
<td>0.038</td>
<td>0.063</td>
<td>0.100</td>
<td>0.140</td>
</tr>
<tr>
<td>Q</td>
<td>0.140</td>
<td>0.220</td>
<td>0.310</td>
<td>0.470</td>
</tr>
</tbody>
</table>

### Tape Packaging

- **Paper/Embossed**
- **All Embossed**

### Diagram

![Diagram of MLCC Tin/Lead Termination “B”](image-url)
AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a “B” in the 12th position of the AVX Catalog Part Number. This fulfills AVX’s commitment to providing a full range of products to our customers. AVX has provided in the following pages a full range of values that we are currently offering in this special “B” termination. Please contact the factory if you require additional information on our MLCC Tin/Lead Termination “B” products.

### PART NUMBER (see page 2 for complete part number explanation)

<table>
<thead>
<tr>
<th>LD05</th>
<th>5</th>
<th>F</th>
<th>101</th>
<th>J</th>
<th>A</th>
<th>B</th>
<th>2</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>LD02 - 0402</td>
<td>6.3V = 6</td>
<td>Voltage</td>
<td>Dielectric Code</td>
<td>Capacitance Code (In pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Terminations</td>
<td>Packaging</td>
</tr>
<tr>
<td>LD03 - 0603</td>
<td>10V = Z</td>
<td>16V = Y</td>
<td>25V = 3</td>
<td>X8R = F</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>B = ±.10 pF (&lt;10pF)</td>
<td>A = Not Applicable</td>
<td>B = 5% min lead</td>
</tr>
<tr>
<td>LD04 - 0504*</td>
<td>35V = D</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>C = ±.25 pF (&lt;10pF)</td>
<td></td>
<td>X = FLEXITERM® with 5% min lead**</td>
</tr>
<tr>
<td>LD05 - 0805</td>
<td>50V = 5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>D = ±.50 pF (&lt;10pF)</td>
<td></td>
<td>**X7R only</td>
</tr>
<tr>
<td>LD06 - 1206</td>
<td>100V = 1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>F = ±1% (≥ 10 pF)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD10 - 1210</td>
<td>200V = 2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>G = ±2% (≥ 10 pF)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD12 - 1812</td>
<td>500V = 7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>J = ±5%</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD13 - 1825</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>K = ±10%</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD14 - 2225</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>M = ±20%</td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD20 - 2220</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*LD04 has the same CV ranges as LD03.

NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.

See FLEXITERM® section for CV options

Not RoHS Compliant
# MLCC Tin/Lead Termination “B”

## X8R – Specifications and Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X8R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55ºC to +150ºC</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td></td>
</tr>
<tr>
<td></td>
<td>≤ 2.5% for ≥ 50V DC rating</td>
<td></td>
</tr>
<tr>
<td></td>
<td>≤ 3.5% for 25V DC and 16V DC rating</td>
<td></td>
</tr>
<tr>
<td><strong>Dissipation Factor</strong></td>
<td>≤ 2.5% for ≥ 50V DC rating</td>
<td>Freq.: 1.0 kHz ± 10% Voltage: 1.0Vrms ± .2V</td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>100,000MΩ or 1000MΩ - µF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>No breakdown or visual defects</td>
<td></td>
</tr>
<tr>
<td><strong>Resistance to Flexure</strong></td>
<td>No defects</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.</td>
</tr>
<tr>
<td>Stresses</td>
<td>≤ ±12%</td>
<td></td>
</tr>
<tr>
<td><strong>Appearance</strong></td>
<td>No defects</td>
<td></td>
</tr>
<tr>
<td><strong>Capacitance Variation</strong></td>
<td>≤ ±12%</td>
<td></td>
</tr>
<tr>
<td><strong>Dissipation Factor</strong></td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
<tr>
<td><strong>Solderability</strong></td>
<td>≥ 95% of each terminal should be covered with fresh solder</td>
<td>Dip device in eutectic solder at 230 ± 5ºC for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td><strong>Resistance to Solder Heat</strong></td>
<td>No defects, &lt;25% leaching of either end terminal</td>
<td>Dip device in eutectic solder at 260ºC for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.</td>
</tr>
<tr>
<td><strong>Appearance</strong></td>
<td>No visual defects</td>
<td>Step 1: -55ºC ± 2º 30 ± 3 minutes</td>
</tr>
<tr>
<td><strong>Capacitance Variation</strong></td>
<td>≤ ±7.5%</td>
<td>Step 2: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td><strong>Dissipation Factor</strong></td>
<td>Meets Initial Values (As Above)</td>
<td>Step 3: +125ºC ± 2º 30 ± 3 minutes</td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>Meets Initial Values (As Above)</td>
<td>Step 4: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>Meets Initial Values (As Above)</td>
<td>Repeat for 5 cycles and measure after 24 ± 2 hours at room temperature</td>
</tr>
<tr>
<td><strong>Thermal Shock</strong></td>
<td>No visual defects</td>
<td></td>
</tr>
<tr>
<td><strong>Appearance</strong></td>
<td>No visual defects</td>
<td></td>
</tr>
<tr>
<td><strong>Capacitance Variation</strong></td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td><strong>Dissipation Factor</strong></td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Load Life</strong></td>
<td>No visual defects</td>
<td>Charge device with 1.5 rated voltage (≤ 10V) in test chamber set at 150ºC ± 2ºC for 1000 hours (+48, -0) Remove from test chamber and stabilize at room temperature for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td><strong>Appearance</strong></td>
<td>No visual defects</td>
<td>Store in a test chamber set at 85ºC ± 2ºC/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied. Remove from chamber and stabilize at room temperature and humidity for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td><strong>Capacitance Variation</strong></td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td><strong>Dissipation Factor</strong></td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
### MLCC Tin/Lead Termination “B”

Capacitance Range (X8R Dielectric)

<table>
<thead>
<tr>
<th>SIZE</th>
<th>LD03 25V</th>
<th>LD03 50V</th>
<th>LD05 25V</th>
<th>LD05 50V</th>
<th>LD06 25V</th>
<th>LD06 50V</th>
</tr>
</thead>
<tbody>
<tr>
<td>271</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td></td>
<td></td>
</tr>
<tr>
<td>301</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td></td>
<td></td>
</tr>
<tr>
<td>471</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td></td>
<td></td>
</tr>
<tr>
<td>681</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td></td>
<td></td>
</tr>
<tr>
<td>102</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>152</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>222</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>472</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>822</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>113</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>124</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>164</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>184</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>244</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>334</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>394</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>474</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>684</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>824</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>105</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SIZE</th>
<th>LD03 25V</th>
<th>LD03 50V</th>
<th>LD05 25V</th>
<th>LD05 50V</th>
<th>LD06 25V</th>
<th>LD06 50V</th>
</tr>
</thead>
<tbody>
<tr>
<td>271</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td></td>
<td></td>
</tr>
<tr>
<td>301</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td></td>
<td></td>
</tr>
<tr>
<td>471</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td></td>
<td></td>
</tr>
<tr>
<td>681</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td></td>
<td></td>
</tr>
<tr>
<td>102</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>152</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>222</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>472</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>822</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>113</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>124</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>164</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>184</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>244</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>334</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>394</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>474</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>684</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>824</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>105</td>
<td>G</td>
<td>G</td>
<td>J</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
</tbody>
</table>

### Letter Thickness

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>0.33</td>
<td>0.56</td>
<td>0.71</td>
<td>0.90</td>
<td>0.94</td>
<td>1.02</td>
<td>1.27</td>
<td>1.40</td>
<td>1.52</td>
<td>1.78</td>
<td>2.29</td>
<td>2.54</td>
<td>2.79</td>
</tr>
<tr>
<td></td>
<td>(0.013)</td>
<td>(0.022)</td>
<td>(0.028)</td>
<td>(0.035)</td>
<td>(0.037)</td>
<td>(0.040)</td>
<td>(0.050)</td>
<td>(0.055)</td>
<td>(0.060)</td>
<td>(0.070)</td>
<td>(0.090)</td>
<td>(0.100)</td>
<td>(0.110)</td>
</tr>
</tbody>
</table>
MLCC Tin/Lead Termination “B”
X7R – General Specifications

AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a “B” in the 12th position of the AVX Catalog Part Number. This fulfills AVX’s commitment to providing a full range of products to our customers. AVX has provided in the following pages a full range of values that we are currently offering in this special “B” termination. Please contact the factory if you require additional information on our MLCC Tin/Lead Termination “B” products.

PART NUMBER (see page 2 for complete part number explanation)

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (In pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>LD02 - 0402</td>
<td>6.3V = 6</td>
<td>X7R = C</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>B = ±10 pF (&lt;10pF)</td>
<td>A = Not Applicable</td>
<td>B = 5% min lead</td>
<td>2 = 7” Reel</td>
<td>A = Std. Product</td>
</tr>
<tr>
<td>LD03 - 0603</td>
<td>10V = Z</td>
<td></td>
<td></td>
<td>C = ±25 pF (&lt;10pF)</td>
<td></td>
<td></td>
<td>4 = 13” Reel</td>
<td></td>
</tr>
<tr>
<td>LD04 - 0504*</td>
<td>16V = Y</td>
<td></td>
<td></td>
<td>D = ±50 pF (&lt;10pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD05 - 0805</td>
<td>25V = 3</td>
<td></td>
<td></td>
<td>E = ±1% (≥ 10 pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD06 - 1206</td>
<td>35V = D</td>
<td></td>
<td></td>
<td>F = ±2% (≥ 10 pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD10 - 1210</td>
<td>50V = 5</td>
<td></td>
<td></td>
<td>G = ±5%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD12 - 1812</td>
<td>100V = 1</td>
<td></td>
<td></td>
<td>H = ±10%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD13 - 1825</td>
<td>200V = 2</td>
<td></td>
<td></td>
<td>J = ±20%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD14 - 2225</td>
<td>500V = 7</td>
<td></td>
<td></td>
<td>K = ±10%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD20 - 2220</td>
<td></td>
<td></td>
<td></td>
<td>M = ±20%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*LD04 has the same CV ranges as LD03.

NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers.
Contact factory for non-specified capacitance values.

See FLEXITERM® section for CV options.

Not RoHS Compliant

- **X7R only**

See FLEXITERM® section for CV options.
<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X7R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td></td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>≤ 10% for ≥ 50V DC rating</td>
<td>Freq.: 1.0 kHz ± 10%</td>
</tr>
<tr>
<td></td>
<td>≤ 12.5% for ≥ 25V DC rating</td>
<td>Voltage: 1.0Vrms ± .2V</td>
</tr>
<tr>
<td></td>
<td>≤ 12.5% for ≥ 25V and ≤ 10V DC rating</td>
<td></td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>100,000MΩ or 1000MΩ - μF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>No breakdown or visual defects</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max)  Note: Charge device with 150% of rated voltage for 500V devices.</td>
</tr>
<tr>
<td>Resistance to Flexure Stresses</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects</td>
<td>Deflection: 2mm</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12%</td>
<td>Test Time: 30 seconds</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
<tr>
<td><strong>Solderability</strong></td>
<td>≥ 95% of each terminal should be covered with fresh solder</td>
<td>Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td>Resistance to Solder Heat</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects, &lt;25% leaching of either end terminal</td>
<td>Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Thermal Shock</strong></td>
<td>No visual defects</td>
<td>Step 1: -55ºC ± 2º, 30 ± 3 minutes</td>
</tr>
<tr>
<td>Appearance</td>
<td></td>
<td>Step 2: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td>Step 3: +125ºC ± 2º, 30 ± 3 minutes</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 4: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td>Repeat for 5 cycles and measure after 24 ± 2 hours at room temperature</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Load Life</strong></td>
<td>No visual defects</td>
<td>Charge device with 1.5 rated voltage (≤ 10V) in test chamber set at 125ºC ± 2ºC for 1000 hours (+48, -0)</td>
</tr>
<tr>
<td>Appearance</td>
<td></td>
<td>Remove from test chamber and stabilize at room temperature for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Load Humidity</strong></td>
<td>No visual defects</td>
<td>Store in a test chamber set at 85ºC ± 2ºC/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.</td>
</tr>
<tr>
<td>Appearance</td>
<td></td>
<td>Remove from chamber and stabilize at room temperature and humidity for 24 ± 2 hours before measuring.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
## MLCC Tin/Lead Termination “B”

**Capacitance Range (X7R Dielectric)**

**Preferred Sizes Are Shaded**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>LD02</th>
<th>LD03</th>
<th>LD05</th>
<th>LD06</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
</tr>
<tr>
<td>Packaging</td>
<td>All Paper</td>
<td>All Paper</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td>(l) Length</td>
<td>mm</td>
<td>1.00 ± 0.10</td>
<td>1.60 ± 0.15</td>
<td>2.01 ± 0.20</td>
</tr>
<tr>
<td>(in.)</td>
<td>(0.040 ± 0.004)</td>
<td>(0.063 ± 0.008)</td>
<td>(0.079 ± 0.016)</td>
<td>(0.126 ± 0.016)</td>
</tr>
<tr>
<td>(w) Width</td>
<td>mm</td>
<td>0.50 ± 0.10</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
</tr>
<tr>
<td>(in.)</td>
<td>(0.020 ± 0.004)</td>
<td>(0.032 ± 0.006)</td>
<td>(0.049 ± 0.008)</td>
<td>(0.063 ± 0.008)</td>
</tr>
<tr>
<td>(t) Terminal</td>
<td>mm</td>
<td>0.25 ± 0.05</td>
<td>0.35 ± 0.15</td>
<td>0.50 ± 0.25</td>
</tr>
</tbody>
</table>

| | | | | |
| WVDC | 18 | 25 | 50 | 100 | 200 | 6.3 | 10 | 16 | 25 | 50 | 100 | 200 | 6.3 | 10 | 16 | 25 | 50 | 100 | 200 | 6.3 | 10 | 16 | 25 | 50 | 100 | 200 |
| Cap. (pF) | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 |

| 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 | 0.033 |
| 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 | 0.047 |

| 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 | 0.089 |

| 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 | 0.109 |

| 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 | 0.092 |

| 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 | 0.68 |

| 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 | 2.2 |

| 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 |

| 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 | 4.7 |

| 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 | 10 |

| 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 | 22 |

| 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 | 47 |

| 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 |

| WVDC | 16 | 25 | 50 | 6.3 | 10 | 16 | 25 | 50 | 100 | 200 | 6.3 | 10 | 16 | 25 | 50 | 100 | 200 | 6.3 | 10 | 16 | 25 | 50 | 100 | 200 |

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max. Thickness</td>
<td>0.33</td>
<td>0.56</td>
<td>0.71</td>
<td>0.90</td>
<td>0.94</td>
<td>1.02</td>
<td>1.27</td>
<td>1.40</td>
<td>1.52</td>
<td>1.78</td>
<td>2.29</td>
<td>2.54</td>
<td>2.79</td>
</tr>
</tbody>
</table>

| Thickness | (0.013) | (0.022) | (0.028) | (0.036) | (0.037) | (0.040) | (0.055) | (0.058) | (0.060) | (0.070) | (0.093) | (0.100) | (0.110) |

| Paper | EMBOSSED |

= Under Development
**MLCC Tin/Lead Termination “B”**

Capacitance Range (X7R Dielectric)

<table>
<thead>
<tr>
<th>SIZE</th>
<th>LD10</th>
<th>LD12</th>
<th>LD13</th>
<th>LD20</th>
<th>LD14</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Rellow Only</td>
<td>Rellow Only</td>
<td>Rellow Only</td>
<td>Rellow Only</td>
<td>Rellow Only</td>
</tr>
<tr>
<td>Packaging</td>
<td>Paper/Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>3.30 ± 0.20</td>
<td>4.50 ± 0.30</td>
<td>5.70 ± 0.40</td>
<td>6.32 ± 0.25</td>
<td></td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>2.50 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>4.00 ± 0.20</td>
<td>5.00 ± 0.01</td>
<td></td>
</tr>
<tr>
<td>(t) Terminal (mm)</td>
<td>0.50 ± 0.20</td>
<td>0.61 ± 0.30</td>
<td>0.64 ± 0.20</td>
<td>0.84 ± 0.29</td>
<td></td>
</tr>
</tbody>
</table>

**PREFERRED SIZES ARE SHADED**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>LD10</th>
<th>LD12</th>
<th>LD13</th>
<th>LD20</th>
<th>LD14</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cap (pF)</td>
<td>100</td>
<td>150</td>
<td>220</td>
<td></td>
<td></td>
</tr>
<tr>
<td>WvDC</td>
<td>10</td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>100</td>
</tr>
</tbody>
</table>

---

**Letter**

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max. Thickness</td>
<td>0.33</td>
<td>0.56</td>
<td>0.71</td>
<td>0.90</td>
<td>0.94</td>
<td>1.02</td>
<td>1.27</td>
<td>1.40</td>
<td>1.52</td>
<td>1.76</td>
<td>2.29</td>
<td>2.54</td>
<td>2.79</td>
</tr>
<tr>
<td>Thickness (μm)</td>
<td>(0.013)</td>
<td>(0.022)</td>
<td>(0.028)</td>
<td>(0.035)</td>
<td>(0.037)</td>
<td>(0.040)</td>
<td>(0.050)</td>
<td>(0.055)</td>
<td>(0.060)</td>
<td>(0.070)</td>
<td>(0.090)</td>
<td>(0.100)</td>
<td>(0.110)</td>
</tr>
</tbody>
</table>

**PAPER**

**EMBOSS**
AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a “B” in the 12th position of the AVX Catalog Part Number. This fulfills AVX’s commitment to providing a full range of products to our customers. AVX has provided in the following pages a full range of values that we are currently offering in this special “B” termination. Please contact the factory if you require additional information on our MLCC Tin/Lead Termination “B” products.

**PART NUMBER (see page 2 for complete part number explanation)**

<table>
<thead>
<tr>
<th>LD05</th>
<th>5</th>
<th>D</th>
<th>101</th>
<th>J</th>
<th>A</th>
<th>B</th>
<th>2</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD02 - 0402</td>
<td>6.3V = 6</td>
<td>Voltage</td>
<td>Dielectric</td>
<td>Capacitance Code (In pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Terminations</td>
<td>Packaging</td>
</tr>
<tr>
<td>LD03 - 0603</td>
<td>10V = Z</td>
<td>X5R = D</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>B = ±10 pF (&lt;10pF)</td>
<td>A = Not Applicable</td>
<td>B = 5% min lead</td>
<td>2 = 7” Reel</td>
<td></td>
</tr>
<tr>
<td>LD04 - 0604*</td>
<td>16V = Y</td>
<td></td>
<td></td>
<td>C = ±.25 pF (&lt;10pF)</td>
<td></td>
<td></td>
<td>X = FLEXITERM® with 5% min lead**</td>
<td>4 = 13” Reel</td>
</tr>
<tr>
<td>LD05 - 0805</td>
<td>25V = 3</td>
<td></td>
<td></td>
<td>D = ±.50 pF (&lt;10pF)</td>
<td></td>
<td></td>
<td>**X5R only</td>
<td></td>
</tr>
<tr>
<td>LD06 - 1206</td>
<td>35V = D</td>
<td></td>
<td></td>
<td>F = ±1% (≤ 10 pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD10 - 1210</td>
<td>50V = 5</td>
<td></td>
<td></td>
<td>G = ±2% (≤ 10 pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD12 - 1812</td>
<td>100V = 1</td>
<td></td>
<td></td>
<td>J = ±5%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD13 - 1825</td>
<td>200V = 2</td>
<td></td>
<td></td>
<td>K = ±10%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD14 - 2225</td>
<td>500V = 7</td>
<td></td>
<td></td>
<td>M = ±20%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LD20 - 2220</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*LD04 has the same CV ranges as LD03.

NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers.
Contact factory for non-specified capacitance values.

**TYPICAL ELECTRICAL CHARACTERISTICS**

**Temperature Coefficient**

![Temperature Coefficient Graph]

**Insulation Resistance vs Temperature**

![Insulation Resistance vs Temperature Graph]
<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X5R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55°C to +85°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td><strong>Capacitance</strong></td>
<td>Within specified tolerance</td>
<td></td>
</tr>
<tr>
<td>-</td>
<td>2.5% for ≥ 50V DC rating</td>
<td></td>
</tr>
<tr>
<td>-</td>
<td>3.0% for 25V, 35V DC rating</td>
<td></td>
</tr>
<tr>
<td>-</td>
<td>12.5% Max. for 16V DC rating and lower</td>
<td></td>
</tr>
<tr>
<td>-</td>
<td>Contact Factory for DF by PN</td>
<td></td>
</tr>
<tr>
<td><strong>Dissipation Factor</strong></td>
<td>≤ 2.5% for ≥ 50V DC rating</td>
<td>Freq.: 1.0 kHz ± 10%</td>
</tr>
<tr>
<td>-</td>
<td>≤ 3.0% for 25V, 35V DC rating</td>
<td>Voltage: 1.0Vrms ± .2V</td>
</tr>
<tr>
<td>-</td>
<td>≤ 12.5% Max. for 16V DC rating and lower</td>
<td>For Cap &gt; 10 μF, 0.5Vrms @ 120Hz</td>
</tr>
<tr>
<td>-</td>
<td>Contact Factory for DF by PN</td>
<td></td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>10,000MΩ or 500MΩ - μF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>No breakdown or visual defects</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max)</td>
</tr>
<tr>
<td><strong>Resistance to Flexure Stresses</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
<tr>
<td><strong>Solderability</strong></td>
<td>≥ 95% of each terminal should be covered with fresh solder</td>
<td>Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td><strong>Resistance to Solder Heat</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects, &lt;25% leaching of either end terminal</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Thermal Shock</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Step 1: -55°C ± 2°C 30 ± 3 minutes</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td>Step 2: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 3: +85°C ± 2°C 30 ± 3 minutes</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td>Step 4: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td>Repeat for 5 cycles and measure after 24 ± 2 hours at room temperature</td>
</tr>
<tr>
<td><strong>Load Life</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Charge device with 1.5X rated voltage in test chamber set at 85°C ± 2°C for 1000 hours (+48, -0). Note: Contact factory for “optional specification part numbers that are tested at &lt; 1.5X rated voltage.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td>Remove from test chamber and stabilize at room temperature after 24 ± 2 hours before measuring</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td><strong>Load Humidity</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Store in a test chamber set at 85°C ± 2°C/ 85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td>Remove from chamber and stabilize at room temperature and humidity for 24 ± 2 hours before measuring</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>
## MLCC Tin/Lead Termination “B”

### Capacitance Range (X5R Dielectric)

**PREFERRED SIZES ARE SHADEd**

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max. Thickness</td>
<td>0.33 (0.013)</td>
<td>0.56 (0.022)</td>
<td>0.71 (0.028)</td>
<td>0.90 (0.035)</td>
<td>1.04 (0.037)</td>
<td>1.32 (0.040)</td>
<td>1.27 (0.045)</td>
<td>1.40 (0.055)</td>
<td>1.52 (0.060)</td>
<td>1.78 (0.070)</td>
<td>2.29 (0.090)</td>
<td>2.54 (0.100)</td>
<td>2.79 (0.110)</td>
</tr>
<tr>
<td>Paper</td>
<td>PAPER</td>
<td>EMBOSSED</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Specifications

- **Capacitance Range**: (Cap) 100, 150, 220, 330, 470, 680, 1000, 1500, 2200, 3300, 4700, 6800
- **Soldering**: Reflow/Wave, Reflow/Wave, Reflow/Wave, Reflow/Wave, Reflow/Wave, Reflow/Wave, Reflow/Wave, Reflow/Wave, Reflow/Wave, Reflow/Wave
- **(L) Length**: (in.) (0.040 ± 0.004) (0.063 ± 0.006) (0.079 ± 0.008) (0.126 ± 0.008)
- **(W) Width**: (in.) (0.020 ± 0.004) (0.032 ± 0.006) (0.049 ± 0.008) (0.063 ± 0.008)
- **(t) Terminal**: (in.) (0.010 ± 0.004) (0.014 ± 0.006) (0.020 ± 0.010) (0.020 ± 0.010) (0.020 ± 0.010)
- **WVDC**: 4 6.3 10 16 25 50 4 6.3 10 16 25 35 50 4 6.3 10 16 25 35 50

*Optional Specifications – Contact factory

**NOTE**: Contact factory for non-specified capacitance values
GENERAL DESCRIPTION

AVX Corporation has supported the Automotive Industry requirements for Multilayer Ceramic Capacitors consistently for more than 10 years. Products have been developed and tested specifically for automotive applications and all manufacturing facilities are QS9000 and VDA 6.4 approved.

As part of our sustained investment in capacity and state of the art technology, we are now transitioning from the established Pd/Ag electrode system to a Base Metal Electrode system (BME).

AVX is using AECQ200 as the qualification vehicle for this transition. A detailed qualification package is available on request and contains results on a range of part numbers including:

- X7R dielectric components containing BME electrode and copper terminations with a Ni/Sn plated overcoat
- X7R dielectric components, BME electrode with epoxy finish for conductive glue mounting

HOW TO ORDER

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric Code</th>
<th>Capacitance Code (in pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0402</td>
<td>10V = Z</td>
<td>NP0 = A</td>
<td>2 Sig. Digits + Number of Zeros e.g. 10 F = 106</td>
<td>F = ±1% (≥10pF)*</td>
<td>4 = Automotive</td>
<td>T = Plated Ni and Sn Z = FLEXITERM**</td>
<td>2 = 7” Reel</td>
<td>A = Std. Product</td>
</tr>
<tr>
<td>0603</td>
<td>16V = Y</td>
<td>X7R = C</td>
<td></td>
<td>G = ±2% (≥10pF)*</td>
<td></td>
<td>U = Conductive Epo</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0805</td>
<td>25V = 3</td>
<td>X8R = F</td>
<td></td>
<td>J = ±5% (≥1µF)</td>
<td></td>
<td>**X7R X8R only</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1206</td>
<td>50V = 5</td>
<td></td>
<td></td>
<td>K = ±10%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>100V = 1</td>
<td></td>
<td></td>
<td>M = ±20%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1812</td>
<td>200V = 2</td>
<td></td>
<td></td>
<td>*NPO only</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>250V = V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>500V = 7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Contact factory for availability of Tolerance Options for Specific Part Numbers.

NOTE: Contact factory for non-specified capacitance values 0402 case size available in T termination only.

COMMERCIAL VS AUTOMOTIVE MLCC PROCESS COMPARISON

<table>
<thead>
<tr>
<th>Administrative</th>
<th>Commercial</th>
<th>No restriction on who purchases these parts.</th>
<th>Automotive</th>
<th>Specific Automotive Part Number. sed to control supply of product to Automotive customers.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Design</td>
<td>Minimum ceramic thickness of 0.020”</td>
<td>Minimum Ceramic thickness of 0.029” (0.74mm) on all X7R product.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Dicing</td>
<td>Side &amp; End Margins = 0.003” min</td>
<td>Side &amp; End Margins = 0.004” min Cover Layers = 0.003” min</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Lot Qualification (Destructive Physical Analysis - DPA)</td>
<td>As per EIA RS469</td>
<td>Increased sample plan stricter criteria.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Visual/Cosmetic Quality</td>
<td>Standard process and inspection</td>
<td>100% inspection</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Application Robustness</td>
<td>Standard sampling for accelerated wave solder on X7R dielectrics</td>
<td>Increased sampling for accelerated wave solder on X7R and NP0 followed by lot by lot reliability testing.</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

All Tests have Accept/Reject Criteria 0/1
a) Bend Test
The capacitor is soldered to the PC Board as shown:

```
1mm/sec

90 mm
```

Typical bend test results are shown below:

<table>
<thead>
<tr>
<th>Style</th>
<th>Conventional</th>
<th>Soft Term</th>
</tr>
</thead>
<tbody>
<tr>
<td>0603</td>
<td>&gt;2mm</td>
<td>&gt;5</td>
</tr>
<tr>
<td>0805</td>
<td>&gt;2mm</td>
<td>&gt;5</td>
</tr>
<tr>
<td>1206</td>
<td>&gt;2mm</td>
<td>&gt;5</td>
</tr>
</tbody>
</table>

b) Temperature Cycle testing
FLEXITERM® has the ability to withstand at least 1000 cycles between -55°C and +125°C
### Automotive MLCC-NP0 Capacitance Range

<table>
<thead>
<tr>
<th>Soldering</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>25V</td>
<td>50V</td>
<td>100V</td>
<td>200V</td>
</tr>
<tr>
<td>100</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>120</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>150</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>180</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>220</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>270</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>330</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>390</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>470</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>510</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>560</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>680</td>
<td>C</td>
<td>C</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>820</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>101</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>121</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>151</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>181</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>221</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>271</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>331</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>391</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>471</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>511</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>561</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>681</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>821</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>101</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>121</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>151</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>181</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>221</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>271</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>331</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>391</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>471</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>511</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>561</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>681</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>821</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>101</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>121</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>151</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>181</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>221</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>271</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>331</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>391</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>471</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>511</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>561</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>681</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>821</td>
<td>G</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
</tbody>
</table>

### Thickness

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max.</td>
<td>0.33</td>
<td>0.56</td>
<td>0.71</td>
<td>0.90</td>
<td>0.94</td>
<td>1.02</td>
<td>1.27</td>
<td>1.40</td>
<td>1.52</td>
<td>1.78</td>
<td>2.29</td>
<td>2.54</td>
<td>2.79</td>
</tr>
<tr>
<td>Thickness</td>
<td>(0.013)</td>
<td>(0.022)</td>
<td>(0.028)</td>
<td>(0.035)</td>
<td>(0.037)</td>
<td>(0.040)</td>
<td>(0.050)</td>
<td>(0.055)</td>
<td>(0.060)</td>
<td>(0.070)</td>
<td>(0.090)</td>
<td>(0.100)</td>
<td>(0.110)</td>
</tr>
</tbody>
</table>
## Automotive MLCC - X8R Capacitance Range

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>WVDC</td>
<td>25V</td>
<td>50V</td>
</tr>
<tr>
<td>271</td>
<td>270</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>491</td>
<td>470</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>681</td>
<td>680</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>102</td>
<td>1000</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>152</td>
<td>1500</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>182</td>
<td>1800</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>222</td>
<td>2200</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>272</td>
<td>2700</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>332</td>
<td>3300</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>392</td>
<td>3900</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>342</td>
<td>3400</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>472</td>
<td>4700</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>562</td>
<td>5600</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>682</td>
<td>6800</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>692</td>
<td>6900</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>103</td>
<td>101</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>133</td>
<td>132</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>153</td>
<td>151</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>133</td>
<td>132</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>223</td>
<td>221</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>273</td>
<td>271</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>333</td>
<td>331</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>393</td>
<td>391</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>473</td>
<td>471</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>363</td>
<td>361</td>
<td>G</td>
<td>N</td>
</tr>
<tr>
<td>663</td>
<td>661</td>
<td>G</td>
<td>N</td>
</tr>
<tr>
<td>823</td>
<td>821</td>
<td>G</td>
<td>N</td>
</tr>
<tr>
<td>104</td>
<td>102</td>
<td>N</td>
<td>N</td>
</tr>
<tr>
<td>124</td>
<td>122</td>
<td>N</td>
<td>N</td>
</tr>
<tr>
<td>144</td>
<td>142</td>
<td>N</td>
<td>N</td>
</tr>
<tr>
<td>184</td>
<td>182</td>
<td>N</td>
<td>N</td>
</tr>
<tr>
<td>224</td>
<td>222</td>
<td>N</td>
<td>N</td>
</tr>
<tr>
<td>274</td>
<td>272</td>
<td>N</td>
<td>M</td>
</tr>
<tr>
<td>334</td>
<td>332</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>394</td>
<td>392</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>474</td>
<td>472</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>684</td>
<td>682</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>824</td>
<td>822</td>
<td>M</td>
<td>M</td>
</tr>
<tr>
<td>105</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>WVDC</td>
<td>25V</td>
<td>50V</td>
</tr>
<tr>
<td>271</td>
<td>270</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>491</td>
<td>470</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>681</td>
<td>680</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>102</td>
<td>1000</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>152</td>
<td>1500</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>182</td>
<td>1800</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>222</td>
<td>2200</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>272</td>
<td>2700</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>332</td>
<td>3300</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>392</td>
<td>3900</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>342</td>
<td>3400</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>472</td>
<td>4700</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>562</td>
<td>5600</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>682</td>
<td>6800</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>692</td>
<td>6900</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>103</td>
<td>101</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>133</td>
<td>132</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>153</td>
<td>151</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>133</td>
<td>132</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>223</td>
<td>221</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>273</td>
<td>271</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>333</td>
<td>331</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>393</td>
<td>391</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>473</td>
<td>471</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>363</td>
<td>361</td>
<td>G</td>
<td>N</td>
</tr>
<tr>
<td>663</td>
<td>661</td>
<td>G</td>
<td>N</td>
</tr>
<tr>
<td>823</td>
<td>821</td>
<td>G</td>
<td>N</td>
</tr>
<tr>
<td>104</td>
<td>102</td>
<td>N</td>
<td></td>
</tr>
<tr>
<td>124</td>
<td>122</td>
<td>N</td>
<td></td>
</tr>
<tr>
<td>144</td>
<td>142</td>
<td>N</td>
<td></td>
</tr>
<tr>
<td>184</td>
<td>182</td>
<td>N</td>
<td></td>
</tr>
<tr>
<td>224</td>
<td>222</td>
<td>N</td>
<td></td>
</tr>
<tr>
<td>274</td>
<td>272</td>
<td>N</td>
<td></td>
</tr>
<tr>
<td>334</td>
<td>332</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>394</td>
<td>392</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>474</td>
<td>472</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>684</td>
<td>682</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>824</td>
<td>822</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>105</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max.</td>
<td>0.33</td>
<td>0.56</td>
<td>0.71</td>
<td>0.90</td>
<td>0.94</td>
<td>1.02</td>
<td>1.27</td>
<td>1.40</td>
<td>1.52</td>
<td>1.78</td>
<td>2.29</td>
<td>2.54</td>
<td>2.79</td>
</tr>
<tr>
<td>Thickness</td>
<td>0.013</td>
<td>0.022</td>
<td>0.028</td>
<td>0.035</td>
<td>0.037</td>
<td>0.040</td>
<td>0.050</td>
<td>0.055</td>
<td>0.060</td>
<td>0.070</td>
<td>0.090</td>
<td>0.100</td>
<td>0.110</td>
</tr>
</tbody>
</table>

**PAPER**

**EMBOSSED**
AVX’s APS COTS+ series of multilayer ceramic capacitors offers the customer a high reliability solution with an ultralow failure rate, ≤1ppb, in a variety of case sizes and voltages. The APS range encompasses a wide range of dielectric types to meet the customer’s requirements from low temperature/voltage capacitance change dielectric, NP0, to high preforming capacitance voltage X7R to high temperature reliability dielectrics, X8R/L.

APS capacitors have a wider capacitance range than MIL spec parts that satisfies the need for higher CV demands and board space saving requirements. Each production lot is extensively tested and removes the requirement for customer specific drawings. The testing regime uses many of the MIL-STD test methods as per MIL-PRF-55681 and has a field failure rate of less than 1 ppb. The APS testing series uses AVX’s unique in-house maverick testing detection system that eliminates infant mortality failures.

Applications suitable for APS include Industrial, Telecommunications, Aviation, and Military. The APS is available with a range of different termination finishes, Flexiterm®, Nickel / Tin and Tin with Pb1. Flexiterm® technology delivers improved thermo-mechanical stress resistance.

**AVX’S APS RELIABILITY TEST SUMMARY**
- 100% Visual Inspection
- DPA
- IR, DF, Cap, DWV
- Maverick Lot Review
- Thermal Shocl
- 85/85 Testing
- Life Testing 125°C 2xRV
- C of C with every Order
- Quarterly Data Package

**FEATURES**
- The APS range has been extensively reliability tested as standard resulting in an ultralow failure rate, ≤1ppb
- The APS range is available with Flexiterm® that deliver’s high thermo-mechanical stress resistance.
- High CV range enabling board space saving requirements.

**HOW TO ORDER**

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (in pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>AP03</td>
<td>10V = Z</td>
<td>NP0 = A</td>
<td>2 Sig. Digits + Number of Zeros e.g. 10 μF = 106</td>
<td>J = ±5% K = ±10% M = ±20%</td>
<td>Q = APS</td>
<td>T = Plated Ni and Sn</td>
<td>2 = 7” Reel</td>
<td>A = Std. Product</td>
</tr>
<tr>
<td>AP05</td>
<td>16V = Y</td>
<td>X7R = C</td>
<td></td>
<td></td>
<td></td>
<td>Z = FLEXITERM***</td>
<td></td>
<td></td>
</tr>
<tr>
<td>AP06</td>
<td>25V = 3</td>
<td>X8R = F</td>
<td></td>
<td></td>
<td></td>
<td>B = 10% min lead</td>
<td></td>
<td></td>
</tr>
<tr>
<td>AP10</td>
<td>50V = 5</td>
<td>X8L = L</td>
<td></td>
<td></td>
<td></td>
<td>X = FLEXITERM® with 10% min lead</td>
<td></td>
<td></td>
</tr>
<tr>
<td>AP12</td>
<td>100V = 1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Z.X for X7R only</td>
<td></td>
<td></td>
</tr>
<tr>
<td>AP20</td>
<td>200V = 2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>**RoHS compliant</td>
<td></td>
<td></td>
</tr>
<tr>
<td>AP22</td>
<td>250V = V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AP50</td>
<td>500V = 7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for availability of Termination and Tolerance Options for Specific Part Number.
## APS COTS+ NP0 Series

### Capacitance Range

<table>
<thead>
<tr>
<th></th>
<th>AP03 = 0603</th>
<th>AP05 = 0805</th>
<th>AP06 = 1206</th>
<th>AP10 = 1210</th>
</tr>
</thead>
<tbody>
<tr>
<td>25V</td>
<td>50V</td>
<td>100V</td>
<td>25V</td>
<td>50V</td>
</tr>
<tr>
<td>100</td>
<td>100</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>120</td>
<td>12</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>150</td>
<td>15</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>220</td>
<td>22</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>270</td>
<td>27</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>330</td>
<td>33</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>390</td>
<td>39</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>470</td>
<td>47</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>510</td>
<td>51</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>680</td>
<td>68</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>820</td>
<td>82</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>101</td>
<td>101</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>121</td>
<td>121</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>151</td>
<td>151</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>221</td>
<td>221</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>271</td>
<td>271</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>331</td>
<td>331</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>391</td>
<td>391</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>471</td>
<td>471</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>511</td>
<td>511</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>681</td>
<td>681</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>821</td>
<td>821</td>
<td>G</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>102</td>
<td>102</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>122</td>
<td>122</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>152</td>
<td>152</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>182</td>
<td>182</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>222</td>
<td>222</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>272</td>
<td>272</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>332</td>
<td>332</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>392</td>
<td>392</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>472</td>
<td>472</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>512</td>
<td>512</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>682</td>
<td>682</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>822</td>
<td>822</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>103</td>
<td>103</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>123</td>
<td>123</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>153</td>
<td>153</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>183</td>
<td>183</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>223</td>
<td>223</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>273</td>
<td>273</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>333</td>
<td>333</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>393</td>
<td>393</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>473</td>
<td>473</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>513</td>
<td>513</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>683</td>
<td>683</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>823</td>
<td>823</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Letter</th>
<th>G</th>
<th>C</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max. Thickness</td>
<td>0.33 (0.013)</td>
<td>0.56 (0.022)</td>
<td>0.71 (0.028)</td>
<td>0.90 (0.035)</td>
<td>0.94 (0.037)</td>
<td>1.02 (0.040)</td>
<td>1.27 (0.050)</td>
<td>1.40 (0.056)</td>
<td>1.52 (0.060)</td>
<td>1.78 (0.070)</td>
<td>2.29 (0.090)</td>
</tr>
</tbody>
</table>

**PAPER** | **EMBOSS**

**TS 16949, ISO 9001 Certified**
# APS COTS+ X7R Series

## Capacitance Range

<table>
<thead>
<tr>
<th>Thickness ((\text{mm}))</th>
<th>AP03 = 0603</th>
<th>AP05 = 0805</th>
<th>AP06 = 1206</th>
<th>AP10 = 1210</th>
<th>AP12 = 1812</th>
<th>AP20 = 2220</th>
</tr>
</thead>
</table>

*TS 16949, ISO 9001 Certified*
### Capacitance Range

#### APS COTS+ X8R/L Series

<table>
<thead>
<tr>
<th>SIZE</th>
<th>Width</th>
<th>AP03 = 0603</th>
<th>AP05 = 0805</th>
<th>AP06 = 1206</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>WVDC</td>
<td>25V</td>
<td>50V</td>
<td>100V</td>
</tr>
<tr>
<td>102</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>222</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>332</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>472</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>682</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>103</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>153</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>223</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>333</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>473</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>683</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>104</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>154</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>224</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>334</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>474</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
<tr>
<td>684</td>
<td>g</td>
<td>J</td>
<td>J</td>
<td>J</td>
</tr>
</tbody>
</table>

**Note:**
- **Max. Thickness:** 0.33mm (0.013), 0.56mm (0.022), 0.71mm (0.028), 0.90mm (0.035), 0.94mm (0.037), 1.02mm (0.040), 1.27mm (0.050), 1.40mm (0.056), 1.52mm (0.060), 1.71mm (0.067), 2.22mm (0.087), 2.54mm (0.100), 2.79mm (0.110)
- **RoHS Compliance:**
- **TS 16949, ISO 9001 Certified**

---

**PAPER EMBOSSED**

---

**082917 63**

---

**AVX**
GENERAL DESCRIPTION

With increased requirements from the automotive industry for additional component robustness, AVX recognized the need to produce a MLCC with enhanced mechanical strength. It was noted that many components may be subject to severe flexing and vibration when used in various under the hood automotive and other harsh environment applications.

To satisfy the requirement for enhanced mechanical strength, AVX had to find a way of ensuring electrical integrity is maintained whilst external forces are being applied to the component. It was found that the structure of the termination needed to be flexible and after much research and development, AVX launched FLEXITERM®. FLEXITERM® is designed to enhance the mechanical flexure and temperature cycling performance of a standard ceramic capacitor with an X7R dielectric. The industry standard for flexure is 2mm minimum. Using FLEXITERM®, AVX provides up to 5mm of flexure without internal cracks. Beyond 5mm, the capacitor will generally fail “open”.

As well as for automotive applications FLEXITERM® will provide Design Engineers with a satisfactory solution when designing PCB’s which may be subject to high levels of board flexure.

PRODUCT ADVANTAGES

• High mechanical performance able to withstand, 5mm bend test guaranteed.
• Increased temperature cycling performance, 3000 cycles and beyond.
• Flexible termination system.
• Reduction in circuit board flex failures.
• Base metal electrode system.
• Automotive or commercial grade products available.

APPLICATIONS

High Flexure Stress Circuit Boards
• e.g. Depanelization: Components near edges of board.
Variable Temperature Applications
• Soft termination offers improved reliability performance in applications where there is temperature variation.
• e.g. All kind of engine sensors: Direct connection to battery rail.
Automotive Applications
• Improved reliability.
• Excellent mechanical performance and thermo mechanical performance.

HOW TO ORDER

<table>
<thead>
<tr>
<th>Style</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (in pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0803</td>
<td>6=6.3V</td>
<td>C = X7R</td>
<td>2 Sig Digits + Number of Zeros e.g., 104 = 100nF</td>
<td>J = ±5%* K = ±10% M = ±20%</td>
<td>A=Commercial</td>
<td>Z = FLEXITERM® with Tin/Lead termination see AVX LD Series</td>
<td>2 = 7&quot; reel</td>
<td>A = Std. Product</td>
</tr>
<tr>
<td>0805</td>
<td>10V</td>
<td>F = X8R</td>
<td></td>
<td></td>
<td>4 = Automotive</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1206</td>
<td>16V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>25V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1312</td>
<td>50V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2220</td>
<td>100V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>200V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers.
MLCC with FLEXITERM®
Specifications and Test Methods

PERFORMANCE TESTING

AEC-Q200 Qualification:
• Created by the Automotive Electronics Council
• Specification defining stress test qualification for passive components

Testing:
Key tests used to compare soft termination to AEC-Q200 qualification:
• Bend Test
• Temperature Cycle Test

BOARD BEND TEST RESULTS

AEC-Q200 Vs AVX FLEXITERM® Bend Test

![Graph showing bend test results for different component styles.]

BOARD BEND TEST PROCEDURE

According to AEC-Q200

Test Procedure as per AEC-Q200:
Sample size: 20 components
Span: 90mm Minimum deflection spec: 2 mm
• Components soldered onto FR4 PCB (Figure 1)
• Board connected electrically to the test equipment (Figure 2)

AVX ENHANCED SOFT TERMINATION BEND TEST PROCEDURE

Bend Test
The capacitor is soldered to the printed circuit board as shown and is bent up to 10mm at 1mm per second:

• The board is placed on 2 supports 90mm apart (capacitor side down)
• The row of capacitors is aligned with the load stressing knife

• The load is applied and the deflection where the part starts to crack is recorded (Note: Equipment detects the start of the crack using a highly sensitive current detection circuit)
• The maximum deflection capability is 10mm

TEMPERATURE CYCLE TEST PROCEDURE

Test Procedure as per AEC-Q200:
The test is conducted to determine the resistance of the component when it is exposed to extremes of alternating high and low temperatures.
• Sample lot size quantity 77 pieces
• TC chamber cycle from -55°C to +125°C for 1000 cycles
• Interim electrical measurements at 250, 500, 1000 cycles
• Measure parameter capacitance dissipation factor, insulation resistance

![Graph showing test temperature profile.]

TABLE SUMMARY

Typical bend test results are shown below:

<table>
<thead>
<tr>
<th>Style</th>
<th>Conventional Termination</th>
<th>FLEXITERM®</th>
</tr>
</thead>
<tbody>
<tr>
<td>0603</td>
<td>&gt;2mm</td>
<td>&gt;5mm</td>
</tr>
<tr>
<td>0805</td>
<td>&gt;2mm</td>
<td>&gt;5mm</td>
</tr>
<tr>
<td>1206</td>
<td>&gt;2mm</td>
<td>&gt;5mm</td>
</tr>
<tr>
<td>1210</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
**MLCC with FLEXITERM®**

**Specifications and Test Methods**

**BEYOND 1000 CYCLES: TEMPERATURE CYCLE TEST RESULTS**

![Graphs showing % Failure vs. Number of Cycles](image)

**Soft Term - No Defects up to 3000 cycles**

**FLEXITERM® TEST SUMMARY**

- Qualified to AEC-Q200 test/specification with the exception of using AVX 3000 temperature cycles (up to +150°C bend test guaranteed greater than 5mm).
- FLEXITERM® provides improved performance compared to standard termination systems.

- Board bend test improvement by a factor of 2 to 4 times.
- Temperature Cycling:
  - 0% Failure up to 3000 cycles
  - No ESR change up to 3000 cycles

**WITHOUT SOFT TERMINATION**

**WITH SOFT TERMINATION**

Major fear is of latent board flex failures.

Far superior mechanical performance. Generally open failure mode beyond 5mm flexure.
## MLCC with FLEXITERM®
### X8R Dielectric Capacitance Range

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Soldering</td>
<td>25V</td>
<td>50V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Hollow/Wave</td>
<td>Hollow/Wave</td>
</tr>
<tr>
<td>271</td>
<td>271</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>331</td>
<td>330</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>471</td>
<td>470</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>681</td>
<td>680</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>102</td>
<td>100</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>152</td>
<td>150</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>222</td>
<td>220</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>272</td>
<td>270</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>472</td>
<td>470</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>682</td>
<td>680</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>882</td>
<td>880</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>104</td>
<td>100</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>154</td>
<td>0.015</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>184</td>
<td>0.018</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>224</td>
<td>0.022</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>274</td>
<td>0.027</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>334</td>
<td>0.034</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>474</td>
<td>0.047</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>684</td>
<td>0.068</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>884</td>
<td>0.088</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>104</td>
<td>0.1</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>124</td>
<td>0.12</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>154</td>
<td>0.15</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>184</td>
<td>0.18</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>224</td>
<td>0.22</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>274</td>
<td>0.27</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>334</td>
<td>0.33</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>474</td>
<td>0.47</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>684</td>
<td>0.68</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>884</td>
<td>0.88</td>
<td>G</td>
<td>G</td>
</tr>
<tr>
<td>104</td>
<td>1</td>
<td>G</td>
<td>G</td>
</tr>
</tbody>
</table>

### Table:

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max. Thickness</td>
<td>0.33</td>
<td>0.36</td>
<td>0.71</td>
<td>1.00</td>
<td>0.94</td>
<td>1.02</td>
<td>1.27</td>
<td>1.40</td>
<td>1.52</td>
<td>1.76</td>
<td>2.29</td>
<td>2.34</td>
<td>2.79</td>
</tr>
</tbody>
</table>

PAPER

EMBOSSED

AEC-Q200 Qualified
## MLCC with FLEXITERM®

### X7R Dielectric Capacitance Range

<table>
<thead>
<tr>
<th>Soldering</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1812</th>
<th>2220</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>G</th>
<th>J</th>
<th>K</th>
<th>M</th>
<th>N</th>
<th>P</th>
<th>Q</th>
<th>X</th>
<th>Y</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max. Thickness</td>
<td>0.33 (0.013)</td>
<td>0.56 (0.022)</td>
<td>0.71 (0.028)</td>
<td>0.90 (0.035)</td>
<td>0.94 (0.037)</td>
<td>1.02 (0.040)</td>
<td>1.27 (0.050)</td>
<td>1.40 (0.055)</td>
<td>1.52 (0.060)</td>
<td>1.78 (0.070)</td>
<td>2.29 (0.090)</td>
<td>2.54 (0.100)</td>
<td>2.79 (0.110)</td>
</tr>
</tbody>
</table>

| PAPER | EMBOSSED |
FLEXISAFE MLC Chips
For Ultra Safety Critical Applications

AVX have developed a range of components specifically for safety critical applications.

Utilizing the award-winning FLEXITERM™ layer in conjunction with the cascade design previously used for high voltage MLCCs, a range of ceramic capacitors is now available for customers who require components designed with an industry leading set of safety features.

The FLEXITERM™ layer protects the component from any damage to the ceramic resulting from mechanical stress during PCB assembly or use with end customers. Board flexure type mechanical damage accounts for the majority of MLCC failures. The addition of the cascade structure protects the component from low insulation resistance failure resulting from other common causes for failure; thermal stress damage, repetitive strike ESD damage and placement damage. With the inclusion of the cascade design structure to complement the FLEXITERM™ layer, the FLEXISAFE range of capacitors has unbeatable safety features.

**HOW TO ORDER**

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (In pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Termination</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>FS03</td>
<td>16V = Y</td>
<td>X7R = C</td>
<td>2 Sig. Digits + Number of Zeros e.g. 10μF = 106</td>
<td>J = ±5%</td>
<td>A = Commercial</td>
<td>Z = FLEXITERM™</td>
<td>2 = 7&quot; Reel</td>
<td>A = Std. Product</td>
</tr>
<tr>
<td>FS05</td>
<td>16V = Y</td>
<td>X7R = C</td>
<td>2 Sig. Digits + Number of Zeros e.g. 10μF = 106</td>
<td>K = ±10%</td>
<td>4 = Automotive</td>
<td>X = FLEXITERM™ with 5% min lead</td>
<td></td>
<td></td>
</tr>
<tr>
<td>FS06</td>
<td>25V = 3</td>
<td>X7R = C</td>
<td>2 Sig. Digits + Number of Zeros e.g. 10μF = 106</td>
<td>M = ±20%</td>
<td>Q = APS</td>
<td>*Not RoHS Compliant</td>
<td></td>
<td></td>
</tr>
<tr>
<td>FS10</td>
<td>50V = 5</td>
<td>X7R = C</td>
<td>2 Sig. Digits + Number of Zeros e.g. 10μF = 106</td>
<td></td>
<td></td>
<td></td>
<td>4 = 13&quot; Reel</td>
<td></td>
</tr>
<tr>
<td>FS03</td>
<td>100V = 1</td>
<td>X7R = C</td>
<td>2 Sig. Digits + Number of Zeros e.g. 10μF = 106</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**FLEXISAFE X7R RANGE**

<table>
<thead>
<tr>
<th>Capacitance Code</th>
<th>FS03 = 0603</th>
<th>FS05 = 0805</th>
<th>FS06 = 1206</th>
<th>FS10 = 1210</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>102μF</td>
<td>0.001</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>182</td>
<td>0.0018</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>222</td>
<td>0.0022</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>332</td>
<td>0.0033</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>472</td>
<td>0.0047</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>103</td>
<td>0.01</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>123</td>
<td>0.012</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>153</td>
<td>0.015</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>183</td>
<td>0.018</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>223</td>
<td>0.022</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>273</td>
<td>0.027</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>333</td>
<td>0.033</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>473</td>
<td>0.047</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>563</td>
<td>0.056</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>683</td>
<td>0.068</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>823</td>
<td>0.082</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>104</td>
<td>0.1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>124</td>
<td>0.12</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>154</td>
<td>0.15</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>224</td>
<td>0.22</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>334</td>
<td>0.33</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>474</td>
<td>0.47</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Qualified
Capacitor Array

Capacitor Array (IPC)

**BENEFITS OF USING CAPACITOR ARRAYS**

AVX capacitor arrays offer designers the opportunity to lower placement costs, increase assembly line output through lower component count per board and to reduce real estate requirements.

**Reduced Costs**

Placement costs are greatly reduced by effectively placing one device instead of four or two. This results in increased throughput and translates into savings on machine time. Inventory levels are lowered and further savings are made on solder materials, etc.

**Space Saving**

Space savings can be quite dramatic when compared to the use of discrete chip capacitors. As an example, the 0508 4-element array offers a space reduction of >40% vs. 4 x 0402 discrete capacitors and of >70% vs. 4 x 0603 discrete capacitors. (This calculation is dependent on the spacing of the discrete components.)

**Increased Throughput**

Assuming that there are 220 passive components placed in a mobile phone:

A reduction in the passive count to 200 (by replacing discrete components with arrays) results in an increase in throughput of approximately 9%.

A reduction of 40 placements increases throughput by 18%.

For high volume users of cap arrays using the very latest placement equipment capable of placing 10 components per second, the increase in throughput can be very significant and can have the overall effect of reducing the number of placement machines required to mount components:

If 120 million 2-element arrays or 40 million 4-element arrays were placed in a year, the requirement for placement equipment would be reduced by one machine.

During a 20Hr operational day a machine places 720K components. Over a working year of 167 days the machine can place approximately 120 million. If 2-element arrays are mounted instead of discrete components, then the number of placements is reduced by a factor of two and in the scenario where 120 million 2-element arrays are placed there is a saving of one pick and place machine.

Smaller volume users can also benefit from replacing discrete components with arrays. The total number of placements is reduced thus creating spare capacity on placement machines. This in turn generates the opportunity to increase overall production output without further investment in new equipment.

### W2A (0508) Capacitor Arrays

- 4 pcs 0402 Capacitors = 1 pc 0508 Array
- AREA = 7.0mm² (0.276 in²) AREA = 3.95mm² (0.156 in²)

The 0508 4-element capacitor array gives a PCB space saving of over 40% vs four 0402 discretes and over 70% vs four 0603 discrete capacitors.

### W3A (0612) Capacitor Arrays

- 4 pcs 0603 Capacitors = 1 pc 0612 Array
- AREA = 13.8mm² (0.543 in²) AREA = 6.4mm² (0.252 in²)

The 0612 4-element capacitor array gives a PCB space saving of over 50% vs four 0603 discretes and over 70% vs four 0805 discrete capacitors.
AVX is the market leader in the development and manufacture of capacitor arrays. The array family of products also includes the 0612 4-element device as well as 0508 2-element and 4-element series, all of which have received widespread acceptance in the marketplace.

AVX capacitor arrays are available in X5R, X7R and NP0 (C0G) ceramic dielectrics to cover a broad range of capacitance values. Voltage ratings from 6.3 Volts up to 100 Volts are offered. AVX also now offers a range of automotive capacitor arrays qualified to AEC-Q200 (see separate table).

Key markets for capacitor arrays are Mobile and Cordless Phones, Digital Set Top Boxes, Computer Motherboards and Peripherals as well as Automotive applications, RF Modems, Networking Products, etc.

**GENERAL DESCRIPTION**

**HOW TO ORDER**

<table>
<thead>
<tr>
<th>W</th>
<th>Style</th>
<th>2</th>
<th>A</th>
<th>4</th>
<th>3</th>
<th>C</th>
<th>103</th>
<th>M</th>
<th>A</th>
<th>T</th>
<th>2A</th>
</tr>
</thead>
<tbody>
<tr>
<td>W</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>W = RoHS</td>
<td>L = SnPb</td>
<td>2 = 0508</td>
<td>3 = 0612</td>
<td>2 = 2 Element</td>
<td>4 = 4 Element</td>
<td>A = NP0</td>
<td>C = X7R</td>
<td>D = X5R</td>
<td>2 = Sig Digits + Number of Zeros</td>
<td>J = ±5%</td>
<td>K = ±10%</td>
</tr>
<tr>
<td>5pF</td>
<td>10pF</td>
<td>15pF</td>
<td>22pF</td>
<td>33pF</td>
<td>39pF</td>
<td>68pF</td>
<td>0.01</td>
<td>0.1</td>
<td>1</td>
<td>10</td>
<td></td>
</tr>
</tbody>
</table>

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.
# Capacitor Array

## Capacitance Range – NP0/C0G

<table>
<thead>
<tr>
<th>SIZE</th>
<th># Elements</th>
<th>W2 = 0508</th>
<th>W3 = 0612</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>4</td>
<td>4</td>
</tr>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td>Packaging</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td>Length</td>
<td>mm (in.)</td>
<td>1.30 ± 0.15 (0.051 ± 0.006)</td>
<td>1.60 ± 0.150 (0.063 ± 0.006)</td>
</tr>
<tr>
<td>Width</td>
<td>mm (in.)</td>
<td>2.10 ± 0.15 (0.083 ± 0.006)</td>
<td>3.20 ± 0.200 (0.126 ± 0.008)</td>
</tr>
<tr>
<td>Max. Thickness</td>
<td>mm (in.)</td>
<td>0.94 (0.037)</td>
<td>1.35 (0.053)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>WVDC</th>
<th>16</th>
<th>25</th>
<th>50</th>
<th>16</th>
<th>25</th>
<th>50</th>
</tr>
</thead>
<tbody>
<tr>
<td>100</td>
<td>10</td>
<td></td>
<td></td>
<td>10</td>
<td></td>
<td></td>
</tr>
<tr>
<td>120</td>
<td>12</td>
<td></td>
<td></td>
<td>12</td>
<td></td>
<td></td>
</tr>
<tr>
<td>150</td>
<td>15</td>
<td></td>
<td></td>
<td>15</td>
<td></td>
<td></td>
</tr>
<tr>
<td>180</td>
<td>18</td>
<td></td>
<td></td>
<td>18</td>
<td></td>
<td></td>
</tr>
<tr>
<td>220</td>
<td>22</td>
<td></td>
<td></td>
<td>22</td>
<td></td>
<td></td>
</tr>
<tr>
<td>270</td>
<td>27</td>
<td></td>
<td></td>
<td>27</td>
<td></td>
<td></td>
</tr>
<tr>
<td>330</td>
<td>33</td>
<td></td>
<td></td>
<td>33</td>
<td></td>
<td></td>
</tr>
<tr>
<td>390</td>
<td>39</td>
<td></td>
<td></td>
<td>39</td>
<td></td>
<td></td>
</tr>
<tr>
<td>470</td>
<td>47</td>
<td></td>
<td></td>
<td>47</td>
<td></td>
<td></td>
</tr>
<tr>
<td>560</td>
<td>56</td>
<td></td>
<td></td>
<td>56</td>
<td></td>
<td></td>
</tr>
<tr>
<td>660</td>
<td>66</td>
<td></td>
<td></td>
<td>66</td>
<td></td>
<td></td>
</tr>
<tr>
<td>820</td>
<td>82</td>
<td></td>
<td></td>
<td>82</td>
<td></td>
<td></td>
</tr>
<tr>
<td>101</td>
<td>100</td>
<td></td>
<td></td>
<td>100</td>
<td></td>
<td></td>
</tr>
<tr>
<td>121</td>
<td>120</td>
<td></td>
<td></td>
<td>120</td>
<td></td>
<td></td>
</tr>
<tr>
<td>151</td>
<td>150</td>
<td></td>
<td></td>
<td>150</td>
<td></td>
<td></td>
</tr>
<tr>
<td>181</td>
<td>180</td>
<td></td>
<td></td>
<td>180</td>
<td></td>
<td></td>
</tr>
<tr>
<td>221</td>
<td>220</td>
<td></td>
<td></td>
<td>220</td>
<td></td>
<td></td>
</tr>
<tr>
<td>271</td>
<td>270</td>
<td></td>
<td></td>
<td>270</td>
<td></td>
<td></td>
</tr>
<tr>
<td>331</td>
<td>330</td>
<td></td>
<td></td>
<td>330</td>
<td></td>
<td></td>
</tr>
<tr>
<td>391</td>
<td>390</td>
<td></td>
<td></td>
<td>390</td>
<td></td>
<td></td>
</tr>
<tr>
<td>471</td>
<td>470</td>
<td></td>
<td></td>
<td>470</td>
<td></td>
<td></td>
</tr>
<tr>
<td>561</td>
<td>560</td>
<td></td>
<td></td>
<td>560</td>
<td></td>
<td></td>
</tr>
<tr>
<td>661</td>
<td>660</td>
<td></td>
<td></td>
<td>660</td>
<td></td>
<td></td>
</tr>
<tr>
<td>821</td>
<td>820</td>
<td></td>
<td></td>
<td>820</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

= Supported Values
# Capacitor Array

## Capacitance Range – X7R

<table>
<thead>
<tr>
<th>SIZE # Elements</th>
<th>W2 = 0508</th>
<th>W2 = 0508</th>
<th>W3 = 0612</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>2</td>
<td>4</td>
<td>4</td>
</tr>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
</tr>
<tr>
<td>Packaging</td>
<td>All Paper</td>
<td>Paper/Embossed</td>
<td>Paper/Embossed</td>
</tr>
<tr>
<td>Length (mm)</td>
<td>1.30 ± 0.15 (0.051 ± 0.006)</td>
<td>1.30 ± 0.15 (0.051 ± 0.006)</td>
<td>1.60 ± 0.150 (0.063 ± 0.006)</td>
</tr>
<tr>
<td>Width (mm)</td>
<td>2.10 ± 0.15 (0.083 ± 0.006)</td>
<td>2.10 ± 0.15 (0.083 ± 0.006)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
</tr>
<tr>
<td>Max. Thickness (mm)</td>
<td>0.94 (0.037)</td>
<td>0.94 (0.037)</td>
<td>1.35 (0.053)</td>
</tr>
<tr>
<td>WVDC (VDC)</td>
<td>6 10 16 25 50 100</td>
<td>6 10 16 25 50 100</td>
<td>6 10 16 25 50 100</td>
</tr>
<tr>
<td>Cap (μF)</td>
<td>0.010</td>
<td>0.012</td>
<td>0.015</td>
</tr>
<tr>
<td>101</td>
<td>100</td>
<td>120</td>
<td>150</td>
</tr>
<tr>
<td>121</td>
<td>180</td>
<td></td>
<td></td>
</tr>
<tr>
<td>221</td>
<td>220</td>
<td></td>
<td></td>
</tr>
<tr>
<td>271</td>
<td>270</td>
<td></td>
<td></td>
</tr>
<tr>
<td>331</td>
<td>330</td>
<td></td>
<td></td>
</tr>
<tr>
<td>391</td>
<td>390</td>
<td></td>
<td></td>
</tr>
<tr>
<td>471</td>
<td>470</td>
<td></td>
<td></td>
</tr>
<tr>
<td>561</td>
<td>560</td>
<td></td>
<td></td>
</tr>
<tr>
<td>681</td>
<td>680</td>
<td></td>
<td></td>
</tr>
<tr>
<td>821</td>
<td>820</td>
<td></td>
<td></td>
</tr>
<tr>
<td>122</td>
<td>1200</td>
<td></td>
<td></td>
</tr>
<tr>
<td>152</td>
<td>1500</td>
<td></td>
<td></td>
</tr>
<tr>
<td>182</td>
<td>1800</td>
<td></td>
<td></td>
</tr>
<tr>
<td>222</td>
<td>2200</td>
<td></td>
<td></td>
</tr>
<tr>
<td>272</td>
<td>2700</td>
<td></td>
<td></td>
</tr>
<tr>
<td>332</td>
<td>3300</td>
<td></td>
<td></td>
</tr>
<tr>
<td>392</td>
<td>3900</td>
<td></td>
<td></td>
</tr>
<tr>
<td>472</td>
<td>4700</td>
<td></td>
<td></td>
</tr>
<tr>
<td>562</td>
<td>5600</td>
<td></td>
<td></td>
</tr>
<tr>
<td>682</td>
<td>6800</td>
<td></td>
<td></td>
</tr>
<tr>
<td>822</td>
<td>8200</td>
<td></td>
<td></td>
</tr>
<tr>
<td>103</td>
<td>0.010</td>
<td></td>
<td></td>
</tr>
<tr>
<td>123</td>
<td>0.012</td>
<td></td>
<td></td>
</tr>
<tr>
<td>153</td>
<td>0.015</td>
<td></td>
<td></td>
</tr>
<tr>
<td>183</td>
<td>0.018</td>
<td></td>
<td></td>
</tr>
<tr>
<td>223</td>
<td>0.022</td>
<td></td>
<td></td>
</tr>
<tr>
<td>273</td>
<td>0.027</td>
<td></td>
<td></td>
</tr>
<tr>
<td>333</td>
<td>0.033</td>
<td></td>
<td></td>
</tr>
<tr>
<td>393</td>
<td>0.039</td>
<td></td>
<td></td>
</tr>
<tr>
<td>473</td>
<td>0.047</td>
<td></td>
<td></td>
</tr>
<tr>
<td>563</td>
<td>0.056</td>
<td></td>
<td></td>
</tr>
<tr>
<td>683</td>
<td>0.068</td>
<td></td>
<td></td>
</tr>
<tr>
<td>823</td>
<td>0.082</td>
<td></td>
<td></td>
</tr>
<tr>
<td>134</td>
<td>0.10</td>
<td></td>
<td></td>
</tr>
<tr>
<td>124</td>
<td>0.12</td>
<td></td>
<td></td>
</tr>
<tr>
<td>154</td>
<td>0.15</td>
<td></td>
<td></td>
</tr>
<tr>
<td>184</td>
<td>0.18</td>
<td></td>
<td></td>
</tr>
<tr>
<td>224</td>
<td>0.22</td>
<td></td>
<td></td>
</tr>
<tr>
<td>274</td>
<td>0.27</td>
<td></td>
<td></td>
</tr>
<tr>
<td>334</td>
<td>0.33</td>
<td></td>
<td></td>
</tr>
<tr>
<td>474</td>
<td>0.47</td>
<td></td>
<td></td>
</tr>
<tr>
<td>564</td>
<td>0.56</td>
<td></td>
<td></td>
</tr>
<tr>
<td>684</td>
<td>0.68</td>
<td></td>
<td></td>
</tr>
<tr>
<td>824</td>
<td>0.82</td>
<td></td>
<td></td>
</tr>
<tr>
<td>105</td>
<td>1.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>125</td>
<td>1.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>155</td>
<td>1.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>185</td>
<td>1.8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>225</td>
<td>2.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>335</td>
<td>3.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>475</td>
<td>4.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>106</td>
<td>10</td>
<td></td>
<td></td>
</tr>
<tr>
<td>226</td>
<td>22</td>
<td></td>
<td></td>
</tr>
<tr>
<td>476</td>
<td>47</td>
<td></td>
<td></td>
</tr>
<tr>
<td>107</td>
<td>100</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
As the market leader in the development and manufacture of capacitor arrays, AVX is pleased to offer a range of AEC-Q200 qualified arrays to complement our product offering to the Automotive industry. Both the AVX 0612 and 0508 4-element capacitor array styles are qualified to the AEC-Q200 automotive specifications.

AEC-Q200 is the Automotive Industry qualification standard and a detailed qualification package is available on request.

All AVX automotive capacitor array production facilities are certified to ISO/TS 16949:2002.

*Contact factory for availability by part number for K = ±10% and J = ±5% tolerance.

---

### HOW TO ORDER

<table>
<thead>
<tr>
<th>W</th>
<th>Style</th>
<th>A</th>
<th>Array</th>
<th>Y</th>
<th>C</th>
<th>104</th>
<th>K</th>
<th>T</th>
<th>2A</th>
</tr>
</thead>
<tbody>
<tr>
<td>W</td>
<td>RoHS</td>
<td>4</td>
<td>Element</td>
<td>4</td>
<td>Number of Caps</td>
<td>104</td>
<td>Capacitance Code (in pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
</tr>
<tr>
<td>L</td>
<td>SnPb</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Significant Digits + Number of Zeros</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>e.g. 10μF=106</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

W2 = 0508 W3 = 0612

### NPO/C0G

<table>
<thead>
<tr>
<th>SIZE</th>
<th>W2 = 0508</th>
<th>W3 = 0612</th>
</tr>
</thead>
<tbody>
<tr>
<td>W</td>
<td>Number of Elements</td>
<td>W</td>
</tr>
<tr>
<td>RX</td>
<td>W2 = 0508</td>
<td>W3 = 0612</td>
</tr>
<tr>
<td>1R</td>
<td>1.0</td>
<td>1.0</td>
</tr>
<tr>
<td>2R</td>
<td>2.2</td>
<td>2.2</td>
</tr>
<tr>
<td>3R</td>
<td>3.3</td>
<td>3.3</td>
</tr>
<tr>
<td>4R</td>
<td>4.7</td>
<td>4.7</td>
</tr>
</tbody>
</table>

### X7R

<table>
<thead>
<tr>
<th>SIZE</th>
<th>W2 = 0508</th>
<th>W3 = 0612</th>
</tr>
</thead>
<tbody>
<tr>
<td>W</td>
<td>Number of Elements</td>
<td>W</td>
</tr>
<tr>
<td>X7R</td>
<td>W2 = 0508</td>
<td>W3 = 0612</td>
</tr>
<tr>
<td>101</td>
<td>0.1</td>
<td>0.1</td>
</tr>
<tr>
<td>121</td>
<td>0.15</td>
<td>0.15</td>
</tr>
<tr>
<td>151</td>
<td>0.22</td>
<td>0.22</td>
</tr>
</tbody>
</table>

---

*Contact factory for availability by part number for K = ±10% and J = ±5% tolerance.
## Capacitor Array

### PART & PAD LAYOUT DIMENSIONS

#### PART DIMENSIONS

**0508 - 2 Element**

<table>
<thead>
<tr>
<th>L</th>
<th>W</th>
<th>T</th>
<th>BW</th>
<th>BL</th>
<th>P</th>
<th>S</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.30 ± 0.15</td>
<td>2.10 ± 0.15</td>
<td>0.94 MAX</td>
<td>0.43 ± 0.10</td>
<td>0.53 ± 0.08</td>
<td>1.00 REF</td>
<td>0.50 ± 0.10</td>
</tr>
</tbody>
</table>

**0508 - 4 Element**

<table>
<thead>
<tr>
<th>L</th>
<th>W</th>
<th>T</th>
<th>BW</th>
<th>BL</th>
<th>P</th>
<th>X</th>
<th>S</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.30 ± 0.15</td>
<td>2.10 ± 0.15</td>
<td>0.94 MAX</td>
<td>0.25 ± 0.06</td>
<td>0.20 ± 0.06</td>
<td>0.50 REF</td>
<td>0.25 ± 0.10</td>
<td></td>
</tr>
</tbody>
</table>

**0612 - 4 Element**

<table>
<thead>
<tr>
<th>L</th>
<th>W</th>
<th>T</th>
<th>BW</th>
<th>BL</th>
<th>P</th>
<th>X</th>
<th>S</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.65 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>1.35 MAX</td>
<td>0.41 ± 0.10</td>
<td>0.18 ± 0.04</td>
<td>0.76 REF</td>
<td>0.38 ± 0.10</td>
<td></td>
</tr>
</tbody>
</table>

### PAD LAYOUT DIMENSIONS

**0508 - 2 Element**

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
<th>E</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.68 (0.027)</td>
<td>1.32 (0.052)</td>
<td>2.60 (0.079)</td>
<td>0.46 (0.018)</td>
<td>1.00 (0.039)</td>
</tr>
</tbody>
</table>

**0508 - 4 Element**

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
<th>E</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.56 (0.022)</td>
<td>1.32 (0.052)</td>
<td>1.88 (0.074)</td>
<td>0.30 (0.012)</td>
<td>0.50 (0.020)</td>
</tr>
</tbody>
</table>

**0612 - 4 Element**

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
<th>E</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.88 (0.035)</td>
<td>1.65 (0.065)</td>
<td>2.54 (0.100)</td>
<td>0.46 (0.018)</td>
<td>0.76 (0.030)</td>
</tr>
</tbody>
</table>
Low Inductance Capacitors

Introduction

The signal integrity characteristics of a Power Delivery Network (PDN) are becoming critical aspects of board level and semiconductor package designs due to higher operating frequencies, larger power demands, and the ever shrinking lower and upper voltage limits around low operating voltages. These power system challenges are coming from mainstream designs with operating frequencies of 300MHz or greater, modest ICs with power demand of 15 watts or more, and operating voltages below 3 volts.

The classic PDN topology is comprised of a series of capacitor stages. Figure 1 is an example of this architecture with multiple capacitor stages.

An ideal capacitor can transfer all its stored energy to a load instantly. A real capacitor has parasitics that prevent instantaneous transfer of a capacitor’s stored energy. The true nature of a capacitor can be modeled as an RLC equivalent circuit. For most simulation purposes, it is possible to model the characteristics of a real capacitor with one capacitor, one resistor, and one inductor. The RLC values in this model are commonly referred to as equivalent series capacitance (ESC), equivalent series resistance (ESR), and equivalent series inductance (ESL).

The ESL of a capacitor determines the speed of energy transfer to a load. The lower the ESL of a capacitor, the faster that energy can be transferred to a load. Historically, there has been a tradeoff between energy storage (capacitance) and inductance (speed of energy delivery). Low ESL devices typically have low capacitance. Likewise, higher capacitance devices typically have higher ESLs. This tradeoff between ESL (speed of energy delivery) and capacitance (energy storage) drives the PDN design topology that places the fastest low ESL capacitors as close to the load as possible.

Low Inductance MLCCs are found on semiconductor packages and on boards as close as possible to the load.

LOW INDUCTANCE CHIP CAPACITORS

The key physical characteristic determining equivalent series inductance (ESL) of a capacitor is the size of the current loop it creates. The smaller the current loop, the lower the ESL. A standard surface mount MLCC is rectangular in shape with electrical terminations on its shorter sides. A Low Inductance Chip Capacitor (LICC) sometimes referred to as Reverse Geometry Capacitor (RGC) has its terminations on the longer side of its rectangular shape.

When the distance between terminations is reduced, the size of the current loop is reduced. Since the size of the current loop is the primary driver of inductance, an 0306 with a smaller current loop has significantly lower ESL than an 0603. The reduction in ESL varies by EIA size, however, ESL is typically reduced 60% or more with an LICC versus a standard MLCC.

INTERDIGITATED CAPACITORS

The size of a current loop has the greatest impact on the ESL characteristics of a surface mount capacitor. There is a secondary method for decreasing the ESL of a capacitor. This secondary method uses adjacent opposing current loops to reduce ESL. The InterDigitated Capacitor (IDC) utilizes both primary and secondary methods of reducing inductance. The IDC architecture shrinks the distance between terminations to minimize the current loop size, then further reduces inductance by creating adjacent opposing current loops.

An IDC is one single capacitor with an internal structure that has been optimized for low ESL. Similar to standard MLCC versus LICCs, the reduction in ESL varies by EIA case size. Typically, for the same EIA size, an IDC delivers an ESL that is at least 80% lower than an MLCC.

Figure 1 Classic Power Delivery Network (PDN) Architecture
Low Inductance Capacitors

Introduction

LAND GRID ARRAY (LGA) CAPACITORS

Land Grid Array (LGA) capacitors are based on the first Low ESL MLCC technology created to specifically address the design needs of current day Power Delivery Networks (PDNs). This is the 3rd low inductance capacitor technology developed by AVX. LGA technology provides engineers with new options. The LGA internal structure and manufacturing technology eliminates the historic need for a device to be physically small to create small current loops to minimize inductance.

The first family of LGA products are 2 terminal devices. A 2 terminal 0306 LGA delivers ESL performance that is equal to or better than an 0306 8 terminal IDC. The 2 terminal 0805 LGA delivers ESL performance that approaches the 0508 8 terminal IDC. New designs that would have used 8 terminal IDCs are moving to 2 terminal LGAs because the layout is easier for a 2 terminal device and manufacturing yield is better for a 2 terminal LGA versus an 8 terminal IDC.

LGA technology is also used in a 4 terminal family of products that AVX is sampling and will formerly introduce in 2008. Beyond 2008, there are new multi-terminal LGA product families that will provide even more attractive options for PDN designers.

LOW INDUCTANCE CHIP ARRAYS (LICA®)

The LICA® product family is the result of a joint development effort between AVX and IBM to develop a high performance MLCC family of decoupling capacitors. LICA was introduced in the 1980s and remains the leading choice of designers in high performance semiconductor packages and high reliability board level decoupling applications.

LICA® products are used in 99.999% uptime semiconductor package applications on both ceramic and organic substrates. The C4 solder ball termination option is the perfect compliment to flip-chip packaging technology. Mainframe class CPUs, ultimate performance multi-chip modules, and communications systems that must have the reliability of 5 9’s use LICA®.

LICA® products with either Sn/Pb or Pb-free solder balls are used for decoupling in high reliability military and aerospace applications. These LICA® devices are used for decoupling of large pin count FPGAs, ASICs, CPUs, and other high power ICs with low operating voltages.

When high reliability decoupling applications require the very lowest ESL capacitors, LICA® products are the best option.

470 nF 0306 Impedance Comparison

Figure 2 MLCC, LICC, IDC, and LGA technologies deliver different levels of equivalent series inductance (ESL).
Low Inductance Ceramic Capacitors LICC
0306/0508/0612 RoHS Compliant

GENERAL DESCRIPTION

The key physical characteristic determining equivalent series inductance (ESL) of a capacitor is the size of the current loop it creates. The smaller the current loop, the lower the ESL. A standard surface mount MLCC is rectangular in shape with electrical terminations on its shorter sides. A Low Inductance Chip Capacitor (LICC) sometimes referred to as Reverse Geometry Capacitor (RGC) has its terminations on the longer sides of its rectangular shape. The image on the right shows the termination differences between an MLCC and an LICC.

When the distance between terminations is reduced, the size of the current loop is reduced. Since the size of the current loop is the primary driver of inductance, an 0306 with a smaller current loop has significantly lower ESL than an 0603. The reduction in ESL varies by EIA size, however, ESL is typically reduced 60% or more with an LICC versus a standard MLCC.

AVX LICC products are available with a lead-free finish of plated Nickel/Tin.

PERFORMANCE CHARACTERISTICS

<table>
<thead>
<tr>
<th>Capacitance Tolerances</th>
<th>K = ±10%; M = ±20%</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operation</td>
<td>X7R = -55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Range</td>
<td>X5R = -55°C to +85°C</td>
</tr>
<tr>
<td></td>
<td>X7S = -55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Coefficient</td>
<td>X7R, X5R = ±15%; X7S = ±22%</td>
</tr>
<tr>
<td>Voltage Ratings</td>
<td>4, 6.3, 10, 16, 25 VDC</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>4V, 6.3V = 6.5% max; 10V = 5.0% max; 16V = 3.5% max; 25V = 3.0% max</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>100,000MΩ min, or 1,000MΩ per μF min., whichever is less</td>
</tr>
</tbody>
</table>

HOW TO ORDER

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code (in pF)</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Thickness</th>
</tr>
</thead>
<tbody>
<tr>
<td>0306</td>
<td>4V</td>
<td>C = X7R</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>K = ±10%; M = ±20%</td>
<td>A = N/A</td>
<td>T = Plated Ni and Sn</td>
<td>Available</td>
<td>0.56 (0.022)</td>
</tr>
<tr>
<td>0508</td>
<td>4, 6.3V</td>
<td>D = X5R</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>0.76 (0.033)</td>
</tr>
<tr>
<td>0612</td>
<td>10V</td>
<td>E = X6S</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>1.02 (0.040)</td>
</tr>
<tr>
<td></td>
<td>16V</td>
<td>Z = X7S</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>1.27 (0.050)</td>
</tr>
<tr>
<td></td>
<td>3 = 25V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>5 = 50V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*TYPICAL IMPEDANCE CHARACTERISTICS*

*See the thickness tables on the next page.*

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.
Low Inductance Ceramic Capacitors LICC

0306/0508/0612 RoHS Compliant

## PHYSICAL DIMENSIONS AND PAD LAYOUT

### PHYSICAL DIMENSIONS

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0306</th>
<th>0508</th>
<th>0612</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Packaging</td>
<td>Embossed</td>
<td>Embossed</td>
</tr>
<tr>
<td>Length</td>
<td>mm (in)</td>
<td>(0.032 ± 0.008)</td>
<td>(0.050 ± 0.010)</td>
</tr>
<tr>
<td>Width</td>
<td>mm (in)</td>
<td>(0.063 ± 0.010)</td>
<td>(0.126 ± 0.010)</td>
</tr>
<tr>
<td>Cap Code</td>
<td>WWDC4</td>
<td>A A A A</td>
<td>S S S S V S S S S V</td>
</tr>
<tr>
<td></td>
<td>102</td>
<td>6.3 10 16 25 50</td>
<td>6.3 10 16 25 50</td>
</tr>
<tr>
<td></td>
<td>222</td>
<td>0.81 ± 0.15</td>
<td>1.27 ± 0.25</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(0.0015)</td>
<td>(0.0015)</td>
</tr>
<tr>
<td></td>
<td>332</td>
<td>0.0033</td>
<td>S S S V S S S V</td>
</tr>
<tr>
<td></td>
<td>472</td>
<td>0.0047</td>
<td>S S S V S S S V</td>
</tr>
<tr>
<td></td>
<td>682</td>
<td>0.0068</td>
<td>S S S V S S S V</td>
</tr>
<tr>
<td></td>
<td>103</td>
<td>0.01</td>
<td>S S S V S S S V</td>
</tr>
<tr>
<td></td>
<td>153</td>
<td>0.015</td>
<td>S S S V S S S W</td>
</tr>
<tr>
<td></td>
<td>223</td>
<td>0.022</td>
<td>S S S V S S S W</td>
</tr>
<tr>
<td></td>
<td>333</td>
<td>0.033</td>
<td>S S S V S S S W</td>
</tr>
<tr>
<td></td>
<td>473</td>
<td>0.047</td>
<td>S S S A S S W</td>
</tr>
<tr>
<td></td>
<td>683</td>
<td>0.068</td>
<td>S S S A S S W</td>
</tr>
<tr>
<td></td>
<td>104</td>
<td>0.1</td>
<td>S S S V V A A</td>
</tr>
<tr>
<td></td>
<td>154</td>
<td>0.15</td>
<td>S S V A A A</td>
</tr>
<tr>
<td></td>
<td>224</td>
<td>0.22</td>
<td>S S V V A A</td>
</tr>
<tr>
<td></td>
<td>334</td>
<td>0.33</td>
<td>S S S S S</td>
</tr>
<tr>
<td></td>
<td>474</td>
<td>0.47</td>
<td>S S S S S</td>
</tr>
<tr>
<td></td>
<td>684</td>
<td>0.68</td>
<td>S S S S S</td>
</tr>
<tr>
<td></td>
<td>105</td>
<td>1.0</td>
<td>S S S S S</td>
</tr>
<tr>
<td></td>
<td>155</td>
<td>1.5</td>
<td>S S S S S</td>
</tr>
<tr>
<td></td>
<td>225</td>
<td>2.2</td>
<td>S S S S S</td>
</tr>
<tr>
<td></td>
<td>335</td>
<td>3.3</td>
<td>S S S S S</td>
</tr>
<tr>
<td></td>
<td>475</td>
<td>4.7</td>
<td>S S S S S</td>
</tr>
<tr>
<td></td>
<td>685</td>
<td>6.8</td>
<td>S S S S S</td>
</tr>
<tr>
<td></td>
<td>106</td>
<td>10.0</td>
<td>S S S S S</td>
</tr>
</tbody>
</table>

Solid = X7R  = X5R  = X7S  = X6S

### PAD LAYOUT DIMENSIONS

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0306</th>
<th>0508</th>
<th>0612</th>
</tr>
</thead>
<tbody>
<tr>
<td>Code</td>
<td>Thickness</td>
<td>(0.022)</td>
<td>(0.022)</td>
</tr>
<tr>
<td></td>
<td>Thickness</td>
<td>(0.022)</td>
<td>(0.022)</td>
</tr>
<tr>
<td></td>
<td>Thickness</td>
<td>(0.030)</td>
<td>(0.030)</td>
</tr>
<tr>
<td></td>
<td>Thickness</td>
<td>(0.040)</td>
<td>(0.040)</td>
</tr>
<tr>
<td></td>
<td>Thickness</td>
<td>(0.050)</td>
<td>(0.050)</td>
</tr>
</tbody>
</table>

### PHYSICAL DIMENSIONS

<table>
<thead>
<tr>
<th>SIZE</th>
<th>L (mm)</th>
<th>W (mm)</th>
<th>t (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0306</td>
<td>0.81 ± 0.15</td>
<td>1.27 ± 0.25</td>
<td>0.13 min.</td>
</tr>
<tr>
<td></td>
<td>(0.032 ± 0.008)</td>
<td>(0.050 ± 0.010)</td>
<td>(0.005 min.)</td>
</tr>
<tr>
<td>0508</td>
<td>1.27 ± 0.25</td>
<td>2.00 ± 0.25</td>
<td>0.13 min.</td>
</tr>
<tr>
<td></td>
<td>(0.050 ± 0.010)</td>
<td>(0.080 ± 0.010)</td>
<td>(0.005 min.)</td>
</tr>
<tr>
<td>0612</td>
<td>1.60 ± 0.25</td>
<td>3.20 ± 0.25</td>
<td>0.13 min.</td>
</tr>
<tr>
<td></td>
<td>(0.063 ± 0.010)</td>
<td>(0.126 ± 0.010)</td>
<td>(0.005 min.)</td>
</tr>
</tbody>
</table>

T - See Range Chart for Thickness and Codes

### Pad Layout Dimensions

<table>
<thead>
<tr>
<th>SIZE</th>
<th>A (mm)</th>
<th>B (mm)</th>
<th>C (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0306</td>
<td>0.31 (0.012)</td>
<td>1.52 (0.060)</td>
<td>0.51 (0.020)</td>
</tr>
<tr>
<td>0508</td>
<td>0.51 (0.020)</td>
<td>2.03 (0.080)</td>
<td>0.76 (0.030)</td>
</tr>
<tr>
<td>0612</td>
<td>0.76 (0.030)</td>
<td>3.05 (0.120)</td>
<td>0.635 (0.025)</td>
</tr>
</tbody>
</table>
**Low Inductance Capacitors with SnPb Terminations**

**LD16/LD17/LD18 Tin-Lead Termination “B”**

**GENERAL DESCRIPTION**

The key physical characteristic determining equivalent series inductance (ESL) of a capacitor is the size of the current loop it creates. The smaller the current loop, the lower the ESL.

A standard surface mount MLCC is rectangular in shape with electrical terminations on its shorter sides. A Low Inductance Chip Capacitor (LICC) sometimes referred to as Reverse Geometry Capacitor (RGC) has its terminations on the longer sides of its rectangular shape. The image on the right shows the termination differences between an MLCC and an LICC.

When the distance between terminations is reduced, the size of the current loop is reduced. Since the size of the current loop is the primary driver of inductance, an 0306 with a smaller current loop has significantly lower ESL than an 0603. The reduction in ESL varies by EIA size, however, ESL is typically reduced 60% or more with a LICC versus a standard MLCC.

AVX LICC products are available with a lead termination for high reliability military and aerospace applications that must avoid tin whisker reliability issues.

**HOW TO ORDER**

<table>
<thead>
<tr>
<th>LD18</th>
<th>Z</th>
<th>D</th>
<th>105</th>
<th>M</th>
<th>A</th>
<th>B</th>
<th>2</th>
<th>A*</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>Voltage</td>
<td>Dielectric</td>
<td>Capacitance Code (in pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Terminations</td>
<td>Packaging</td>
<td>Thickness</td>
</tr>
<tr>
<td>LD16 = 0306</td>
<td>4 = 4V</td>
<td>C = X7R</td>
<td>K = ±10%; M = ±20%</td>
<td>A = N/A</td>
<td>B = 5% min lead</td>
<td>Available</td>
<td>Thickness (mm (in))</td>
<td></td>
</tr>
<tr>
<td>LD17 = 0308</td>
<td>6 = 6.3V</td>
<td>D = X5R</td>
<td></td>
<td></td>
<td></td>
<td>2 = 7&quot; Reel; 4 = 13&quot; Reel</td>
<td>0.56 (0.022)</td>
<td></td>
</tr>
<tr>
<td>LD18 = 0612</td>
<td>Z = 10V</td>
<td>W = X6S</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>0.76 (0.030)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Y = 16V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>1.02 (0.040)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3 = 25V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>1.27 (0.050)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>5 = 50V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*See the thickness tables on the next page.

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

**PERFORMANCE CHARACTERISTICS**

<table>
<thead>
<tr>
<th>Capacitance Tolerances</th>
<th>K = ±10%; M = ±20%</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operation</td>
<td>X7R = -55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Range</td>
<td>X5R = -55°C to +85°C</td>
</tr>
<tr>
<td></td>
<td>X7S = -55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Coefficient</td>
<td>X7R, X5R = ±15%; X7S = ±22%</td>
</tr>
<tr>
<td>Voltage Ratings</td>
<td>4, 6.3, 10, 16, 25 VDC</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>4V, 6.3V = 6.5% max; 10V = 5.0% max; 16V = 3.5% max; 25V = 3.0% max</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>100,000MΩ min, or 1,000MΩ per μF min., whichever is less</td>
</tr>
</tbody>
</table>

**TYPICAL IMPEDANCE CHARACTERISTICS**

---

**Not RoHS Compliant**
Low Inductance Capacitors with SnPb Terminations

LD16/LD17/LD18 Tin-Lead Termination “B”

### PHYSICAL DIMENSIONS AND PAD LAYOUT

<table>
<thead>
<tr>
<th>SIZE</th>
<th>LD16 (0306)</th>
<th>LD17 (0508)</th>
<th>LD18 (0612)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Packaging</td>
<td>Embossed</td>
<td>Embossed</td>
<td>Embossed</td>
</tr>
<tr>
<td>Length (mm)</td>
<td>0.81 ± 0.15 ( (0.032 ± 0.006) )</td>
<td>1.27 ± 0.25 ( (0.050 ± 0.010) )</td>
<td>1.60 ± 0.25 ( (0.063 ± 0.010) )</td>
</tr>
<tr>
<td>Width (mm)</td>
<td>1.60 ± 0.15 ( (0.063 ± 0.006) )</td>
<td>2.00 ± 0.25 ( (0.080 ± 0.010) )</td>
<td>3.20 ± 0.28 ( (0.126 ± 0.010) )</td>
</tr>
<tr>
<td>Cap Code</td>
<td>WVDC</td>
<td>WVDC</td>
<td>WVDC</td>
</tr>
<tr>
<td>Value</td>
<td>0.001</td>
<td>0.006</td>
<td>0.015</td>
</tr>
<tr>
<td>Value</td>
<td>0.0022</td>
<td>0.0068</td>
<td>0.01</td>
</tr>
<tr>
<td>Value</td>
<td>0.0033</td>
<td>0.015</td>
<td>0.022</td>
</tr>
<tr>
<td>Value</td>
<td>0.0047</td>
<td>0.0088</td>
<td>0.033</td>
</tr>
<tr>
<td>Value</td>
<td>0.047</td>
<td>0.068</td>
<td>0.09</td>
</tr>
<tr>
<td>Value</td>
<td>0.1</td>
<td>0.15</td>
<td>0.22</td>
</tr>
<tr>
<td>Value</td>
<td>0.33</td>
<td>0.47</td>
<td>0.68</td>
</tr>
<tr>
<td>Value</td>
<td>0.68</td>
<td>1</td>
<td>1.5</td>
</tr>
<tr>
<td>Value</td>
<td>2.2</td>
<td>3.3</td>
<td>4.7</td>
</tr>
<tr>
<td>Value</td>
<td>6.8</td>
<td>10</td>
<td>10</td>
</tr>
</tbody>
</table>

#### PHYSICAL DIMENSIONS

<table>
<thead>
<tr>
<th>mm (in)</th>
<th>L</th>
<th>W</th>
<th>t</th>
</tr>
</thead>
<tbody>
<tr>
<td>LD16 (0306)</td>
<td>0.81 ± 0.15 ( (0.032 ± 0.006) )</td>
<td>1.60 ± 0.15 ( (0.063 ± 0.006) )</td>
<td>0.13 min. ( (0.005 \text{ min.}) )</td>
</tr>
<tr>
<td>LD17 (0508)</td>
<td>1.27 ± 0.25 ( (0.050 ± 0.010) )</td>
<td>2.00 ± 0.25 ( (0.080 ± 0.010) )</td>
<td>0.13 min. ( (0.005 \text{ min.}) )</td>
</tr>
<tr>
<td>LD18 (0612)</td>
<td>1.60 ± 0.25 ( (0.063 ± 0.010) )</td>
<td>3.20 ± 0.28 ( (0.126 ± 0.010) )</td>
<td>0.13 min. ( (0.005 \text{ min.}) )</td>
</tr>
</tbody>
</table>

T - See Range Chart for Thickness and Codes

#### PAD LAYOUT DIMENSIONS

<table>
<thead>
<tr>
<th>mm (in)</th>
<th>A</th>
<th>B</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>LD16 (0306)</td>
<td>0.31 (0.012)</td>
<td>1.52 (0.060)</td>
<td>0.51 (0.020)</td>
</tr>
<tr>
<td>LD17 (0508)</td>
<td>0.51 (0.020)</td>
<td>2.03 (0.080)</td>
<td>0.76 (0.030)</td>
</tr>
<tr>
<td>LD18 (0612)</td>
<td>0.76 (0.030)</td>
<td>3.05 (0.120)</td>
<td>0.635 (0.025)</td>
</tr>
</tbody>
</table>

Solid = X7R

= X5R

= X6S

---

041416

81
GENERAL DESCRIPTION

Inter-Digitated Capacitors (IDCs) are used for both semiconductor package and board level decoupling. The equivalent series inductance (ESL) of a single capacitor or an array of capacitors in parallel determines the response time of a Power Delivery Network (PDN). The lower the ESL of a PDN, the faster the response time. A designer can use many standard MLCCs in parallel to reduce ESL or a low ESL Inter-Digitated Capacitor (IDC) device. These IDC devices are available in versions with a maximum height of 0.95mm or 0.55mm.

IDCs are typically used on packages of semiconductor products with power levels of 15 watts or greater. Inter-Digitated Capacitors are used on CPU, GPU, ASIC, and ASSP devices produced on 0.13μ, 90nm, 65nm, and 45nm processes. IDC devices are used on both ceramic and organic package substrates. These low ESL surface mount capacitors can be placed on the bottom side or the top side of a package substrate. The low profile 0.55mm maximum height IDCs can easily be used on the bottom side of BGA packages or on the die side of packages under a heat spreader.

IDCs are used for board level decoupling of systems with speeds of 300MHz or greater. Low ESL IDCs free up valuable board space by reducing the number of capacitors required versus standard MLCCs. There are additional benefits to reducing the number of capacitors beyond saving board space including higher reliability from a reduction in the number of components and lower placement costs based on the need for fewer capacitors.

The Inter-Digitated Capacitor (IDC) technology was developed by AVX. This is the second family of Low Inductance MLCC products created by AVX. IDCs are a cost effective alternative to AVX’s first generation low ESL family for high-reliability applications known as LIC (Low Inductance Chip Array).

AVX IDC products are available with a lead-free finish of plated Nickel/Tin.

HOW TO ORDER

<table>
<thead>
<tr>
<th>W</th>
<th>3</th>
<th>L</th>
<th>1</th>
<th>6</th>
<th>D</th>
<th>225</th>
<th>M</th>
<th>A</th>
<th>T</th>
<th>3</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Style</td>
<td>0306/0612/0508 IDC (InterDigitated Capacitors)</td>
<td>Low Inductance</td>
<td>Number of Terminals</td>
<td>Voltage</td>
<td>Dielectric Code (In pF)</td>
<td>Capacitance Tolerance</td>
<td>Capacitance Failure Rate</td>
<td>Termination</td>
<td>Packaging</td>
<td>Thickness</td>
<td></td>
</tr>
<tr>
<td>Case Size</td>
<td>2 = 0508</td>
<td>3 = 0612</td>
<td>4 = 0306</td>
<td>4 = 4V</td>
<td>6 = 6.3V</td>
<td>2 Sig. Digits</td>
<td>±20%</td>
<td>A = N/A</td>
<td>1=7” Reel</td>
<td>Max. Thickness mm (in.)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>6</td>
<td>10V</td>
<td>Number of Zeros</td>
<td></td>
<td></td>
<td>3=13” Reel</td>
<td>A=Standard</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Z</td>
<td>16V</td>
<td></td>
<td></td>
<td></td>
<td>S=0.55 (0.022)</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>3</td>
<td>25V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

PERFORMANCE CHARACTERISTICS

<table>
<thead>
<tr>
<th>Capacitance Tolerance</th>
<th>±20% Preferred</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operation Temperature Range</td>
<td>X7R = -55°C to +125°C</td>
</tr>
<tr>
<td></td>
<td>X5R = -55°C to +85°C</td>
</tr>
<tr>
<td></td>
<td>X7S = -55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Coefficient</td>
<td>±15% (UVDC), ±22% (X7S)</td>
</tr>
<tr>
<td>Voltage Ratings</td>
<td>4, 6.3, 10, 16, 25 VDC</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ 6.3V = 6.5% max; 10V = 5.0% max; ≥ 16V = 3.5% max</td>
</tr>
<tr>
<td>Insulation Resistance (@+25°C, RVDC)</td>
<td>100,000MΩ min, or 1,000MΩ per µF min., whichever is less</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>No problems observed after 2.5 x RVDC for 5 seconds at 50mA max current</td>
</tr>
<tr>
<td>CTE (ppm/C)</td>
<td>12.0</td>
</tr>
<tr>
<td>Thermal Conductivity</td>
<td>4-5W/M K</td>
</tr>
<tr>
<td>Terminations Available</td>
<td>Plated Nickel and Solder</td>
</tr>
</tbody>
</table>
## IDC Low Inductance Capacitors (RoHS)

### 0306/0612/0508 IDC (InterDigitated Capacitors)

<table>
<thead>
<tr>
<th>SIZE</th>
<th>W4 = 0306</th>
<th>W2 = Thin 0508</th>
<th>W2 = 0508</th>
<th>W3 = Thin 0612</th>
<th>W3 = 0612</th>
<th>W3 = THICK 0612</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max Thickness (mm)</td>
<td>0.55 (0.022)</td>
<td>0.55 (0.022)</td>
<td>0.65 (0.026)</td>
<td>0.65 (0.026)</td>
<td>0.65 (0.026)</td>
<td>0.85 (0.033)</td>
</tr>
<tr>
<td>WVDC</td>
<td>4</td>
<td>6.3</td>
<td>4</td>
<td>6.3</td>
<td>10</td>
<td>16</td>
</tr>
<tr>
<td>Cap (μF)</td>
<td>0.010</td>
<td>0.022</td>
<td>0.033</td>
<td>0.047</td>
<td>0.068</td>
<td>0.10</td>
</tr>
<tr>
<td>= X7R</td>
<td>= X5R</td>
<td>= X7S</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Consult factory for additional requirements.

### PHYSICAL DIMENSIONS AND PAD LAYOUT

**PHYSICAL CHIP DIMENSIONS** millimeters (inches)

<table>
<thead>
<tr>
<th>SIZE</th>
<th>W</th>
<th>L</th>
<th>BW</th>
<th>BL</th>
<th>P</th>
</tr>
</thead>
<tbody>
<tr>
<td>0306</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
<td>0.82 ± 0.10 (0.032 ± 0.006)</td>
<td>0.25 ± 0.10 (0.010 ± 0.004)</td>
<td>0.20 ± 0.10 (0.008 ± 0.004)</td>
<td>0.40 ± 0.05 (0.015 ± 0.002)</td>
</tr>
<tr>
<td>0508</td>
<td>2.03 ± 0.20 (0.080 ± 0.008)</td>
<td>1.27 ± 0.20 (0.050 ± 0.008)</td>
<td>0.30 ± 0.10 (0.012 ± 0.004)</td>
<td>0.25 ± 0.15 (0.010 ± 0.006)</td>
<td>0.50 ± 0.05 (0.020 ± 0.002)</td>
</tr>
<tr>
<td>0612</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
<td>0.50 ± 0.10 (0.020 ± 0.004)</td>
<td>0.25 ± 0.15 (0.010 ± 0.006)</td>
<td>0.80 ± 0.10 (0.031 ± 0.004)</td>
</tr>
</tbody>
</table>

**PAD LAYOUT DIMENSIONS**
IDC Low Inductance Capacitors (SnPb)

0306/0612/0508 IDC with Sn/Pb Termination

GENERAL DESCRIPTION

Inter-Digitated Capacitors (IDCs) are used for both semiconductor package and board level decoupling. The equivalent series inductance (ESL) of a single capacitor or an array of capacitors in parallel determines the response time of a Power Delivery Network (PDN). The lower the ESL of a PDN, the faster the response time. A designer can use many standard MLCCs in parallel to reduce ESL or a low ESL Inter-Digitated Capacitor (IDC) device. These IDC devices are available in versions with a maximum height of 0.95mm or 0.55mm.

IDCs are typically used on packages of semiconductor products with power levels of 15 watts or greater. Inter-Digitated Capacitors are used on CPU, GPU, ASIC, and ASSP devices produced on 0.13μ, 90nm, 65nm, and 45nm processes. IDC devices are used on both ceramic and organic package substrates. These low ESL surface mount capacitors can be placed on the bottom side or the top side of a package substrate. The low profile 0.55mm maximum height IDCs can easily be used on the bottom side of BGA packages or on the die side of packages under a heat spreader.

IDCs are used for board level decoupling of systems with speeds of 300MHz or greater. Low ESL IDCs free up valuable board space by reducing the number of capacitors required versus standard MLCCs. There are additional benefits to reducing the number of capacitors beyond saving board space including higher reliability from a reduction in the number of components and lower placement costs based on the need for fewer capacitors.

The Inter-Digitated Capacitor (IDC) technology was developed by AVX. This is the second family of Low Inductance MLCC products created by AVX. IDCs are a cost effective alternative to AVX’s first generation low ESL family for high-reliability applications known as LICA (Low Inductance Chip Array).

AVX IDC products are available with a lead termination for high reliability military and aerospace applications that must avoid tin whisker reliability issues.

HOW TO ORDER

<table>
<thead>
<tr>
<th>L</th>
<th>3</th>
<th>6</th>
<th>D</th>
<th>225</th>
<th>M</th>
<th>A</th>
<th>B</th>
<th>3</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Style</td>
<td>IDC Case Size</td>
<td>Voltage</td>
<td>Dielectric Code</td>
<td>Capacitance Code (In pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Termination</td>
<td>Packaging Available</td>
<td>Thickness Max. Thickness</td>
</tr>
<tr>
<td>2</td>
<td>0508</td>
<td>4V</td>
<td>X7R</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>±20%</td>
<td>A</td>
<td>B = 5%</td>
<td>1=7” Reel</td>
<td>mm (in.)</td>
</tr>
<tr>
<td>3</td>
<td>0612</td>
<td>6V</td>
<td>X5R</td>
<td>3</td>
<td>= 16V</td>
<td>A = N/A</td>
<td>A</td>
<td>3=13” Reel</td>
<td>A=Standard</td>
</tr>
<tr>
<td>4</td>
<td>0306</td>
<td>6V</td>
<td>Z</td>
<td>4</td>
<td>= 10V</td>
<td></td>
<td></td>
<td></td>
<td>S=0.55 (0.022)</td>
</tr>
</tbody>
</table>

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

PERFORMANCE CHARACTERISTICS

<table>
<thead>
<tr>
<th>Capacitance Tolerance</th>
<th>±20% Preferred</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operation Temperature Range</td>
<td>X7R = -55°C to +125°C</td>
</tr>
<tr>
<td></td>
<td>X5R = -55°C to +85°C</td>
</tr>
<tr>
<td></td>
<td>X7S = -55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Coefficient</td>
<td>±15% (VDC), ±22% (X7S)</td>
</tr>
<tr>
<td>Voltage Ratings</td>
<td>4, 6.3, 10, 16, 25 VDC</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ 6.3V = 6.5% max;</td>
</tr>
<tr>
<td></td>
<td>10V = 5.0% max;</td>
</tr>
<tr>
<td></td>
<td>≥ 16V = 3.5% max</td>
</tr>
<tr>
<td>Insulation Resistance (@+25°C, RVDC)</td>
<td>100,000MΩ min, or 1,000MΩ per μF min., whichever is less</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>No problems observed after 2.5 x RVDC for 5 seconds at 50mA max current</td>
</tr>
<tr>
<td>CTE (ppm/C)</td>
<td>12.0</td>
</tr>
<tr>
<td>Thermal Conductivity</td>
<td>4-5W/M K</td>
</tr>
<tr>
<td>Terminations Available</td>
<td>Plated Nickel and Solder</td>
</tr>
</tbody>
</table>
### IDC Low Inductance Capacitors (SnPb)

#### 0306/0612/0508 IDC with Sn/Pb Termination

**PAD LAYOUT**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>W4 = 0306</th>
<th>W2 = Thin 0508</th>
<th>W2 = 0508</th>
<th>W3= Thin 0612</th>
<th>W3 = 0612</th>
<th>W3 = THICK 0612</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max Thickness (in.)</td>
<td>0.55</td>
<td>0.55</td>
<td>0.85</td>
<td>0.35</td>
<td>0.85</td>
<td>1.22</td>
</tr>
<tr>
<td>WDC</td>
<td>4</td>
<td>6.3</td>
<td>4</td>
<td>6.3</td>
<td>4</td>
<td>6.3</td>
</tr>
<tr>
<td>Cap (μF)</td>
<td>0.010</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.022</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.033</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.047</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.068</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.10</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.22</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.33</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.47</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.68</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1.5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2.2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>3.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**PHYSICAL DIMENSIONS AND PAD LAYOUT**

Consult factory for additional requirements.

- **= X7R**
- **= X5R**
- **= X7S**

**PHYSICAL CHIP DIMENSIONS**  millimeters (inches)

<table>
<thead>
<tr>
<th>SIZE</th>
<th>W</th>
<th>L</th>
<th>BW</th>
<th>BL</th>
<th>P</th>
</tr>
</thead>
<tbody>
<tr>
<td>0306</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
<td>0.82 ± 0.10 (0.032 ± 0.006)</td>
<td>0.25 ± 0.10 (0.010 ± 0.004)</td>
<td>0.20 ± 0.10 (0.008 ± 0.004)</td>
<td>0.40 ± 0.05 (0.015 ± 0.002)</td>
</tr>
<tr>
<td>0508</td>
<td>2.03 ± 0.20 (0.080 ± 0.008)</td>
<td>1.27 ± 0.20 (0.050 ± 0.008)</td>
<td>0.30 ± 0.10 (0.012 ± 0.004)</td>
<td>0.25 ± 0.15 (0.010 ± 0.006)</td>
<td>0.50 ± 0.05 (0.020 ± 0.002)</td>
</tr>
<tr>
<td>0612</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
<td>0.50 ± 0.10 (0.020 ± 0.004)</td>
<td>0.25 ± 0.15 (0.010 ± 0.006)</td>
<td>0.80 ± 0.10 (0.031 ± 0.004)</td>
</tr>
</tbody>
</table>

**PAD LAYOUT DIMENSIONS**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
<th>E</th>
</tr>
</thead>
<tbody>
<tr>
<td>0306</td>
<td>0.38 (0.015)</td>
<td>0.89 (0.035)</td>
<td>1.27 (0.050)</td>
<td>0.20 (0.008)</td>
<td>0.40 (0.015)</td>
</tr>
<tr>
<td>0508</td>
<td>0.64 (0.025)</td>
<td>1.27 (0.050)</td>
<td>1.91 (0.075)</td>
<td>0.28 (0.011)</td>
<td>0.50 (0.020)</td>
</tr>
<tr>
<td>0612</td>
<td>0.89 (0.035)</td>
<td>1.65 (0.065)</td>
<td>2.54 (0.101)</td>
<td>0.45 (0.018)</td>
<td>0.80 (0.031)</td>
</tr>
</tbody>
</table>
Land Grid Array (LGA) capacitors are the latest family of low inductance MLCCs from AVX. These new LGA products are the third low inductance family developed by AVX. The innovative LGA technology sets a new standard for low inductance MLCC performance. Our initial 2 terminal versions of LGA technology deliver the performance of an 8 terminal IDC low inductance MLCC with a number of advantages including:

- Simplified layout of 2 large solder pads compared to 8 small pads for IDCs
- Opportunity to reduce PCB or substrate contribution to system ESL by using multiple parallel vias in solder pads
- Advanced FCT manufacturing process used to create uniformly flat terminations on the capacitor that resist “tombstoning”
- Better solder joint reliability

APPLICATIONS

Semiconductor Packages
- Microprocessors/CPUs
- Graphics Processors/GPUs
- Chipsets
- FPGAs
- ASICs

Board Level Device Decoupling
- Frequencies of 300 MHz or more
- ICs drawing 15W or more
- Low voltages
- High speed buses

0306 2 TERMINAL LGA COMPARISON WITH 0306 8 TERMINAL IDC
### LGA Low Inductance Capacitors

**0204/0306 Land Grid Array**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>LG12 (0204)</th>
<th>LG22 (0306)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Length</strong> (mm [in.])</td>
<td>0.50 (0.020)</td>
<td>0.76 (0.030)</td>
</tr>
<tr>
<td><strong>Width</strong> (mm [in.])</td>
<td>1.00 (0.039)</td>
<td>1.60 (0.063)</td>
</tr>
<tr>
<td><strong>Temp. Char.</strong></td>
<td>X5R (D)</td>
<td>X7S (Z)</td>
</tr>
<tr>
<td>Working Voltage</td>
<td>6.3 (4)</td>
<td>6.3 (4)</td>
</tr>
<tr>
<td>Cap (μF)</td>
<td>0.010 (103)</td>
<td>0.010 (103)</td>
</tr>
<tr>
<td></td>
<td>0.022 (223)</td>
<td>0.022 (223)</td>
</tr>
<tr>
<td></td>
<td>0.047 (472)</td>
<td>0.047 (472)</td>
</tr>
<tr>
<td></td>
<td>0.100 (104)</td>
<td>0.100 (104)</td>
</tr>
<tr>
<td></td>
<td>0.220 (224)</td>
<td>0.220 (224)</td>
</tr>
<tr>
<td></td>
<td>0.330 (334)</td>
<td>0.330 (334)</td>
</tr>
<tr>
<td></td>
<td>0.470 (474)</td>
<td>0.470 (474)</td>
</tr>
<tr>
<td></td>
<td>1.000 (105)</td>
<td>1.000 (105)</td>
</tr>
<tr>
<td></td>
<td>2.200 (225)</td>
<td>2.200 (225)</td>
</tr>
</tbody>
</table>

#### HOW TO ORDER

<table>
<thead>
<tr>
<th>LG</th>
<th>1</th>
<th>2</th>
<th>6</th>
<th>Z</th>
<th>104</th>
<th>M</th>
<th>A</th>
<th>T</th>
<th>2</th>
<th>S</th>
<th>1</th>
</tr>
</thead>
<tbody>
<tr>
<td>Style</td>
<td>Case Size</td>
<td>Number of Terminals</td>
<td>Working Voltage</td>
<td>Temperature Characteristic</td>
<td>Coded Cap</td>
<td>Cap Tolerance</td>
<td>M = 20%</td>
<td>Termination Style</td>
<td>A = &quot;U&quot; Land</td>
<td>Termination 100% Sn*</td>
<td>Packaging Tape &amp; Reel</td>
</tr>
<tr>
<td>1 = 0204</td>
<td>2 = 0306</td>
<td>4 = 4V</td>
<td>6 = 6.3V</td>
<td>2 = 10V</td>
<td>X7R</td>
<td>D = X5R</td>
<td>Z = X7S</td>
<td>W = X6S</td>
<td>Contact factory for other termination finishes</td>
<td>2 = 7&quot; Reel</td>
<td>4 = 13&quot; Reel</td>
</tr>
</tbody>
</table>

#### PART DIMENSIONS

<table>
<thead>
<tr>
<th>Series</th>
<th>L (mm)</th>
<th>W (mm)</th>
<th>T (mm)</th>
<th>BW (mm)</th>
<th>BL (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LG12 (0204)</td>
<td>0.50 ± 0.05 (0.020±0.002)</td>
<td>1.00 ± 0.10 (0.039 ± 0.004)</td>
<td>0.50 ± 0.05 (0.020 ± 0.002)</td>
<td>0.8 ± 0.10 (0.031 ± 0.004)</td>
<td>0.13 ± 0.08 (0.005 ± 0.003)</td>
</tr>
<tr>
<td>LG22 (0306)</td>
<td>0.76 ± 0.10 (0.030 ± 0.004)</td>
<td>1.60 ± 0.10 (0.063 ± 0.004)</td>
<td>0.50 ± 0.05 (0.020 ± 0.002)</td>
<td>1.50 ± 0.10 (0.059 ± 0.004)</td>
<td>0.28 ± 0.08 (0.011 ± 0.003)</td>
</tr>
</tbody>
</table>

#### RECOMMENDED SOLDER PAD DIMENSIONS

<table>
<thead>
<tr>
<th>Series</th>
<th>PL (mm)</th>
<th>PW1 (mm)</th>
<th>G (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LG12 (0204)</td>
<td>0.50 (0.020)</td>
<td>1.00 (0.039)</td>
<td>0.20 (0.008)</td>
</tr>
<tr>
<td>LG22 (0306)</td>
<td>0.65 (0.026)</td>
<td>1.50 (0.059)</td>
<td>0.20 (0.008)</td>
</tr>
</tbody>
</table>

*Contact factory for other termination finishes*
### LGA Low Inductance Capacitors

**0204/0306 Land Grid Array – Tin/Lead Termination “B”**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>PG12 (0204)</th>
<th>PG22 (0306)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Length (mm)</td>
<td>0.50 (0.020)</td>
<td>0.76 (0.030)</td>
</tr>
<tr>
<td>Width (mm)</td>
<td>1.00 (0.039)</td>
<td>1.60 (0.063)</td>
</tr>
<tr>
<td>Temp. Char.</td>
<td>X5R (D)</td>
<td>X7S (Z)</td>
</tr>
<tr>
<td>Working Voltage</td>
<td>6.3 (4)</td>
<td>6.3 (4)</td>
</tr>
<tr>
<td>Cap (μF)</td>
<td>0.010 (103)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.022 (222)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.047 (472)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.100 (104)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.220 (224)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.330 (334)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.470 (476)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1.000 (105)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2.200 (225)</td>
<td></td>
</tr>
</tbody>
</table>

*Not RoHS Compliant*

### HOW TO ORDER

<table>
<thead>
<tr>
<th>PG</th>
<th>1</th>
<th>2</th>
<th>6</th>
<th>Z</th>
<th>104</th>
<th>M</th>
<th>A</th>
<th>B</th>
<th>2</th>
<th>S</th>
<th>1</th>
</tr>
</thead>
<tbody>
<tr>
<td>Style</td>
<td>Case Size</td>
<td>Number of Terminals</td>
<td>Working Voltage</td>
<td>Temperature Characteristic</td>
<td>Coded Cap</td>
<td>Cap Tolerance</td>
<td>Termination Style</td>
<td>Termination 5% Min Lead</td>
<td>Packaging Tape &amp; Reel</td>
<td>Thickness S = 0.55mm max</td>
<td>Number of Capacitors</td>
</tr>
<tr>
<td>1 = 0204</td>
<td>2 = 0306</td>
<td>2</td>
<td>4 = 4V</td>
<td>6 = 6.3V</td>
<td>Z = 10V</td>
<td>C = X7R</td>
<td>D = X5R</td>
<td>W = X6S</td>
<td>2 = 7&quot; Reel</td>
<td>4 = 13&quot; Reel</td>
<td></td>
</tr>
</tbody>
</table>

**PART DIMENSIONS** (mm (inches))

<table>
<thead>
<tr>
<th>Series</th>
<th>L</th>
<th>W</th>
<th>T</th>
<th>BW</th>
<th>BL</th>
</tr>
</thead>
<tbody>
<tr>
<td>PG12 (0204)</td>
<td>0.5 ± 0.05 (0.020±0.002)</td>
<td>1.00 ± 0.10 (0.039±0.004)</td>
<td>0.50 ± 0.05 (0.020±0.002)</td>
<td>0.8 ± 0.10 (0.031±0.004)</td>
<td>0.13 ± 0.08 (0.005±0.003)</td>
</tr>
<tr>
<td>PG22 (0306)</td>
<td>0.76 ± 0.10 (0.030±0.004)</td>
<td>1.60 ± 0.10 (0.063±0.004)</td>
<td>0.50 ± 0.05 (0.020±0.002)</td>
<td>1.50 ± 0.10 (0.059±0.004)</td>
<td>0.28 ± 0.08 (0.011±0.003)</td>
</tr>
</tbody>
</table>

**RECOMMENDED SOLDER PAD DIMENSIONS** (mm (inches))

<table>
<thead>
<tr>
<th>Series</th>
<th>PL</th>
<th>PW1</th>
<th>G</th>
</tr>
</thead>
<tbody>
<tr>
<td>PG12 (0204)</td>
<td>0.50 (0.020)</td>
<td>1.00 (0.039)</td>
<td>0.20 (0.008)</td>
</tr>
<tr>
<td>PG22 (0306)</td>
<td>0.65 (0.026)</td>
<td>1.50 (0.059)</td>
<td>0.20 (0.008)</td>
</tr>
</tbody>
</table>

**Reverse Geometry LGA**

- **PG12, PG22**

---

*AVX*
AT Series
High Temperature MLCC – 200ºC & 250ºC Rated

Present military specifications, as well as a majority of commercial applications, require a maximum operating temperature of 125°C. However, the emerging market for high temperature electronics demands capacitors operating reliably at temperatures beyond 125°C. AVX’s high temperature chip capacitor product line, has been extended with the BME C0G chip. All AT chips have verified capabilities of long term operation up to 250°C for applications in both military and commercial businesses. These capacitors demonstrate high volumetric efficiency, high insulation resistance and low ESR/ESL for the most demanding applications, such as “down-hole” oil exploration and aerospace programs.

HOW TO ORDER

ELECTRICAL SPECIFICATIONS

Temperature Coefficient
PME C0G ±30ppm/°C, -55C to 200°C
BME C0G ±30ppm/°C, -55C to 200°C

Capacitance Test (MIL-STD-202, Method 305)
25°C, 1.0 ± 0.2 Vrms (open circuit voltage) @ 1kHz
Dissipation factor 25°C
COG: 0.15% Max at 1.0 ± 0.2 Vrms (open circuit voltage) @ 1kHz
VHT: 2.5% Max at 1.0 ± 0.2 Vrms (open circuit voltage) @ 1kHz

Insulation Resistance 25°C (MIL-STD-202, Method 302)
100GΩ or 1000MΩ-μF (whichever is less)

Direct Withstanding Voltage 25°C (Flash Test)
250% rated voltage for 5 seconds with 50mA max charging current

DIMENSIONS

millimeters (inches)

<table>
<thead>
<tr>
<th>Size</th>
<th>AT03 = 0603</th>
<th>AT05 = 0805</th>
<th>AT06 = 1206</th>
<th>AT10 = 1210</th>
<th>AT12 = 1812</th>
<th>AT14 = 2225</th>
</tr>
</thead>
<tbody>
<tr>
<td>(L) Length</td>
<td>1.60 ± 0.15 (0.063 ± 0.006)</td>
<td>2.01 ± 0.20 (0.079 ± 0.008)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>4.50 ± 0.30 (0.177 ± 0.012)</td>
<td>5.72 ± 0.25 (0.225 ± 0.010)</td>
</tr>
<tr>
<td>(W) Width</td>
<td>0.81 ± 0.15 (0.032 ± 0.006)</td>
<td>1.25 ± 0.20 (0.049 ± 0.008)</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
<td>2.50 ± 0.20 (0.098 ± 0.008)</td>
<td>3.20 ± 0.20 (0.126 ± 0.008)</td>
<td>6.35 ± 0.25 (0.250 ± 0.010)</td>
</tr>
<tr>
<td>(T) Thickness Max.</td>
<td>1.02 (0.040)</td>
<td>1.30 (0.051)</td>
<td>1.52 (0.060)</td>
<td>1.70 (0.067)</td>
<td>2.54 (0.100)</td>
<td>2.54 (0.100)</td>
</tr>
<tr>
<td>(t) terminal min.</td>
<td>0.25 (0.010)</td>
<td>0.25 (0.010)</td>
<td>0.25 (0.010)</td>
<td>0.25 (0.010)</td>
<td>0.25 (0.010)</td>
<td>0.25 (0.010)</td>
</tr>
<tr>
<td></td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
</tr>
<tr>
<td></td>
<td>(0.040)</td>
<td>(0.040)</td>
<td>(0.040)</td>
<td>(0.040)</td>
<td>(0.040)</td>
<td>(0.040)</td>
</tr>
</tbody>
</table>
AT Series
High Temperature MLCC – 200°C & 250°C Rated

PERFORMANCE CHARACTERISTICS

Typical Temperature Coefficient of Capacitance (VHT Dielectric)

Typical Temperature Coefficient of Capacitance (COG Dielectric)

Typical Voltage Coefficient of Capacitance (VHT Dielectric)

Typical Voltage Coefficient of Capacitance (COG Dielectric)

Typical RC vs Temperature (VHT Dielectric)

Typical RC vs Temperature (COG Dielectric)
AT Series
High Temperature MLCC – 200°C & 250°C Rated

RELIABILITY

FREQUENCY RESPONSE

250°C Life Test @ 2x Rated Voltage (VHT Dielectric)

250°C Life Test @ 2x Rated Voltage (C0G Dielectric)

VHT - Failure Rate @ 90% Confidence Level (%/1000 hours)

<table>
<thead>
<tr>
<th>Temperature (°C)</th>
<th>50% Rated Voltage</th>
<th>100% Rated Voltage</th>
</tr>
</thead>
<tbody>
<tr>
<td>200</td>
<td>0.002</td>
<td>0.017</td>
</tr>
<tr>
<td>250</td>
<td>0.026</td>
<td>0.210</td>
</tr>
</tbody>
</table>

*C Typical 1210, 1812, 2225 Failure Rate Analysis based on 250°C testing and voltage ratings specified on the following page.

C0G - Failure Rate @ 90% Confidence Level (%/1000 hours)

<table>
<thead>
<tr>
<th>Temperature (°C)</th>
<th>50% Rated Voltage</th>
<th>100% Rated Voltage</th>
</tr>
</thead>
<tbody>
<tr>
<td>200</td>
<td>0.006</td>
<td>0.047</td>
</tr>
<tr>
<td>250</td>
<td>0.074</td>
<td>0.390</td>
</tr>
</tbody>
</table>

*C Typical 1812 and 2225 Failure Rate Analysis based on 250°C testing and voltage ratings specified on the following page.
# AT Series

## High Temperature MLCC – 200°C & 250°C Rated

### CAPACITANCE RANGE

**PREFERRED SIZES ARE SHADED**

<table>
<thead>
<tr>
<th>Case Size</th>
<th>AT03 = 0603</th>
<th>AT05 = 0805</th>
<th>AT06 = 1206</th>
<th>AT10 = 1210</th>
<th>AT12 = 1812</th>
<th>AT14 = 2225</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>VHT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Temp. Coefficient:</td>
<td>4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>200°C Rated</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>1.60 ± 0.15</td>
<td>2.01 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>4.55 ± 0.30</td>
<td>4.55 ± 0.30</td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
<td>1.60 ± 0.20</td>
<td>1.60 ± 0.20</td>
<td>2.50 ± 0.20</td>
<td>2.50 ± 0.20</td>
</tr>
<tr>
<td>(T) Thickness (mm)</td>
<td>1.02</td>
<td>1.30</td>
<td>1.52</td>
<td>1.70</td>
<td>2.54</td>
<td>2.54</td>
</tr>
<tr>
<td>(t) Terminal (mm)</td>
<td>0.25 (0.010)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>1.02 (0.040)</td>
<td>1.02 (0.040)</td>
</tr>
<tr>
<td>Rated Temp. (ºC)</td>
<td>200</td>
<td>200</td>
<td>200</td>
<td>200</td>
<td>200</td>
<td>200</td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>25</td>
<td>25</td>
<td>50</td>
<td>50</td>
<td>50</td>
<td>50</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Case Size</th>
<th>AT03 = 0603</th>
<th>AT05 = 0805</th>
<th>AT06 = 1206</th>
<th>AT10 = 1210</th>
<th>AT12 = 1812</th>
<th>AT14 = 2225</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>VHT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Temp. Coefficient:</td>
<td>T</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>250°C Rated</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>1.60 ± 0.15</td>
<td>2.01 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>4.55 ± 0.30</td>
<td>4.55 ± 0.30</td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
<td>1.60 ± 0.20</td>
<td>1.60 ± 0.20</td>
<td>2.50 ± 0.20</td>
<td>2.50 ± 0.20</td>
</tr>
<tr>
<td>(T) Thickness (mm)</td>
<td>1.02</td>
<td>1.30</td>
<td>1.52</td>
<td>1.70</td>
<td>2.54</td>
<td>2.54</td>
</tr>
<tr>
<td>(t) Terminal (mm)</td>
<td>0.25 (0.010)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>1.02 (0.040)</td>
<td>1.02 (0.040)</td>
</tr>
<tr>
<td>Rated Temp. (ºC)</td>
<td>250</td>
<td>250</td>
<td>250</td>
<td>250</td>
<td>250</td>
<td>250</td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>16</td>
<td>16</td>
<td>25</td>
<td>25</td>
<td>25</td>
<td>25</td>
</tr>
</tbody>
</table>

Voltage rating per table. Capacitance values specified at 25°C, derate capacitance value based on TCC and VCC Plots on page 90.

**NOTE:** Contact factory for non-specified capacitance values.
# AT Series

**High Temperature MLCC – 200°C & 250°C Rated**

## CAPACITANCE RANGE

**BME C0G**  
Temp. Coefficient: 3  
200°C Rated

<table>
<thead>
<tr>
<th>Case Size</th>
<th>AT03 = 0603</th>
<th>AT05 = 0805</th>
<th>AT06 = 1206</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>1.60 ± 0.15</td>
<td>2.01 ± 0.20</td>
<td>3.20 ± 0.30</td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
<td>1.60 ± 0.20</td>
</tr>
<tr>
<td>(t) Terminal min</td>
<td>0.25 (0.010)</td>
<td>0.25 (0.010)</td>
<td>0.25 (0.010)</td>
</tr>
<tr>
<td>max</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
</tr>
<tr>
<td>Rated Temp. (ºC)</td>
<td>200</td>
<td>200</td>
<td>200</td>
</tr>
<tr>
<td>Temp. Coefficient</td>
<td>3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>25</td>
<td>50</td>
<td>25</td>
</tr>
<tr>
<td>Cap (µF)</td>
<td>39</td>
<td>390</td>
<td>47</td>
</tr>
<tr>
<td>120</td>
<td>121</td>
<td>150</td>
<td>151</td>
</tr>
<tr>
<td>390</td>
<td>391</td>
<td>470</td>
<td>471</td>
</tr>
<tr>
<td>1200</td>
<td>122</td>
<td>1500</td>
<td>152</td>
</tr>
<tr>
<td>3900</td>
<td>392</td>
<td>4700</td>
<td>472</td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>25</td>
<td>25</td>
<td>25</td>
</tr>
<tr>
<td>Rated Temp. (ºC)</td>
<td>390</td>
<td>391</td>
<td>470</td>
</tr>
<tr>
<td>1200</td>
<td>122</td>
<td>1500</td>
<td>152</td>
</tr>
<tr>
<td>3900</td>
<td>392</td>
<td>4700</td>
<td>472</td>
</tr>
</tbody>
</table>

## CAPACITANCE RANGE

**BME C0G (Ni/Au)**  
Temp. Coefficient: 5  
250°C Rated

<table>
<thead>
<tr>
<th>Case Size</th>
<th>AT03 = 0603</th>
<th>AT05 = 0805</th>
<th>AT06 = 1206</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>1.60 ± 0.15</td>
<td>2.01 ± 0.20</td>
<td>3.20 ± 0.30</td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
<td>1.60 ± 0.20</td>
</tr>
<tr>
<td>(t) Terminal min</td>
<td>0.25 (0.010)</td>
<td>0.25 (0.010)</td>
<td>0.25 (0.010)</td>
</tr>
<tr>
<td>max</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
<td>0.75 (0.030)</td>
</tr>
<tr>
<td>Rated Temp. (ºC)</td>
<td>250</td>
<td>250</td>
<td>250</td>
</tr>
<tr>
<td>Temp. Coefficient</td>
<td>3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>25</td>
<td>25</td>
<td>25</td>
</tr>
<tr>
<td>Cap (µF)</td>
<td>39</td>
<td>390</td>
<td>47</td>
</tr>
<tr>
<td>120</td>
<td>121</td>
<td>150</td>
<td>151</td>
</tr>
<tr>
<td>390</td>
<td>391</td>
<td>470</td>
<td>471</td>
</tr>
<tr>
<td>1200</td>
<td>122</td>
<td>1500</td>
<td>152</td>
</tr>
<tr>
<td>3900</td>
<td>392</td>
<td>4700</td>
<td>472</td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>25</td>
<td>25</td>
<td>25</td>
</tr>
<tr>
<td>Rated Temp. (ºC)</td>
<td>390</td>
<td>391</td>
<td>470</td>
</tr>
<tr>
<td>1200</td>
<td>122</td>
<td>1500</td>
<td>152</td>
</tr>
<tr>
<td>3900</td>
<td>392</td>
<td>4700</td>
<td>472</td>
</tr>
</tbody>
</table>

Voltage rating per table. Capacitance values specified at 25°C, derate capacitance value based on TCC and VCC Plots on page 90.  
NOTE: Contact factory for non-specified capacitance values.
## CAPACITANCE RANGE

**PREFERRED SIZES ARE SHADED**

### 200°C Rated

<table>
<thead>
<tr>
<th>Case Size</th>
<th>AT05 = 0805</th>
<th>AT06 = 1206</th>
<th>AT10 = 1210</th>
<th>AT12 = 1812</th>
<th>AT14 = 2225</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voltage (V)</td>
<td>50</td>
<td>50</td>
<td>50</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>Capacitance (μF)</td>
<td>0.010</td>
<td>103</td>
<td>0.012</td>
<td>123</td>
<td>0.015</td>
</tr>
</tbody>
</table>

### 250°C Rated

<table>
<thead>
<tr>
<th>Case Size</th>
<th>AT05 = 0805</th>
<th>AT06 = 1206</th>
<th>AT10 = 1210</th>
<th>AT12 = 1812</th>
<th>AT14 = 2225</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voltage (V)</td>
<td>25</td>
<td>25</td>
<td>25</td>
<td>25</td>
<td>25</td>
</tr>
<tr>
<td>Capacitance (μF)</td>
<td>0.010</td>
<td>104</td>
<td>0.012</td>
<td>124</td>
<td>0.015</td>
</tr>
</tbody>
</table>

Voltage rating per table. Capacitance values specified at 25°C, derate capacitance value based on TCC and VCC Plots on page 90.

**NOTE:** Contact factory for non-specified capacitance values.
High Voltage MLC Chips
For 600V to 5000V Applications

High value, low leakage and small size are difficult parameters to obtain in capacitors for high voltage systems. AVX special high voltage MLC chip capacitors meet these performance characteristics and are designed for applications such as snubbers in high frequency power converters, resonators in SMPS, and high voltage coupling/dc blocking. These high voltage chip designs exhibit low ESRs at high frequencies.

Larger physical sizes than normally encountered chips are used to make high voltage MLC chip products. Special precautions must be taken in applying these chips in surface mount assemblies. The temperature gradient during heating or cooling cycles should not exceed 4ºC per second. The preheat temperature must be within 50ºC of the peak temperature reached by the ceramic bodies through the soldering process. Chip sizes 1210 and larger should be reflow soldered only. Capacitors may require protective surface coating to prevent external arcing.

For 1825, 2225 and 3640 sizes, AVX offers leaded version in either thru-hole or SMT configurations (for details see section on high voltage leaded MLC chips).

Notes:
- Capacitors with X7R dielectrics are not intended for applications across AC supply mains or AC line filtering with polarity reversal. Contact plant for recommendations. Contact factory for availability of Termination and Tolerance options for Specific Part Numbers.
- The 3640 Style is not available on 7” Reels.
- AVX offers nonstandard chip sizes. Contact factory for details.

**HOW TO ORDER**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0805</th>
<th>1206</th>
<th>1210*</th>
<th>1808*</th>
<th>1812*</th>
<th>1825*</th>
<th>2220*</th>
<th>2225*</th>
<th>3640*</th>
</tr>
</thead>
<tbody>
<tr>
<td>(L) Length</td>
<td>2.10 ± 0.20 (0.083 ± 0.008)</td>
<td>3.30 ± 0.30 (0.130 ± 0.012)</td>
<td>3.30 ± 0.40 (0.130 ± 0.016)</td>
<td>4.60 ± 0.50 (0.181 ± 0.020)</td>
<td>4.60 ± 0.50 (0.181 ± 0.020)</td>
<td>4.60 ± 0.50 (0.181 ± 0.020)</td>
<td>5.70 ± 0.50 (0.224 ± 0.020)</td>
<td>5.72 ± 0.25 (0.225 ± 0.010)</td>
<td>9.14 ± 0.25 (0.360 ± 0.010)</td>
</tr>
<tr>
<td>(W) Width</td>
<td>1.25 ± 0.20 (0.049 ± 0.008)</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
<td>2.50 ± 0.30 (0.098 ± 0.012)</td>
<td>2.00 ± 0.20 (0.079 ± 0.008)</td>
<td>3.20 ± 0.30 (0.126 ± 0.012)</td>
<td>6.30 ± 0.40 (0.248 ± 0.016)</td>
<td>5.00 ± 0.40 (0.197 ± 0.016)</td>
<td>6.35 ± 0.25 (0.250 ± 0.010)</td>
<td>10.2 ± 0.25 (0.400 ± 0.010)</td>
</tr>
<tr>
<td>(T) Thickness</td>
<td>1.35 (0.053)</td>
<td>1.80 (0.071)</td>
<td>2.80 (0.110)</td>
<td>2.20 (0.087)</td>
<td>2.80 (0.110)</td>
<td>3.40 (0.134)</td>
<td>3.40 (0.134)</td>
<td>2.54 (0.100)</td>
<td>2.54 (0.100)</td>
</tr>
<tr>
<td>(t) terminal min. Max.</td>
<td>0.50 ± 0.20 (0.020 ± 0.008)</td>
<td>0.60 ± 0.20 (0.024 ± 0.008)</td>
<td>0.75 ± 0.35 (0.030 ± 0.014)</td>
<td>0.75 ± 0.35 (0.030 ± 0.014)</td>
<td>0.75 ± 0.35 (0.030 ± 0.014)</td>
<td>0.75 ± 0.35 (0.030 ± 0.014)</td>
<td>0.85 ± 0.35 (0.035 ± 0.014)</td>
<td>0.85 ± 0.35 (0.035 ± 0.014)</td>
<td>0.76 (0.030)</td>
</tr>
</tbody>
</table>

**DIMENSIONS**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0805</th>
<th>1206</th>
<th>1210*</th>
<th>1808*</th>
<th>1812*</th>
<th>1825*</th>
<th>2220*</th>
<th>2225*</th>
<th>3640*</th>
</tr>
</thead>
<tbody>
<tr>
<td>(L) Length</td>
<td>2.10 ± 0.20 (0.083 ± 0.008)</td>
<td>3.30 ± 0.30 (0.130 ± 0.012)</td>
<td>3.30 ± 0.40 (0.130 ± 0.016)</td>
<td>4.60 ± 0.50 (0.181 ± 0.020)</td>
<td>4.60 ± 0.50 (0.181 ± 0.020)</td>
<td>4.60 ± 0.50 (0.181 ± 0.020)</td>
<td>5.70 ± 0.50 (0.224 ± 0.020)</td>
<td>5.72 ± 0.25 (0.225 ± 0.010)</td>
<td>9.14 ± 0.25 (0.360 ± 0.010)</td>
</tr>
<tr>
<td>(W) Width</td>
<td>1.25 ± 0.20 (0.049 ± 0.008)</td>
<td>1.60 ± 0.20 (0.063 ± 0.008)</td>
<td>2.50 ± 0.30 (0.098 ± 0.012)</td>
<td>2.00 ± 0.20 (0.079 ± 0.008)</td>
<td>3.20 ± 0.30 (0.126 ± 0.012)</td>
<td>6.30 ± 0.40 (0.248 ± 0.016)</td>
<td>5.00 ± 0.40 (0.197 ± 0.016)</td>
<td>6.35 ± 0.25 (0.250 ± 0.010)</td>
<td>10.2 ± 0.25 (0.400 ± 0.010)</td>
</tr>
<tr>
<td>(T) Thickness</td>
<td>1.35 (0.053)</td>
<td>1.80 (0.071)</td>
<td>2.80 (0.110)</td>
<td>2.20 (0.087)</td>
<td>2.80 (0.110)</td>
<td>3.40 (0.134)</td>
<td>3.40 (0.134)</td>
<td>2.54 (0.100)</td>
<td>2.54 (0.100)</td>
</tr>
<tr>
<td>(t) terminal min. Max.</td>
<td>0.50 ± 0.20 (0.020 ± 0.008)</td>
<td>0.60 ± 0.20 (0.024 ± 0.008)</td>
<td>0.75 ± 0.35 (0.030 ± 0.014)</td>
<td>0.75 ± 0.35 (0.030 ± 0.014)</td>
<td>0.75 ± 0.35 (0.030 ± 0.014)</td>
<td>0.75 ± 0.35 (0.030 ± 0.014)</td>
<td>0.85 ± 0.35 (0.035 ± 0.014)</td>
<td>0.85 ± 0.35 (0.035 ± 0.014)</td>
<td>0.76 (0.030)</td>
</tr>
</tbody>
</table>

*Reflow Soldering Only
# High Voltage MLC Chips

For 600V to 5000V Applications

## NP0 (C0G) Dielectric Performance Characteristics

<table>
<thead>
<tr>
<th>Capacitance Range</th>
<th>10 pF to 0.100 μF (25°C, 1.0 ±0.2 Vrms at 1kHz, for ≤ 1000 pF use 1 MHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Tolerances</td>
<td>±5%, ±10%, ±20%</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>0.1% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz, for ≤ 1000 pF use 1 MHz)</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>-55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Characteristic</td>
<td>0 ±30 ppm/°C (0 VDC)</td>
</tr>
<tr>
<td>Voltage Ratings</td>
<td>600, 630, 1000, 1500, 2000, 2500, 3000, 4000 &amp; 5000 VDC (+125°C)</td>
</tr>
<tr>
<td>Insulation Resistance (+25°C, at 500 VDC)</td>
<td>≥100 GΩ min. or 1000 GΩ - μF min., whichever is less</td>
</tr>
<tr>
<td>Insulation Resistance (+125°C, at 500 VDC)</td>
<td>≥10 GΩ min. or 100 GΩ - μF min., whichever is less</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Reheat only</td>
</tr>
</tbody>
</table>

## NP0 (C0G) CAPACITANCE RANGE – PREFERRED SIZES ARE SHADED

<table>
<thead>
<tr>
<th>Case Size</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1808</th>
<th>1812</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>(L) Length</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(in.)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Reflow/Wave</td>
<td>0.090</td>
<td>0.090</td>
<td>0.090</td>
<td>0.090</td>
<td>0.090</td>
</tr>
<tr>
<td>Reflow/Wave</td>
<td>(0.085 ± 0.008)</td>
<td>(0.093 ± 0.012)</td>
<td>(0.130 ± 0.016)</td>
<td>(0.181 ± 0.020)</td>
<td>(0.177 ± 0.012)</td>
</tr>
<tr>
<td>Reflow Only</td>
<td>0.090</td>
<td>0.090</td>
<td>0.090</td>
<td>0.090</td>
<td>0.090</td>
</tr>
<tr>
<td>Reflow Only</td>
<td>(0.093 ± 0.012)</td>
<td>(0.130 ± 0.016)</td>
<td>(0.181 ± 0.020)</td>
<td>(0.177 ± 0.012)</td>
<td>(0.177 ± 0.012)</td>
</tr>
<tr>
<td><strong>(W) Width</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(in.)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Reflow/Wave</td>
<td>0.063</td>
<td>0.063</td>
<td>0.063</td>
<td>0.063</td>
<td>0.063</td>
</tr>
<tr>
<td>Reflow/Wave</td>
<td>(0.049 ± 0.009)</td>
<td>(0.063 ± 0.012)</td>
<td>(0.098 ± 0.012)</td>
<td>(0.079 ± 0.008)</td>
<td>(0.126 ± 0.008)</td>
</tr>
<tr>
<td>Reflow Only</td>
<td>0.063</td>
<td>0.063</td>
<td>0.063</td>
<td>0.063</td>
<td>0.063</td>
</tr>
<tr>
<td>Reflow Only</td>
<td>(0.063 ± 0.012)</td>
<td>(0.098 ± 0.012)</td>
<td>(0.079 ± 0.008)</td>
<td>(0.126 ± 0.008)</td>
<td>(0.126 ± 0.008)</td>
</tr>
<tr>
<td><strong>(T) Thickness</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(mil)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Reflow/Wave</td>
<td>0.007</td>
<td>0.007</td>
<td>0.007</td>
<td>0.007</td>
<td>0.007</td>
</tr>
<tr>
<td>Reflow/Wave</td>
<td>(0.003)</td>
<td>(0.003)</td>
<td>(0.003)</td>
<td>(0.003)</td>
<td>(0.003)</td>
</tr>
<tr>
<td>Reflow Only</td>
<td>0.007</td>
<td>0.007</td>
<td>0.007</td>
<td>0.007</td>
<td>0.007</td>
</tr>
<tr>
<td>Reflow Only</td>
<td>(0.003)</td>
<td>(0.003)</td>
<td>(0.003)</td>
<td>(0.003)</td>
<td>(0.003)</td>
</tr>
</tbody>
</table>

### Voltage (V)

<table>
<thead>
<tr>
<th>Cap (µF)</th>
<th>0.050</th>
<th>0.050</th>
<th>0.050</th>
<th>0.050</th>
<th>0.050</th>
</tr>
</thead>
<tbody>
<tr>
<td>600</td>
<td>10.48</td>
<td>6.30</td>
<td>3.30</td>
<td>1.50</td>
<td>0.75</td>
</tr>
<tr>
<td>630</td>
<td>10.65</td>
<td>6.48</td>
<td>3.42</td>
<td>1.56</td>
<td>0.78</td>
</tr>
<tr>
<td>1000</td>
<td>10.00</td>
<td>6.00</td>
<td>3.00</td>
<td>1.50</td>
<td>0.75</td>
</tr>
<tr>
<td>1050</td>
<td>10.48</td>
<td>6.30</td>
<td>3.30</td>
<td>1.50</td>
<td>0.75</td>
</tr>
<tr>
<td>1200</td>
<td>10.65</td>
<td>6.48</td>
<td>3.42</td>
<td>1.56</td>
<td>0.78</td>
</tr>
<tr>
<td>1250</td>
<td>10.00</td>
<td>6.00</td>
<td>3.00</td>
<td>1.50</td>
<td>0.75</td>
</tr>
<tr>
<td>1500</td>
<td>10.48</td>
<td>6.30</td>
<td>3.30</td>
<td>1.50</td>
<td>0.75</td>
</tr>
<tr>
<td>1550</td>
<td>10.65</td>
<td>6.48</td>
<td>3.42</td>
<td>1.56</td>
<td>0.78</td>
</tr>
<tr>
<td>1800</td>
<td>10.00</td>
<td>6.00</td>
<td>3.00</td>
<td>1.50</td>
<td>0.75</td>
</tr>
<tr>
<td>1850</td>
<td>10.48</td>
<td>6.30</td>
<td>3.30</td>
<td>1.50</td>
<td>0.75</td>
</tr>
<tr>
<td>2200</td>
<td>10.65</td>
<td>6.48</td>
<td>3.42</td>
<td>1.56</td>
<td>0.78</td>
</tr>
<tr>
<td>2500</td>
<td>10.00</td>
<td>6.00</td>
<td>3.00</td>
<td>1.50</td>
<td>0.75</td>
</tr>
<tr>
<td>3000</td>
<td>10.48</td>
<td>6.30</td>
<td>3.30</td>
<td>1.50</td>
<td>0.75</td>
</tr>
</tbody>
</table>

### Dielectric Thickness

<table>
<thead>
<tr>
<th>Letter</th>
<th>A</th>
<th>C</th>
<th>E</th>
<th>F</th>
<th>G</th>
<th>X</th>
<th>7</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max. Thickness</td>
<td>0.010</td>
<td>0.015</td>
<td>0.018</td>
<td>0.025</td>
<td>0.033</td>
<td>0.056</td>
<td>0.100</td>
</tr>
<tr>
<td>(mil)</td>
<td>(0.0003)</td>
<td>(0.0005)</td>
<td>(0.0007)</td>
<td>(0.0008)</td>
<td>(0.0010)</td>
<td>(0.0025)</td>
<td>(0.0050)</td>
</tr>
</tbody>
</table>

### Voltage (V)

| Voltage (V) | 600 | 630 | 1000 | 1500 | 2000 | 2500 | 3000 | 4000 | 600 | 630 | 1000 | 1500 | 2000 | 2500 | 3000 | 4000 |
|-------------|-----|-----|------|------|------|------|------|------|-----|-----|------|------|------|------|------|------|------|
| Case Size | 0805 | 1206 | 1210 | 1808 | 1812 | 0805 | 1206 | 1210 | 1808 | 1812 |

### Note

Contact factory for non-specified capacitance values.
# High Voltage MLC Chips

For 600V to 5000V Applications

## NP0 (C0G) Capacitance Range – Preferred Sizes Are Shaded

<table>
<thead>
<tr>
<th>Case Size</th>
<th>1825</th>
<th>2220</th>
<th>2225</th>
<th>3640</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>4.60 ± 0.50</td>
<td>5.70 ± 0.50</td>
<td>5.70 ± 0.50</td>
<td>9.14 ± 0.25</td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>6.30 ± 0.40</td>
<td>5.00 ± 0.40</td>
<td>6.30 ± 0.40</td>
<td>10.2 ± 0.25</td>
</tr>
<tr>
<td>(T) Thickness (mm)</td>
<td>3.40</td>
<td>3.40</td>
<td>3.40</td>
<td>2.54</td>
</tr>
<tr>
<td>(t) Terminal (mm)</td>
<td>0.75 ± 0.35</td>
<td>0.85 ± 0.35</td>
<td>0.85 ± 0.35</td>
<td>0.76</td>
</tr>
</tbody>
</table>

### Voltage (V)

| | 600 | 630 | 1000 | 1500 | 2000 | 2500 | 3000 | 4000 | 600 | 630 | 1000 | 1500 | 2000 | 2500 | 3000 | 4000 | 5000 |
| Cap (pF) | 1.5 | 1R5 | 1.8 | 1R8 | 2.2 | 2R2 | 2.7 | 2R7 | 3.3 | 3R3 | 3.9 | 3R9 | 4.7 | 4R7 | 5.6 | 5R6 | 6.8 | 6R8 |

### Capacitance Range Note

- Capacitance values are shaded for preferred sizes.
- Contact factory for non-specified capacitance values.
## X7R Dielectric Performance Characteristics

<table>
<thead>
<tr>
<th>Capacitance Range</th>
<th>10 pF to 0.82 µF (25°C, 1.0 ±0.2 Vrms at 1kHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Tolerances</td>
<td>±10%; ±20%; +80%, -20%</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>2.5% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz)</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>-55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Characteristic</td>
<td>±15% (0 VDC)</td>
</tr>
<tr>
<td>Voltage Ratings</td>
<td>600, 830, 1000, 1500, 2000, 2500, 3000, 4000 &amp; 5000 VDC (+125°C)</td>
</tr>
<tr>
<td>Insulation Resistance (+25°C, at 500 VDC)</td>
<td>100k Ω min. or 1000 MΩ - µF min., whichever is less</td>
</tr>
<tr>
<td>Insulation Resistance (+125°C, at 500 VDC)</td>
<td>10k Ω min. or 100 MΩ - µF min., whichever is less</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Minimum 120% rated voltage for 5 seconds at 50 mA max. current</td>
</tr>
</tbody>
</table>

### X7R CAPACITANCE RANGE – PREFERRED SIZES ARE SHDED

<table>
<thead>
<tr>
<th>Case Size</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1808</th>
<th>1812</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Relflow/Wave</td>
<td>Relflow/Wave</td>
<td>Relflow Only</td>
<td>Relflow Only</td>
<td>Relflow Only</td>
</tr>
<tr>
<td>(L) Length (mm)</td>
<td>(0.085 ±0.008)</td>
<td>(0.130 ±0.012)</td>
<td>(0.183 ±0.016)</td>
<td>(0.181 ±0.020)</td>
<td>(0.177 ±0.012)</td>
</tr>
<tr>
<td>(W) Width (mm)</td>
<td>(0.048 ±0.008)</td>
<td>(0.063 ±0.012)</td>
<td>(0.098 ±0.012)</td>
<td>(0.079 ±0.008)</td>
<td>(0.126 ±0.008)</td>
</tr>
<tr>
<td>(T) Thickness (mm)</td>
<td>(0.020 ±0.008)</td>
<td>(0.024 ±0.008)</td>
<td>(0.030 ±0.014)</td>
<td>(0.030 ±0.014)</td>
<td>(0.030 ±0.014)</td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>600</td>
<td>630</td>
<td>1000</td>
<td>1500</td>
<td>2000</td>
</tr>
<tr>
<td>Cap (pF)</td>
<td>100</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Cap (µF)</td>
<td>0.010</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>NOTE: Contact factory for non-specified capacitance values</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
# High Voltage MLC Chips

For 600V to 5000V Applications

## X7R CAPACITANCE RANGE

**PREFERRED SIZES ARE SHAPED**

<table>
<thead>
<tr>
<th>Case Size</th>
<th>1825</th>
<th>2220</th>
<th>2225</th>
<th>3640</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th><strong>Soldering</strong></th>
<th>Reflow Only</th>
<th>Reflow Only</th>
<th>Reflow Only</th>
<th>Reflow Only</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>(l) Length</strong></td>
<td>4.60 ± 0.50</td>
<td>5.70 ± 0.50</td>
<td>5.70 ± 0.50</td>
<td>9.14 ± 0.25</td>
</tr>
<tr>
<td><strong>(w) Width</strong></td>
<td>6.30 ± 0.40</td>
<td>5.00 ± 0.40</td>
<td>6.30 ± 0.40</td>
<td>9.14 ± 0.25</td>
</tr>
<tr>
<td><strong>(h) Thickness</strong></td>
<td>3.40</td>
<td>3.40</td>
<td>3.40</td>
<td>2.54</td>
</tr>
<tr>
<td><strong>(t) Terminal</strong></td>
<td>0.75 ± 0.35</td>
<td>0.85 ± 0.35</td>
<td>0.85 ± 0.35</td>
<td>0.76 ± 0.35</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Voltage (V)</th>
<th>600</th>
<th>630</th>
<th>1000</th>
<th>1500</th>
<th>2000</th>
<th>2500</th>
<th>3000</th>
<th>4000</th>
<th>5000</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cap (µF)</td>
<td>0.010</td>
<td>0.015</td>
<td>0.018</td>
<td>0.020</td>
<td>0.022</td>
<td>0.027</td>
<td>0.033</td>
<td>0.039</td>
<td>0.047</td>
</tr>
<tr>
<td>Cap (pF)</td>
<td>100</td>
<td>121</td>
<td>150</td>
<td>181</td>
<td>220</td>
<td>270</td>
<td>331</td>
<td>391</td>
<td>471</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th><strong>Letter</strong></th>
<th>A</th>
<th>B</th>
<th>C</th>
<th>D</th>
<th>E</th>
<th>F</th>
<th>G</th>
<th>X</th>
<th>Z</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Max. Thickness</strong></td>
<td>0.813</td>
<td>1.448</td>
<td>1.8034</td>
<td>2.2098</td>
<td>2.794</td>
<td>0.940</td>
<td>3.30</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for non-specified capacitance values
AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a “B” in the 12th position of the AVX Catalog Part Number. This fulfills AVX’s commitment to providing a full range of products to our customers. AVX has provided in the following pages, a full range of values that we are offering in this “B” termination.

Larger physical sizes than normally encountered chips are used to make high voltage MLC chip product. Special precautions must be taken in applying these chips in surface mount assemblies. The temperature gradient during heating or cooling cycles should not exceed 4ºC per second. The preheat temperature must be within 50ºC of the peak temperature reached by the ceramic bodies through the soldering process. Chip sizes 1210 and larger should be reflow soldered only. Capacitors may require protective surface coating to prevent external arcing.

For 1825, 2225 and 3640 sizes, AVX offers leaded version in either thru-hole or SMT configurations (for details see section on high voltage leaded MLC chips).

AVX offers nonstandard chip sizes. Contact factory for details.

* Not all values are supported in Automotive grade. Please contact factory for availability.

** The LD40 Style is not available on 7” Reels.

*** AVX offers nonstandard chip sizes. Contact factory for details.

Notes: Capacitors with X7R dielectrics are not intended for applications across AC supply mains or AC line filtering with polarity reversal. Contact plant for recommendations.

Contact factory for availability of Termination and Tolerance options for Specific Part Numbers

* FLEXITERM is not available in the LD40 Style

NOT RoHS Compliant
**C0G Dielectric**

**Performance Characteristics**

| Capacitance Range | 10 pF to 0.047 μF  
(25°C, 1.0 ±0.2 Vrms at 1kHz, for ≤ 1000 pF use 1 MHz) |
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Tolerances</td>
<td>±5%, ±10%, ±20%</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>0.1% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz, for ≤ 1000 pF use 1 MHz)</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>-55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Characteristic</td>
<td>0 ±30 ppm/°C (0 VDC)</td>
</tr>
<tr>
<td>Voltage Ratings</td>
<td>600, 630, 1000, 1500, 2000, 2500, 3000, 4000 &amp; 5000 VDC (+125°C)</td>
</tr>
<tr>
<td>Insulation Resistance (+25°C, at 500 VDC)</td>
<td>100K MΩ min. or 100 MΩ - μF min., whichever is less</td>
</tr>
<tr>
<td>Insulation Resistance (+125°C, at 500 VDC)</td>
<td>10K MΩ min. or 100 MO - μF min., whichever is less</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Minimum 120% rated voltage for 5 seconds at 50 mA max. current</td>
</tr>
</tbody>
</table>

**HIGH VOLTAGE C0G CAPACITANCE VALUES**

<table>
<thead>
<tr>
<th>VOLTAGE</th>
<th>LD05 (0805)</th>
<th>LD06 (1206)</th>
<th>LD10 (1210)</th>
<th>LD08 (1808)</th>
<th>LD12 (1812)</th>
<th>LD13 (1825)</th>
<th>LD20 (2220)</th>
<th>LD14 (2225)</th>
<th>LD40 (3640)</th>
</tr>
</thead>
<tbody>
<tr>
<td>600/630</td>
<td>10 pF 330 pF</td>
<td>10 pF 1200 pF</td>
<td>100 pF 2700 pF</td>
<td>100 pF 3300 pF</td>
<td>100 pF 5600 pF</td>
<td>100 pF 0.012 μF</td>
<td>1000 pF 0.012 μF</td>
<td>1000 pF 0.018 μF</td>
<td>10000 pF 0.018 μF</td>
</tr>
<tr>
<td>1000</td>
<td>10 pF 180 pF</td>
<td>10 pF 560 pF</td>
<td>100 pF 1500 pF</td>
<td>100 pF 2200 pF</td>
<td>100 pF 3300 pF</td>
<td>100 pF 8200 pF</td>
<td>1000 pF 0.010 μF</td>
<td>1000 pF 0.010 μF</td>
<td>10000 pF 0.022 μF</td>
</tr>
<tr>
<td>5000</td>
<td>– – –</td>
<td>– –</td>
<td>– 10 pF 1000 pF</td>
<td>– 10 pF 1000 pF</td>
<td>– 10 pF 1000 pF</td>
<td>– 10 pF 1000 pF</td>
<td>– 10 pF 1000 pF</td>
<td>– 100 pF 0.010 μF</td>
<td></td>
</tr>
</tbody>
</table>

**X7R Dielectric**

**Performance Characteristics**

| Capacitance Range | 10 pF to 0.56 μF  
(25°C, 1.0 ±0.2 Vrms at 1kHz) |
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Tolerances</td>
<td>±10%, ±20%, ±80%, ±20%</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>2.5% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz)</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>-55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Characteristic</td>
<td>±1% (0 VDC)</td>
</tr>
<tr>
<td>Voltage Ratings</td>
<td>600, 630, 1000, 1500, 2000, 2500, 3000, 4000 &amp; 5000 VDC (+125°C)</td>
</tr>
<tr>
<td>Insulation Resistance (+25°C, at 500 VDC)</td>
<td>100K MΩ min. or 100 MΩ - μF min., whichever is less</td>
</tr>
<tr>
<td>Insulation Resistance (+125°C, at 500 VDC)</td>
<td>10K MΩ min. or 100 MO - μF min., whichever is less</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Minimum 120% rated voltage for 5 seconds at 50 mA max. current</td>
</tr>
</tbody>
</table>

**HIGH VOLTAGE X7R MAXIMUM CAPACITANCE VALUES**

<table>
<thead>
<tr>
<th>VOLTAGE</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1808</th>
<th>1812</th>
<th>1825</th>
<th>2220</th>
<th>2225</th>
<th>3640</th>
</tr>
</thead>
<tbody>
<tr>
<td>600/630</td>
<td>100 pF 6800 pF</td>
<td>1000 pF 0.022 μF</td>
<td>1000 pF 0.056 μF</td>
<td>1000 pF 0.068 μF</td>
<td>1000 pF 0.120 μF</td>
<td>1000 pF 0.070 μF</td>
<td>1000 pF 0.070 μF</td>
<td>1000 pF 0.070 μF</td>
<td>1000 pF 0.070 μF</td>
</tr>
<tr>
<td>1000</td>
<td>100 pF 1500 pF</td>
<td>1000 pF 0.015 μF</td>
<td>1000 pF 0.018 μF</td>
<td>1000 pF 0.039 μF</td>
<td>1000 pF 0.100 μF</td>
<td>1000 pF 0.120 μF</td>
<td>1000 pF 0.150 μF</td>
<td>1000 pF 0.220 μF</td>
<td></td>
</tr>
<tr>
<td>1500</td>
<td>– 100 pF 2700 pF</td>
<td>100 pF 6800 pF</td>
<td>100 pF 6800 pF</td>
<td>100 pF 6800 pF</td>
<td>100 pF 6800 pF</td>
<td>100 pF 6800 pF</td>
<td>100 pF 6800 pF</td>
<td>100 pF 6800 pF</td>
<td></td>
</tr>
<tr>
<td>4000</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td></td>
</tr>
<tr>
<td>5000</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td>– –</td>
<td></td>
</tr>
</tbody>
</table>
High Voltage MLC Chips FLEXITERM®
For 600V to 5000V Applications

High value, low leakage and small size are difficult parameters to obtain in capacitors for high voltage systems. AVX special high voltage MLC chips capacitors meet these performance characteristics and are designed for applications such as snubbers in high frequency power converters, resonators in SMPS, and high voltage coupling/DC blocking. These high voltage chip designs exhibit low ESRs at high frequencies.

To make high voltage chips, larger physical sizes than are normally encountered are necessary. These larger sizes require that special precautions be taken in applying these chips in surface mount assemblies. In response to this, and to follow from the success of the FLEXITERM® range of low voltage parts, AVX is delighted to offer a FLEXITERM® high voltage range of capacitors, FLEXITERM®.

The FLEXITERM® layer is designed to enhance the mechanical flexure and temperature cycling performance of a standard ceramic capacitor, giving customers a solution where board flexure or temperature cycle damage are concerns.

HOW TO ORDER

<table>
<thead>
<tr>
<th>Style</th>
<th>Voltage</th>
<th>Temperature Coefficient</th>
<th>Capacitance Code</th>
<th>Capacitance Tolerance</th>
<th>Test Level</th>
<th>Termination*</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0805</td>
<td>600V</td>
<td>C</td>
<td>C0G = A</td>
<td>±0.05%</td>
<td>A</td>
<td>100% Tin</td>
<td>2 = 7&quot; Reel</td>
<td>A = Standard</td>
</tr>
<tr>
<td>1206</td>
<td>1000V</td>
<td>A</td>
<td>X7R = C</td>
<td>±10%</td>
<td>K</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>1500V</td>
<td>S</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>2000V</td>
<td>G</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>2500V</td>
<td>W</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>3000V</td>
<td>H</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>4000V</td>
<td>J</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1210</td>
<td>5000V</td>
<td>K</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Notes: Capacitors with X7R dielectrics are not intended for applications across AC supply mains or AC line filtering with polarity reversal. Contact plant for recommendations.

Contact factory for availability of Termination and Tolerance options for Specific Part Numbers.

*** AVX offers nonstandard chip sizes. Contact factory for details.

DIMENSIONS

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0805</th>
<th>1206</th>
<th>1210*</th>
<th>1808*</th>
<th>1812*</th>
<th>1825*</th>
<th>2220*</th>
<th>2225*</th>
</tr>
</thead>
<tbody>
<tr>
<td>(L) Length</td>
<td>2.10 ± 0.20</td>
<td>3.30 ± 0.30</td>
<td>3.30 ± 0.40</td>
<td>4.60 ± 0.50</td>
<td>4.60 ± 0.50</td>
<td>4.60 ± 0.50</td>
<td>5.70 ± 0.50</td>
<td>5.70 ± 0.50</td>
</tr>
<tr>
<td></td>
<td>(0.083 ± 0.008)</td>
<td>(0.130 ± 0.012)</td>
<td>(0.130 ± 0.016)</td>
<td>(0.181 ± 0.020)</td>
<td>(0.181 ± 0.020)</td>
<td>(0.181 ± 0.020)</td>
<td>(0.224 ± 0.020)</td>
<td>(0.224 ± 0.020)</td>
</tr>
<tr>
<td>(W) Width</td>
<td>1.25 ± 0.20</td>
<td>1.60 ± 0.25</td>
<td>2.50 ± 0.30</td>
<td>2.00 ± 0.20</td>
<td>3.20 ± 0.30</td>
<td>6.30 ± 0.40</td>
<td>5.00 ± 0.40</td>
<td>6.30 ± 0.40</td>
</tr>
<tr>
<td></td>
<td>(0.049 ± 0.008)</td>
<td>(0.063 ± 0.012)</td>
<td>(0.098 ± 0.012)</td>
<td>(0.079 ± 0.008)</td>
<td>(0.126 ± 0.012)</td>
<td>(0.248 ± 0.016)</td>
<td>(0.197 ± 0.016)</td>
<td>(0.248 ± 0.016)</td>
</tr>
<tr>
<td>(T) Thickness Max.</td>
<td>1.35 (0.053)</td>
<td>1.80 (0.071)</td>
<td>2.80 (0.110)</td>
<td>2.20 (0.087)</td>
<td>2.80 (0.110)</td>
<td>3.40 (0.134)</td>
<td>3.40 (0.134)</td>
<td>3.40 (0.134)</td>
</tr>
<tr>
<td>(l) terminal min.</td>
<td>0.50 ± 0.20</td>
<td>0.60 ± 0.20</td>
<td>0.75 ± 0.35</td>
<td>0.75 ± 0.35</td>
<td>0.75 ± 0.35</td>
<td>0.75 ± 0.35</td>
<td>0.85 ± 0.35</td>
<td>0.85 ± 0.35</td>
</tr>
<tr>
<td></td>
<td>(0.020 ± 0.008)</td>
<td>(0.024 ± 0.008)</td>
<td>(0.030 ± 0.014)</td>
<td>(0.030 ± 0.014)</td>
<td>(0.030 ± 0.014)</td>
<td>(0.030 ± 0.014)</td>
<td>(0.033 ± 0.014)</td>
<td>(0.033 ± 0.014)</td>
</tr>
</tbody>
</table>

*Reflow Soldering Only

Performance of SMPS capacitors can be simulated by downloading SpiCalci software program - http://www.avx.com/SpiApps/default.asp#spicalci

Custom values, ratings and configurations are also available.
High Voltage MLC Chips FLEXITERM®
For 600V to 5000V Applications

NP0 (C0G) Dielectric
Performance Characteristics

<table>
<thead>
<tr>
<th>Capacitance Range</th>
<th>10 pF to 0.100 μF (+25°C, 1.0 ±0.2 Vrms, 1kHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Tolerances</td>
<td>±5%, ±10%, ±20%</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>0.1% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz)</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>-55°C to +125°C</td>
</tr>
<tr>
<td>Temperature Characteristic</td>
<td>0 ±30 ppm/°C (0 VDC)</td>
</tr>
<tr>
<td>Voltage Ratings</td>
<td>600, 630, 1000, 1500, 2000, 2500, 3000, 4000 &amp; 5000 VDC (+125°C)</td>
</tr>
<tr>
<td>Insulation Resistance (+25°C, at 500 VDC)</td>
<td>100k MΩ min. or 1000 MΩ - μF min., whichever is less</td>
</tr>
<tr>
<td>Insulation Resistance (+125°C, at 500 VDC)</td>
<td>10k MΩ min. or 100 MΩ - μF min., whichever is less</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Minimum 120% rated voltage for 5 seconds at 50 mA max. current</td>
</tr>
</tbody>
</table>

NP0 (C0G) CAPACITANCE RANGE
PREFERRED SIZES ARE SHADED

<table>
<thead>
<tr>
<th>Case Size</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1808</th>
<th>1812</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>(L) Length</td>
<td>2.10 ± 0.20</td>
<td>3.30 ± 0.30</td>
<td>3.30 ± 0.40</td>
<td>4.60 ± 0.50</td>
<td>4.60 ± 0.50</td>
</tr>
<tr>
<td>(W) Width</td>
<td>1.25 ± 0.20</td>
<td>1.60 ± 0.30</td>
<td>2.50 ± 0.30</td>
<td>2.00 ± 0.20</td>
<td>3.20 ± 0.30</td>
</tr>
<tr>
<td>(T) Thickness</td>
<td>1.35</td>
<td>1.80</td>
<td>2.80</td>
<td>2.20</td>
<td>2.80</td>
</tr>
<tr>
<td>(t) Terminal</td>
<td>0.50 ± 0.20</td>
<td>0.60 ± 0.20</td>
<td>0.75 ± 0.35</td>
<td>0.75 ± 0.35</td>
<td>0.75 ± 0.35</td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>600</td>
<td>630</td>
<td>1000</td>
<td>600</td>
<td>630</td>
</tr>
<tr>
<td>Cap (μF)</td>
<td>0.010</td>
<td>103</td>
<td>0.012</td>
<td>123</td>
<td>0.015</td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>600</td>
<td>630</td>
<td>1000</td>
<td>600</td>
<td>630</td>
</tr>
</tbody>
</table>
High Voltage MLC Chips FLEXITERM®
For 600V to 5000V Applications

NP0 (C0G) CAPACITANCE RANGE
PREFERRED SIZES ARE SHADED

<table>
<thead>
<tr>
<th>Case Size</th>
<th>1825</th>
<th>2220</th>
<th>2225</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>(L) Length</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
</tr>
<tr>
<td>4.60 ± 0.50</td>
<td>0.181 ± 0.020</td>
<td>5.70 ± 0.50</td>
<td>0.224 ± 0.020</td>
</tr>
<tr>
<td>6.30 ± 0.40</td>
<td>0.248 ± 0.016</td>
<td>5.00 ± 0.40</td>
<td>0.197 ± 0.016</td>
</tr>
<tr>
<td>3.40</td>
<td>0.134</td>
<td>3.40</td>
<td>0.134</td>
</tr>
<tr>
<td>(W) Width</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
</tr>
<tr>
<td>0.75 ± 0.35</td>
<td>(0.030 ± 0.014)</td>
<td>0.85 ± 0.35</td>
<td>(0.033 ± 0.014)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Voltage (V)</th>
<th>Cap (μF)</th>
<th>(W) Width (in.)</th>
<th>(T) Thickness (in.)</th>
<th>(L) Length (in.)</th>
<th>(Q) Max. (in.)</th>
<th>Thickness (μin.)</th>
</tr>
</thead>
<tbody>
<tr>
<td>600</td>
<td>0.100</td>
<td>0.068</td>
<td>0.056</td>
<td>0.047</td>
<td>0.022</td>
<td>0.015</td>
</tr>
<tr>
<td>1000</td>
<td>0.150</td>
<td>0.050</td>
<td>0.040</td>
<td>0.035</td>
<td>0.018</td>
<td>0.010</td>
</tr>
<tr>
<td>1500</td>
<td>0.200</td>
<td>0.040</td>
<td>0.030</td>
<td>0.025</td>
<td>0.014</td>
<td>0.008</td>
</tr>
<tr>
<td>2000</td>
<td>0.250</td>
<td>0.033</td>
<td>0.025</td>
<td>0.020</td>
<td>0.014</td>
<td>0.008</td>
</tr>
<tr>
<td>3000</td>
<td>0.375</td>
<td>0.020</td>
<td>0.015</td>
<td>0.010</td>
<td>0.008</td>
<td>0.006</td>
</tr>
<tr>
<td>4000</td>
<td>0.500</td>
<td>0.015</td>
<td>0.010</td>
<td>0.007</td>
<td>0.006</td>
<td>0.005</td>
</tr>
</tbody>
</table>

Electrode Letter Codes: A E F G X

NOTE: Contact factory for non-specified capacitance.
High Voltage MLC Chips FLEXITERM®
For 600V to 5000V Applications

X7R Dielectric
Performance Characteristics

Capacitance Range
10 pF to 0.82 μF (25°C, 1.0 ±0.2 Vrms at 1kHz)

Capacitance Tolerances
±10%; ±20%; +80%, -20%

Dissipation Factor
2.5% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz)

Operating Temperature Range
-55°C to +125°C

Temperature Characteristic
±15% (0 VDC)

Voltage Ratings
600, 630, 1000, 1500, 2000, 2500, 3000, 4000 & 5000 VDC (+125°C)

Insulation Resistance (+25°C, at 500 VDC)
100K MΩ min. or 1000 MΩ - μF min., whichever is less

Insulation Resistance (+125°C, at 500 VDC)
10K MΩ min. or 100 MΩ - μF min., whichever is less

Dielectric Strength
Minimum 120% rated voltage for 5 seconds at 50 mA max. current

X7R CAPACITANCE RANGE
PREFERRED SIZES ARE SHADED

<table>
<thead>
<tr>
<th>Case Size</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1808</th>
<th>1812</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voltage (V)</td>
<td>600 630 1000 1500 2000 2500 3000 4000 &amp; 5000 VDC (+125°C)</td>
<td>600 630 1000 1500 2000 2500 3000 4000 &amp; 5000 VDC (+125°C)</td>
<td>600 630 1000 1500 2000 2500 3000 4000 &amp; 5000 VDC (+125°C)</td>
<td>600 630 1000 1500 2000 2500 3000 4000 &amp; 5000 VDC (+125°C)</td>
<td>600 630 1000 1500 2000 2500 3000 4000 &amp; 5000 VDC (+125°C)</td>
</tr>
<tr>
<td>Capacitance (μF)</td>
<td>0.010 103 C C C C E E E E E E E E E F</td>
<td>0.015 153 C C E E E E E F F F E E F G</td>
<td>0.018 183 C C E E E E E F F F E E G</td>
<td>0.022 223 C C C C E E E E F</td>
<td>0.027 273 C C C C E E E E F</td>
</tr>
<tr>
<td></td>
<td>0.033 333 C C C C E E E E F</td>
<td>0.039 393 C C C C E E E E F</td>
<td>0.047 473 C C C C E E E E F</td>
<td>0.056 563 C C C C E E E E F</td>
<td>0.068 683 C C C C E E E E F</td>
</tr>
<tr>
<td></td>
<td>0.082 823 C C C C E E E E F</td>
<td>0.100 104 C C C C E E E E F</td>
<td>0.150 154 C C C C E E E E F</td>
<td>0.200 204 C C C C E E E E F</td>
<td>0.270 274 C C C C E E E E F</td>
</tr>
<tr>
<td></td>
<td>0.300 304 C C C C E E E E F</td>
<td>0.390 394 C C C C E E E E F</td>
<td>0.470 474 C C C C E E E E F</td>
<td>0.560 564 C C C C E E E E F</td>
<td>0.680 684 C C C C E E E E F</td>
</tr>
<tr>
<td></td>
<td>0.820 824 C C C C E E E E F</td>
<td>1.000 105 C C C C E E E E F</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
# High Voltage MLC Chips FLEXITERM®

For 600V to 5000V Applications

## X7R CAPACITANCE RANGE

<table>
<thead>
<tr>
<th>Case Size</th>
<th>1825</th>
<th>2220</th>
<th>2225</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>Length</td>
<td>(mm)</td>
<td>4.60 ± 0.50</td>
<td>5.70 ± 0.50</td>
</tr>
<tr>
<td>(in.)</td>
<td></td>
<td>(0.181 ± 0.020)</td>
<td>(0.224 ± 0.020)</td>
</tr>
<tr>
<td>Width</td>
<td>(mm)</td>
<td>6.30 ± 0.40</td>
<td>5.00 ± 0.40</td>
</tr>
<tr>
<td>(in.)</td>
<td></td>
<td>(0.248 ± 0.016)</td>
<td>(0.197 ± 0.016)</td>
</tr>
<tr>
<td>Thickness</td>
<td>(mm)</td>
<td>3.40</td>
<td>3.40</td>
</tr>
<tr>
<td>(in.)</td>
<td></td>
<td>(0.134)</td>
<td>(0.134)</td>
</tr>
<tr>
<td>Terminal</td>
<td>min</td>
<td>0.75 ± 0.35</td>
<td>0.85 ± 0.35</td>
</tr>
<tr>
<td>max</td>
<td></td>
<td>(0.030 ± 0.014)</td>
<td>(0.033 ± 0.014)</td>
</tr>
<tr>
<td>Voltage (V)</td>
<td></td>
<td>600</td>
<td>630</td>
</tr>
<tr>
<td>Cap (pF)</td>
<td></td>
<td>100</td>
<td>120</td>
</tr>
<tr>
<td></td>
<td></td>
<td>680</td>
<td>820</td>
</tr>
<tr>
<td></td>
<td></td>
<td>6800</td>
<td>8200</td>
</tr>
<tr>
<td></td>
<td></td>
<td>12000</td>
<td>15000</td>
</tr>
<tr>
<td></td>
<td></td>
<td>68000</td>
<td>82000</td>
</tr>
</tbody>
</table>

### NOTE:

- Contact factory for non-specified capacitance values

## Capacitance Range

### Preferred Sizes

| Voltage (V) | 800 820 1000 1200 1500 1800 2200 2500 3000 4000 6000 8200 10000 |
|-------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|
| Thickness   | 0.010  0.015  0.022  0.027  0.033  0.039  0.047  0.056  0.066  0.082  0.105  | 0.015  0.022  0.033  0.039  0.047  0.056  0.066  0.082  0.105  |
|             | 0.039  0.047  0.056  0.066  0.082  0.105  | 0.039  0.047  0.056  0.066  0.082  0.105  |
|             | 0.047  0.056  0.066  0.082  0.105  | 0.047  0.056  0.066  0.082  0.105  |
|             | 0.056  0.066  0.082  0.105  | 0.056  0.066  0.082  0.105  |
|             | 0.066  0.082  0.105  | 0.066  0.082  0.105  |
|             | 0.082  0.105  | 0.082  0.105  |
|             | 0.105  | 0.105  |

### Letter

- **A**: 0.813 (0.032) 1.448 (0.057) 1.834 (0.071) 2.209 (0.087) 2.794 (0.113) 3.048 (0.120) 3.940 (0.037)
Modern automotive electronics could require components capable to work with high voltage (e.g. xenon lamp circuits or power converters in hybrid cards). AVX offers high voltage ceramic capacitors qualified according to AEC-Q200 standard.

High value, low leakage and small size are difficult parameters to obtain in capacitors for high voltage systems. AVX special high voltage MLC chip capacitors meet these performance characteristics and are designed for applications such as snubbers in high frequency power converters, resonators in SMPS, and high voltage coupling/dc blocking. These high voltage chip designs exhibit low ESRs at high frequencies.

Due to high voltage nature, larger physical dimensions are necessary. These larger sizes require special precautions to be taken in applying of MLC chips. The temperature gradient during heating or cooling cycles should not exceed 4°C per second. The preheat temperature must be within 50°C of the peak temperature reached by the ceramic bodies through the soldering process. Chip sizes 1210 and larger should be reflow soldered only. Capacitors may require protective surface coating to prevent external arcing.

To improve mechanical and thermal resistance, AVX recommend to use flexible terminations system - FLEXITERM®.

**HOW TO ORDER**

<table>
<thead>
<tr>
<th>Size</th>
<th>Voltage</th>
<th>Dielectric</th>
<th>Capacitance Code</th>
<th>Capacitance Tolerance</th>
<th>Failure Rate</th>
<th>Terminations</th>
<th>Packaging</th>
<th>Special Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>1206</td>
<td>C = 630V</td>
<td>X7R = C</td>
<td>2 Sig. Digits + Number of Zeros</td>
<td>K = ±10%</td>
<td>4 = Automotive</td>
<td>T = Plated Ni and Sn Z = FLEXITERM®</td>
<td>1 or 2 = 7” Reel 3 or 4 = 13” Reel</td>
<td>A = Std. Product</td>
</tr>
<tr>
<td>1210</td>
<td>A = 1000V</td>
<td></td>
<td></td>
<td>M = ±20%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1808</td>
<td>S = 1500V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1812</td>
<td>G = 2000V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2220</td>
<td>W = 2500V</td>
<td>H = 3000V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Notes: Capacitors with X7R dielectrics are not intended for applications across AC supply mains or AC line filtering with polarity reversal. Please contact AVX for recommendations.

**CHIP DIMENSIONS DESCRIPTION**

(See capacitance range chart on page 109)

**X7R DIELECTRIC PERFORMANCE CHARACTERISTICS**

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating Temperature Range</td>
<td>-55°C to +125°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance Dissipation Factor</td>
<td>within specified tolerance</td>
<td>Freq.: 1kHz ±10% Voltage: 1.0Vrms ±0.2Vrms T = +25°C, V = 0Vdc</td>
</tr>
<tr>
<td>Capacitance Tolerance</td>
<td>2.5% max. ±% (J), ±10% (K), ±20% (M)</td>
<td></td>
</tr>
<tr>
<td>Temperature Characteristics</td>
<td>X7R = ±15%</td>
<td>Vdc = 0V, T = (-55°C to +125°C)</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>100GΩ min. or 1000MΩ • μF min. (whichever is less)</td>
<td>T = +25°C, V = 500Vdc T = +125°C, V = 500Vdc (t ≥ 120 sec, I ≤ 50mA)</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>No breakdown or visual defect</td>
<td>120% of rated voltage t ≤ 5 sec, I ≤ 50mA</td>
</tr>
</tbody>
</table>
### X7R Capacitance Range

**Preferred Sizes Are Shaded**

<table>
<thead>
<tr>
<th>Case Size</th>
<th>1206</th>
<th>1210</th>
<th>1806</th>
<th>1812</th>
<th>2220</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow/Wave</td>
<td>ReflowOnly</td>
<td>ReflowOnly</td>
<td>ReflowOnly</td>
<td>ReflowOnly</td>
</tr>
<tr>
<td>L) Length (mm)</td>
<td>3.20 ± 0.20</td>
<td>3.10 ± 0.20</td>
<td>4.57 ± 0.20</td>
<td>4.50 ± 0.20</td>
<td>5.70 ± 0.40</td>
</tr>
<tr>
<td>W) Width (mm)</td>
<td>1.60 ± 0.20</td>
<td>1.90 ± 0.20</td>
<td>2.03 ± 0.25</td>
<td>2.00 ± 0.25</td>
<td>2.00 ± 0.25</td>
</tr>
<tr>
<td>T) Thickness (mm)</td>
<td>1.50</td>
<td>1.70</td>
<td>2.03</td>
<td>2.03</td>
<td>2.03</td>
</tr>
<tr>
<td>Terminal (mm)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>580-680</td>
<td>680-820</td>
<td>820-1020</td>
<td>1020-1520</td>
<td>1520-2220</td>
</tr>
<tr>
<td>Cap (pF)</td>
<td>0.01-100</td>
<td>0.01-100</td>
<td>0.01-100</td>
<td>0.01-100</td>
<td>0.01-100</td>
</tr>
<tr>
<td>Cap (µF)</td>
<td>0.01-100</td>
<td>0.01-100</td>
<td>0.01-100</td>
<td>0.01-100</td>
<td>0.01-100</td>
</tr>
<tr>
<td>Voltage (V)</td>
<td>830-1000</td>
<td>1000-1500</td>
<td>1500-2000</td>
<td>2000-2500</td>
<td>2500-4000</td>
</tr>
<tr>
<td>Case Size</td>
<td>1206</td>
<td>1210</td>
<td>1806</td>
<td>1812</td>
<td>2220</td>
</tr>
</tbody>
</table>

**NOTE:** Contact factory for non-specified capacitance values.
MIL-PRF-55681/Chips
Part Number Example
CDR01 thru CDR06

MILITARY DESIGNATION PER MIL-PRF-55681

<table>
<thead>
<tr>
<th>Part Number Example</th>
<th>MIL Style</th>
<th>BP</th>
<th>101</th>
<th>B</th>
<th>K</th>
<th>S</th>
<th>M</th>
</tr>
</thead>
<tbody>
<tr>
<td>CDR01</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

MIL Style: CDR01, CDR02, CDR03, CDR04, CDR05, CDR06

Voltage Temperature Limits:
BP = 0 ± 30 ppm/°C without voltage; 0 ± 30 ppm/°C with rated voltage from -55°C to +125°C
BX = ±15% without voltage; +15 –25% with rated voltage from -55°C to +125°C

Capacitance: Two digit figures followed by multiplier (number of zeros to be added) e.g., 101 = 100 pF
Rated Voltage: A = 50V, B = 100V
Capacitance Tolerance: J ± 5%, K ± 10%, M ± 20%

Termination Finish:
M = Palladium silver
N = Silver-nickel-gold
S = Solder coated final with a minimum of 4 percent lead
T = Silver
U = Base metallization-barrier metal-solder coated (tin/lead alloy, with a minimum of 4 percent lead)
W = Base metallization-barrier metal-tinned (tin or tin/lead alloy)
Y = Base metallization-barrier metal-tin (100 percent)
Z = Base metallization-barrier metal-tinned (tin/lead alloy, with a minimum of 4 percent lead)

*See MIL-PRF-55681 Specification for more details

Failure Rate Level: M = 1.0%, P = .1%, R = .01%, S = .001%

Packaging: Bulk is standard packaging. Tape and reel per RS481 is available upon request.

CROSS REFERENCE: AVX/MIL-PRF-55681/CDR01 THRU CDR06*

<table>
<thead>
<tr>
<th>Per MIL-PRF-55681</th>
<th>AVX Style</th>
<th>Length (L)</th>
<th>Width (W)</th>
<th>Thickness (T)</th>
<th>D</th>
<th>Termination Band (t)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CDR01</td>
<td>0805</td>
<td>.080 ± .015</td>
<td>.050 ± .015</td>
<td>.022 – .025</td>
<td>.020 – .030</td>
<td></td>
</tr>
<tr>
<td>CDR02</td>
<td>1805</td>
<td>.180 ± .015</td>
<td>.050 ± .015</td>
<td>.022 – .025</td>
<td>.020 – .030</td>
<td></td>
</tr>
<tr>
<td>CDR03</td>
<td>1808</td>
<td>.180 ± .015</td>
<td>.080 ± .018</td>
<td>.022 – .030</td>
<td>.020 – .030</td>
<td></td>
</tr>
</tbody>
</table>

*For CDR11, 12, 13, and 14 see AVX Microwave Chip Capacitor Catalog

Not RoHS Compliant
## MIL-PRF-55681/Chips

### Military Part Number Identification

**CDR01 thru CDR06**

### CDR01 thru CDR06 to MIL-PRF-55681

<table>
<thead>
<tr>
<th>Military Type Designation</th>
<th>Capacitance in pF</th>
<th>Capacitance Tolerance</th>
<th>Rated temperature and voltage-temperature limits</th>
<th>WVDC</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>AVX Style 0805/CDR01</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CDR01BP100B...</td>
<td>10</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP120B...</td>
<td>12</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP150B...</td>
<td>15</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP180B...</td>
<td>18</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP220B...</td>
<td>22</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP270B...</td>
<td>27</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP330B...</td>
<td>33</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP470B...</td>
<td>47</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP660B...</td>
<td>66</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP820B...</td>
<td>82</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP102B...</td>
<td>100</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP121B...</td>
<td>120</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP151B...</td>
<td>150</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BP181B...</td>
<td>180</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BX221B...</td>
<td>220</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BX271B...</td>
<td>270</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BX331B...</td>
<td>330</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BX391B...</td>
<td>390</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BX471B...</td>
<td>470</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR01BX741B...</td>
<td>740</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td><strong>AVX Style 1805/CDR02</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CDR02BP221B...</td>
<td>220</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP271B...</td>
<td>270</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP332B...</td>
<td>330</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP472B...</td>
<td>470</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP662B...</td>
<td>660</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP822B...</td>
<td>820</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP103B...</td>
<td>1000</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP122B...</td>
<td>1200</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP152B...</td>
<td>1500</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP182B...</td>
<td>1800</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP222B...</td>
<td>2200</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP272B...</td>
<td>2700</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP332B...</td>
<td>3300</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP472B...</td>
<td>4700</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP562B...</td>
<td>5600</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP682B...</td>
<td>6800</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR02BP822B...</td>
<td>8200</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td><strong>AVX Style 1808/CDR03</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CDR03BP331B...</td>
<td>330</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BP391B...</td>
<td>390</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BP471B...</td>
<td>470</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BP561B...</td>
<td>560</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BP681B...</td>
<td>680</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BP821B...</td>
<td>820</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BP102B...</td>
<td>1000</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BX123B...</td>
<td>1200</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BX153B...</td>
<td>1500</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BX183B...</td>
<td>1800</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BX223B...</td>
<td>2200</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BX273B...</td>
<td>2700</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BX333B...</td>
<td>3300</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BX393B...</td>
<td>3900</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BX473B...</td>
<td>4700</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR03BX683B...</td>
<td>6800</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td><strong>AVX Style 1812/CDR04</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CDR04BP122B...</td>
<td>1200</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BP152B...</td>
<td>1500</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BP182B...</td>
<td>1800</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BP222B...</td>
<td>2200</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BP272B...</td>
<td>2700</td>
<td>J</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BP332B...</td>
<td>3300</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BX393B...</td>
<td>3900</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BX473B...</td>
<td>4700</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BX683B...</td>
<td>6800</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BX823B...</td>
<td>8200</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BX104B...</td>
<td>10000</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BX124B...</td>
<td>12000</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BX154B...</td>
<td>15000</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR04BX184B...</td>
<td>18000</td>
<td>K</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td><strong>AVX Style 1825/CDR05</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CDR05BP392B...</td>
<td>3900</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR05BP472B...</td>
<td>4700</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR05BP562B...</td>
<td>5600</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR05BP682B...</td>
<td>6800</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR05BP822B...</td>
<td>8200</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR05BX104B...</td>
<td>10000</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR05BX124B...</td>
<td>12000</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR05BX154B...</td>
<td>15000</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td>CDR05BX184B...</td>
<td>18000</td>
<td>K,M</td>
<td>BX</td>
<td>100</td>
</tr>
<tr>
<td><strong>AVX Style 2225/CDR06</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CDR06BP682B...</td>
<td>6800</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR06BP822B...</td>
<td>8200</td>
<td>J,K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR06BX394A...</td>
<td>39000</td>
<td>K</td>
<td>BX</td>
<td>50</td>
</tr>
<tr>
<td>CDR06BX474A...</td>
<td>47000</td>
<td>K,M</td>
<td>BX</td>
<td>50</td>
</tr>
</tbody>
</table>
**MIL-PRF-55681/Chips**

**Part Number Example**

CDR31 thru CDR35

---

**MIL Style:** CDR31, CDR32, CDR33, CDR34, CDR35

**Voltage Temperature Limits:**

BP = 0 ± 30 ppm/°C without voltage; 0 ± 30 ppm/°C with rated voltage from -55°C to +125°C

BX = ±15% without voltage; +15 –25% with rated voltage from -55°C to +125°C

**Capacitance:** Two digit figures followed by multiplier (number of zeros to be added) e.g., 101 = 100 pF

**Rated Voltage:** A = 50V, B = 100V

**Capacitance Tolerance:** B ± .10 pF, C ± .25 pF, D ± .5 pF, F ± 1%, J ± 5%, K ± 10%, M ± 20%

**Termination Finish:**

- M = Palladium silver
- N = Silver-nickel-gold
- S = Solder coated final with a minimum of 4 percent lead
- T = Silver
- U = Base metallization-barrier metal-solder coated (tin/lead alloy, with a minimum of 4 percent lead)
- W = Base metallization-barrier metal-tinned (tin or tin/lead alloy)
- Y = Base metallization-barrier metal-tin (100 percent)
- Z = Base metallization-barrier metal-tinned (tin/lead alloy, with a minimum of 4 percent lead)

*See MIL-PRF-55681 Specification for more details

**Failure Rate Level:** M = 1.0%, P = .1%, R = .01%, S = .001%

**Packaging:** Bulk is standard packaging. Tape and reel per RS481 is available upon request.

---

**CROSS REFERENCE: AVX/MIL-PRF-55681/CDR31 THRU CDR35**

<table>
<thead>
<tr>
<th>Per MIL-PRF-55681 (Metric Sizes)</th>
<th>AVX Style</th>
<th>Length (L) (mm)</th>
<th>Width (W) (mm)</th>
<th>Thickness (T)</th>
<th>D</th>
<th>Termination Band (t)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Max. (mm)</td>
<td>Min. (mm)</td>
<td>Max. (mm)</td>
<td>Min. (mm)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CDR31</td>
<td>0805</td>
<td>2.00</td>
<td>1.25</td>
<td>1.3</td>
<td>.50</td>
<td>.70</td>
</tr>
<tr>
<td>CDR32</td>
<td>1206</td>
<td>3.20</td>
<td>1.60</td>
<td>1.3</td>
<td>—</td>
<td>.70</td>
</tr>
<tr>
<td>CDR33</td>
<td>1210</td>
<td>3.20</td>
<td>2.50</td>
<td>1.5</td>
<td>—</td>
<td>.70</td>
</tr>
<tr>
<td>CDR34</td>
<td>1812</td>
<td>4.50</td>
<td>3.20</td>
<td>1.5</td>
<td>—</td>
<td>.70</td>
</tr>
<tr>
<td>CDR35</td>
<td>1825</td>
<td>4.50</td>
<td>6.40</td>
<td>1.5</td>
<td>—</td>
<td>.70</td>
</tr>
</tbody>
</table>
## MIL-PRF-55681/Chips
### Military Part Number Identification CDR31

### CDR31 to MIL-PRF-55681/7

<table>
<thead>
<tr>
<th>AVX Style 0805/CDR31 (BP)</th>
<th>AVX Style 0805/CDR31 (BP) cont’d</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Military Type</strong></td>
<td><strong>Capacitance in pF</strong></td>
</tr>
<tr>
<td>CDR31BP1R0B---</td>
<td>1.0</td>
</tr>
<tr>
<td>CDR31BP1R1B---</td>
<td>1.1</td>
</tr>
<tr>
<td>CDR31BP1R2B---</td>
<td>1.2</td>
</tr>
<tr>
<td>CDR31BP1R3B---</td>
<td>1.3</td>
</tr>
<tr>
<td>CDR31BP1R5B---</td>
<td>1.5</td>
</tr>
<tr>
<td>CDR31BP1R6B---</td>
<td>1.6</td>
</tr>
<tr>
<td>CDR31BP1R8B---</td>
<td>1.8</td>
</tr>
<tr>
<td>CDR31BP2R0B---</td>
<td>2.0</td>
</tr>
<tr>
<td>CDR31BP2R2B---</td>
<td>2.2</td>
</tr>
<tr>
<td>CDR31BP2R4B---</td>
<td>2.4</td>
</tr>
<tr>
<td>CDR31BP2R7B---</td>
<td>2.7</td>
</tr>
<tr>
<td>CDR31BP3R0B---</td>
<td>3.0</td>
</tr>
<tr>
<td>CDR31BP3R2B---</td>
<td>3.3</td>
</tr>
<tr>
<td>CDR31BP3R6B---</td>
<td>3.6</td>
</tr>
<tr>
<td>CDR31BP3R9B---</td>
<td>3.9</td>
</tr>
<tr>
<td>CDR31BP4R3B---</td>
<td>4.3</td>
</tr>
<tr>
<td>CDR31BP4R7B---</td>
<td>4.7</td>
</tr>
<tr>
<td>CDR31BP5R1B---</td>
<td>5.1</td>
</tr>
<tr>
<td>CDR31BP5R6B---</td>
<td>5.6</td>
</tr>
<tr>
<td>CDR31BP6R2B---</td>
<td>6.2</td>
</tr>
<tr>
<td>CDR31BP6R8B---</td>
<td>6.8</td>
</tr>
<tr>
<td>CDR31BP7R5B---</td>
<td>7.5</td>
</tr>
<tr>
<td>CDR31BP8R2B---</td>
<td>8.2</td>
</tr>
<tr>
<td>CDR31BP9R1B---</td>
<td>9.1</td>
</tr>
<tr>
<td>CDR31BP100B---</td>
<td>10</td>
</tr>
<tr>
<td>CDR31BP110B---</td>
<td>11</td>
</tr>
<tr>
<td>CDR31BP120B---</td>
<td>12</td>
</tr>
<tr>
<td>CDR31BP130B---</td>
<td>13</td>
</tr>
<tr>
<td>CDR31BP150B---</td>
<td>15</td>
</tr>
<tr>
<td>CDR31BP160B---</td>
<td>16</td>
</tr>
<tr>
<td>CDR31BP180B---</td>
<td>18</td>
</tr>
<tr>
<td>CDR31BP200B---</td>
<td>20</td>
</tr>
<tr>
<td>CDR31BP220B---</td>
<td>22</td>
</tr>
<tr>
<td>CDR31BP240B---</td>
<td>24</td>
</tr>
<tr>
<td>CDR31BP270B---</td>
<td>27</td>
</tr>
<tr>
<td>CDR31BP300B---</td>
<td>30</td>
</tr>
<tr>
<td>CDR31BP330B---</td>
<td>33</td>
</tr>
<tr>
<td>CDR31BP360B---</td>
<td>36</td>
</tr>
<tr>
<td>CDR31BP390B---</td>
<td>39</td>
</tr>
<tr>
<td>CDR31BP430B---</td>
<td>43</td>
</tr>
<tr>
<td>CDR31BP470B---</td>
<td>47</td>
</tr>
<tr>
<td>CDR31BP510B---</td>
<td>51</td>
</tr>
<tr>
<td>CDR31BP560B---</td>
<td>56</td>
</tr>
<tr>
<td>CDR31BP620B---</td>
<td>62</td>
</tr>
<tr>
<td>CDR31BP680B---</td>
<td>68</td>
</tr>
<tr>
<td>CDR31BP750B---</td>
<td>75</td>
</tr>
<tr>
<td>CDR31BP820B---</td>
<td>82</td>
</tr>
<tr>
<td>CDR31BP910B---</td>
<td>91</td>
</tr>
</tbody>
</table>

1/ The complete part number will include additional symbols to indicate capacitance tolerance, termination and failure rate level.

- Add appropriate failure rate
- Add appropriate termination finish
- Capacitance Tolerance
### MIL-PRF-55681/Chips

**Military Part Number Identification CDR32**

<table>
<thead>
<tr>
<th>Military Type Designation / Capacitance in pF / Capacitance Tolerance / Rated temperature and voltage-temperature limits / WVDC</th>
</tr>
</thead>
<tbody>
<tr>
<td>AVX Style 1206/CDR32 (BP)</td>
</tr>
<tr>
<td>CDR32BP100B--- 1.0 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP110B--- 1.1 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP120B--- 1.2 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP130B--- 1.3 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP150B--- 1.5 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP160B--- 1.6 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP180B--- 1.8 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP200B--- 2.0 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP220B--- 2.2 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP240B--- 2.4 B.C BP 100</td>
</tr>
<tr>
<td>CDR32BP270B--- 2.7 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP300B--- 3.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP330B--- 3.3 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP360B--- 3.6 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP390B--- 3.9 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP430B--- 4.3 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP470B--- 4.7 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP510B--- 5.1 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP560B--- 5.6 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP620B--- 6.2 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP680B--- 6.8 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP750B--- 7.5 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP820B--- 8.2 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP910B--- 9.1 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP101B--- 10.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP111B--- 11.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP121B--- 12.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP131B--- 13.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP151B--- 15.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP161B--- 16.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP181B--- 18.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP201B--- 20.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP221B--- 22.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP241B--- 24.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP271B--- 27.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP301B--- 30.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP331B--- 33.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP361B--- 36.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP391B--- 39.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP431B--- 43.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP471B--- 47.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP511B--- 51.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP561B--- 56.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP621B--- 62.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP681B--- 68.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP751B--- 75.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP821B--- 82.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP911B--- 91.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP102B--- 100.0 B.C.D BP 100</td>
</tr>
<tr>
<td>CDR32BP112A--- 110.0 B.C.D BP 50</td>
</tr>
<tr>
<td>CDR32BP122A--- 120.0 B.C.D BP 50</td>
</tr>
<tr>
<td>CDR32BP132A--- 130.0 B.C.D BP 50</td>
</tr>
<tr>
<td>CDR32BP152A--- 150.0 B.C.D BP 50</td>
</tr>
<tr>
<td>CDR32BP162A--- 160.0 B.C.D BP 50</td>
</tr>
<tr>
<td>CDR32BP182A--- 180.0 B.C.D BP 50</td>
</tr>
<tr>
<td>CDR32BP202A--- 200.0 B.C.D BP 50</td>
</tr>
<tr>
<td>CDR32BP222A--- 220.0 B.C.D BP 50</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Military Type Designation / Capacitance in pF / Capacitance Tolerance / Rated temperature and voltage-temperature limits / WVDC</th>
</tr>
</thead>
<tbody>
<tr>
<td>AVX Style 1206/CDR32 (BP) cont’d</td>
</tr>
<tr>
<td>CDR32BP101B--- 100 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP111B--- 110 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP121B--- 120 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP131B--- 130 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP151B--- 150 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP161B--- 160 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP181B--- 180 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP201B--- 200 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP221B--- 220 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP241B--- 240 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP271B--- 270 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP301B--- 300 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP331B--- 330 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP361B--- 360 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP391B--- 390 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP431B--- 430 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP471B--- 470 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP511B--- 510 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP561B--- 560 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP621B--- 620 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP681B--- 680 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP751B--- 750 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP821B--- 820 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP911B--- 910 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP102B--- 1,000 F.J.K BP 100</td>
</tr>
<tr>
<td>CDR32BP112A--- 1,100 F.J.K BP 50</td>
</tr>
<tr>
<td>CDR32BP122A--- 1,200 F.J.K BP 50</td>
</tr>
<tr>
<td>CDR32BP132A--- 1,300 F.J.K BP 50</td>
</tr>
<tr>
<td>CDR32BP152A--- 1,500 F.J.K BP 50</td>
</tr>
<tr>
<td>CDR32BP162A--- 1,600 F.J.K BP 50</td>
</tr>
<tr>
<td>CDR32BP182A--- 1,800 F.J.K BP 50</td>
</tr>
<tr>
<td>CDR32BP202A--- 2,000 F.J.K BP 50</td>
</tr>
<tr>
<td>CDR32BP222A--- 2,200 F.J.K BP 50</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Military Type Designation / Capacitance in pF / Capacitance Tolerance / Rated temperature and voltage-temperature limits / WVDC</th>
</tr>
</thead>
<tbody>
<tr>
<td>AVX Style 1206/CDR32 (BX)</td>
</tr>
<tr>
<td>CDR32BX472B--- 4,700 K.M BX 100</td>
</tr>
<tr>
<td>CDR32BX562B--- 5,600 K.M BX 100</td>
</tr>
<tr>
<td>CDR32BX682B--- 6,800 K.M BX 100</td>
</tr>
<tr>
<td>CDR32BX822B--- 8,200 K.M BX 100</td>
</tr>
<tr>
<td>CDR32BX103B--- 10,000 K.M BX 100</td>
</tr>
<tr>
<td>CDR32BX123B--- 12,000 K.M BX 100</td>
</tr>
<tr>
<td>CDR32BX153B--- 15,000 K.M BX 100</td>
</tr>
<tr>
<td>CDR32BX183A--- 18,000 K.M BX 50</td>
</tr>
<tr>
<td>CDR32BX223A--- 22,000 K.M BX 50</td>
</tr>
<tr>
<td>CDR32BX273A--- 27,000 K.M BX 50</td>
</tr>
<tr>
<td>CDR32BX333A--- 33,000 K.M BX 50</td>
</tr>
<tr>
<td>CDR32BX393A--- 39,000 K.M BX 50</td>
</tr>
</tbody>
</table>

Add appropriate failure rate  
Add appropriate termination finish  
Capacitance Tolerance

1/ The complete part number will include additional symbols to indicate capacitance tolerance, termination and failure rate level.
**MIL-PRF-55681/Chips**

**Military Part Number Identification CDR33/34/35**

<table>
<thead>
<tr>
<th>CDR33/34/35 to MIL-PRF-55681/9/10/11</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Military Type</strong></td>
</tr>
<tr>
<td><strong>Designation</strong></td>
</tr>
<tr>
<td>AVX Style 1210/CDR33 (BP)</td>
</tr>
<tr>
<td>CDR33BP102B---</td>
</tr>
<tr>
<td>CDR33BP112B---</td>
</tr>
<tr>
<td>CDR33BP122B---</td>
</tr>
<tr>
<td>CDR33BP132B---</td>
</tr>
<tr>
<td>CDR33BP152B---</td>
</tr>
<tr>
<td>CDR33BP162B---</td>
</tr>
<tr>
<td>CDR33BP182B---</td>
</tr>
<tr>
<td>CDR33BP202B---</td>
</tr>
<tr>
<td>CDR33BP222B---</td>
</tr>
<tr>
<td>CDR33BP242B---</td>
</tr>
<tr>
<td>CDR33BP272B---</td>
</tr>
<tr>
<td>CDR33BP302B---</td>
</tr>
<tr>
<td>CDR33BP332B---</td>
</tr>
<tr>
<td>AVX Style 1210/CDR33 (BX)</td>
</tr>
<tr>
<td>CDR33BX153B---</td>
</tr>
<tr>
<td>CDR33BX183B---</td>
</tr>
<tr>
<td>CDR33BX223B---</td>
</tr>
<tr>
<td>CDR33BX273B---</td>
</tr>
<tr>
<td>CDR33BX473A---</td>
</tr>
<tr>
<td>CDR33BX683A---</td>
</tr>
<tr>
<td>CDR33BX823A---</td>
</tr>
<tr>
<td>CDR33BX104A---</td>
</tr>
<tr>
<td>AVX Style 1812/CDR34 (BP)</td>
</tr>
<tr>
<td>CDR34BP222B---</td>
</tr>
<tr>
<td>CDR34BP242B---</td>
</tr>
<tr>
<td>CDR34BP272B---</td>
</tr>
<tr>
<td>CDR34BP302B---</td>
</tr>
<tr>
<td>CDR34BP332B---</td>
</tr>
<tr>
<td>CDR34BP362B---</td>
</tr>
<tr>
<td>CDR34BP472B---</td>
</tr>
<tr>
<td>CDR34BP512B---</td>
</tr>
<tr>
<td>CDR34BP562B---</td>
</tr>
<tr>
<td>CDR34BP622B---</td>
</tr>
<tr>
<td>CDR34BP682B---</td>
</tr>
<tr>
<td>CDR34BP752B---</td>
</tr>
<tr>
<td>CDR34BP822B---</td>
</tr>
<tr>
<td>AVX Style 1812/CDR34 (BX)</td>
</tr>
<tr>
<td>CDR34BX273B---</td>
</tr>
<tr>
<td>CDR34BX333B---</td>
</tr>
<tr>
<td>CDR34BX393B---</td>
</tr>
<tr>
<td>CDR34BX473B---</td>
</tr>
<tr>
<td>CDR34BX563B---</td>
</tr>
<tr>
<td>CDR34BX104A---</td>
</tr>
<tr>
<td>CDR34BX124A---</td>
</tr>
<tr>
<td>CDR34BX154A---</td>
</tr>
<tr>
<td>CDR34BX184A---</td>
</tr>
<tr>
<td>AVX Style 1825/CDR35 (BP)</td>
</tr>
<tr>
<td>CDR35BP472B---</td>
</tr>
<tr>
<td>CDR35BP512B---</td>
</tr>
<tr>
<td>CDR35BP562B---</td>
</tr>
<tr>
<td>CDR35BP622B---</td>
</tr>
<tr>
<td>CDR35BP682B---</td>
</tr>
<tr>
<td>CDR35BP752B---</td>
</tr>
<tr>
<td>CDR35BP822B---</td>
</tr>
<tr>
<td>CDR35BP912B---</td>
</tr>
<tr>
<td>CDR35BP103B---</td>
</tr>
<tr>
<td>CDR35BP113A---</td>
</tr>
<tr>
<td>CDR35BP123A---</td>
</tr>
<tr>
<td>CDR35BP133A---</td>
</tr>
<tr>
<td>CDR35BP153A---</td>
</tr>
<tr>
<td>CDR35BP163A---</td>
</tr>
<tr>
<td>CDR35BP183A---</td>
</tr>
<tr>
<td>CDR35BP203A---</td>
</tr>
<tr>
<td>CDR35BP223A---</td>
</tr>
<tr>
<td>AVX Style 1825/CDR35 (BX)</td>
</tr>
<tr>
<td>CDR35BX563B---</td>
</tr>
<tr>
<td>CDR35BX683B---</td>
</tr>
<tr>
<td>CDR35BX823B---</td>
</tr>
<tr>
<td>CDR35BX104B---</td>
</tr>
<tr>
<td>CDR35BX124B---</td>
</tr>
<tr>
<td>CDR35BX154B---</td>
</tr>
<tr>
<td>CDR35BX184A---</td>
</tr>
<tr>
<td>CDR35BX204A---</td>
</tr>
<tr>
<td>CDR35BX224A---</td>
</tr>
</tbody>
</table>

1/ The complete part number will include additional symbols to indicate capacitance tolerance, termination and failure rate level.
AVX offers a wide variety of medically qualified passive components. Medical devices require the utmost reliability with respect to the components incorporated into the designs. Advanced design qualification requirements, in-process controls, requirements, and lot acceptance testing are implemented to ensure these components will meet the superior reliability levels of a life supporting application. AVX medical MLCC reliability documents provide an advanced level of designing, manufacturing, testing and qualification that places AVX as the top supplier and industry leader of medically qualified MLCCs.

AVX MQ series of medically qualified ceramic capacitors are available in EIA case sizes ranging from 0402 to 2225, at typical voltage ratings between 4 – 200 Vdc with various termination options including Sn, SnPb solder, and Au.

## APPLICATIONS
- Implantable cardioverter-defibrillator (ICD)
- Pacemakers
- Neuromodulation

## FEATURES
- 0402 to 2225 case sizes
- Voltage range from 4v to 100v
- Capacitance up to 100μF
- Class I & II dielectric materials
- Tight tolerances on Class I dielectric materials
- Various terminations
- Customer specific requirements, screening, & testing

## GENERAL DESCRIPTION

### HOW TO ORDER

<table>
<thead>
<tr>
<th>MQ02</th>
<th>Z</th>
<th>A</th>
<th>100</th>
<th>J</th>
<th>G</th>
<th>T</th>
<th>3</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>Rated Voltage</td>
<td>Dielectric Code</td>
<td>Capacitance Code (In pF)</td>
<td>Capacitance Tolerance</td>
<td>Medical Grade</td>
<td>Termination Finish</td>
<td>Packaging</td>
<td>Special Code</td>
</tr>
<tr>
<td>MQ02 = 0402</td>
<td>4 = 4V 6 = 6.3V 8 = 10V 3 = 16V 5 = 50V 1 = 100V</td>
<td>A = NP0 (C0G) C = X7R Z = X7S D = X5R</td>
<td>(2 significant digits + no. of zeros)</td>
<td>For Values &lt; 10pF B = ±0.1pF C = ±0.25pF D = ±0.5pF</td>
<td>J = Plated Ni &amp; Sn</td>
<td>T = Plated Ni &amp; Sn B = 60/40 Sn/Pb Plated Solder 7 = Gold Plated</td>
<td>1 = 7” Reel 2 = 7” Reel (0402 only) 3 = 13” Reel 4 = 13” Reel (0402 only) 6 = Waffle</td>
<td>A = Standard Contact AVX for others</td>
</tr>
<tr>
<td>MQ03 = 0603</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ05 = 0805</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ06 = 1206</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ10 = 1210</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ12 = 1812</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ13 = 1825</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ14 = 2225</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ10 = 1210</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ12 = 1812</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ13 = 1825</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ14 = 2225</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ10 = 1210</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ12 = 1812</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ13 = 1825</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MQ14 = 2225</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
**MQ Series – Medical Grade MLCC**

NP0 (C0G) – Capacitance & Voltage Range

**PREFERRED SIZES ARE SHADED**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1812</th>
<th>1825</th>
<th>2225</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow Only</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>Packaging</td>
<td>All Paper</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
</tr>
<tr>
<td>(L) Length</td>
<td>mm</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>1.00 ± 0.10</td>
<td>1.60 ± 0.15</td>
<td>2.01 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>4.50 ± 0.30</td>
<td>4.50 ± 0.30</td>
<td>5.72 ± 0.25</td>
</tr>
<tr>
<td></td>
<td>(0.040 ± 0.004)</td>
<td>(0.063 ± 0.006)</td>
<td>(0.079 ± 0.008)</td>
<td>(0.126 ± 0.008)</td>
<td>(0.126 ± 0.008)</td>
<td>(0.177 ± 0.012)</td>
<td>(0.177 ± 0.012)</td>
<td>(0.225 ± 0.010)</td>
</tr>
<tr>
<td>(W) Width</td>
<td>mm</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.50 ± 0.10</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
<td>1.60 ± 0.20</td>
<td>2.50 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>6.40 ± 0.40</td>
<td>6.35 ± 0.25</td>
</tr>
<tr>
<td></td>
<td>(0.020 ± 0.004)</td>
<td>(0.032 ± 0.006)</td>
<td>(0.049 ± 0.008)</td>
<td>(0.063 ± 0.008)</td>
<td>(0.098 ± 0.008)</td>
<td>(0.126 ± 0.008)</td>
<td>(0.252 ± 0.016)</td>
<td>(0.250 ± 0.010)</td>
</tr>
<tr>
<td>(t) Terminal</td>
<td>mm</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.25 ± 0.15</td>
<td>0.35 ± 0.15</td>
<td>0.50 ± 0.25</td>
<td>0.50 ± 0.25</td>
<td>0.50 ± 0.25</td>
<td>0.61 ± 0.36</td>
<td>0.61 ± 0.36</td>
<td>0.94 ± 0.39</td>
</tr>
<tr>
<td></td>
<td>(0.010 ± 0.006)</td>
<td>(0.037)</td>
<td>(0.020 ± 0.010)</td>
<td>(0.020 ± 0.010)</td>
<td>(0.020 ± 0.010)</td>
<td>(0.024 ± 0.014)</td>
<td>(0.024 ± 0.014)</td>
<td>(0.025 ± 0.015)</td>
</tr>
<tr>
<td>Maximum Thickness</td>
<td>mm</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0.56</td>
<td>0.94</td>
<td>1.52</td>
<td>1.78</td>
<td>1.78</td>
<td>2.79</td>
<td>2.79</td>
<td>2.79</td>
</tr>
<tr>
<td></td>
<td>(0.022)</td>
<td>(0.037)</td>
<td>(0.070)</td>
<td>(0.070)</td>
<td>(0.070)</td>
<td>(0.110)</td>
<td>(0.110)</td>
<td>(0.110)</td>
</tr>
<tr>
<td>WVDC</td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>6.3</td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>100</td>
</tr>
<tr>
<td></td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>100</td>
<td>25</td>
<td>50</td>
<td>100</td>
<td>100</td>
</tr>
<tr>
<td></td>
<td>10</td>
<td>12</td>
<td>5</td>
<td>25</td>
<td>50</td>
<td>100</td>
<td>25</td>
<td>50</td>
</tr>
<tr>
<td></td>
<td>100</td>
<td>120</td>
<td>150</td>
<td>180</td>
<td>220</td>
<td>270</td>
<td>330</td>
<td>390</td>
</tr>
<tr>
<td></td>
<td>470</td>
<td>560</td>
<td>680</td>
<td>820</td>
<td>1000</td>
<td>1200</td>
<td>1500</td>
<td>1800</td>
</tr>
<tr>
<td></td>
<td>2200</td>
<td>2700</td>
<td>3300</td>
<td>3900</td>
<td>4700</td>
<td>5600</td>
<td>6800</td>
<td>8200</td>
</tr>
<tr>
<td></td>
<td>10000</td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>6.3</td>
<td>16</td>
<td>25</td>
<td>50</td>
</tr>
<tr>
<td></td>
<td>100</td>
<td>25</td>
<td>50</td>
<td>100</td>
<td>25</td>
<td>50</td>
<td>100</td>
<td>100</td>
</tr>
</tbody>
</table>

**Cap (pF)**

- 0.5
- 1.0
- 1.2
- 1.5
- 1.8
- 2.2
- 2.7
- 3.3
- 3.9
- 4.7
- 5.6
- 6.8
- 8.2
- 10
- 12
- 15
- 18
- 22
- 27
- 33
- 39
- 47
- 56
- 68
- 82
- 100
- 120
- 150
- 180
- 220
- 270
- 330
- 390
- 470
- 560
- 680
- 820
- 1000
- 1200
- 1500
- 1800
- 2200
- 2700
- 3300
- 3900
- 4700
- 5600
- 6800
- 8200
- 10000

**WVDC**

- 16
- 25
- 50
- 6.3
- 16
- 25
- 50
- 100
- 16
- 25
- 50
- 100
- 25
- 50
- 100
- 50
- 100
- 25
- 50
- 100
- 25
- 50
- 100
- 25
- 50
- 100
- 25
- 50
- 100
- 25
- 50
- 100
- 25
- 50
- 100
- 25
- 50
- 100
- 25
- 50
- 100
MQ Series – Medical Grade MLCC
NP0 (C0G) – General Specifications

TYPICAL ELECTRICAL CHARACTERISTICS

- **Temperature Coefficient**
  - Typical Capacitance Change
    - Envelope: 0 30 ppm/C

- **Capacitance vs. Frequency**

- **Variation of Impedance with Cap Value**
  - Impedance vs. Frequency
    - 0805 - C0G (NP0)
    - 10 pF vs. 100 pF vs. 1000 pF

- **Variation of Impedance with Ceramic Formulation**
  - Impedance vs. Frequency
    - 1000 pF - C0G (NP0) vs. X7R 0805

- **Variation of Impedance with Chip Size**
  - Impedance vs. Frequency
    - 1000 pF - C0G (NP0)

- **Insulation Resistance vs. Temperature**

- **Insulation Resistance vs. Frequency**

- **Impedance vs. Frequency**
  - X7R NPO
  - 0805

050516
# MQ Series – Medical Grade MLCC

**NP0 (C0G) – Specifications & Test Methods**

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>NP0 Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55°C to +125°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
</tbody>
</table>
| Capacitance | Within specified tolerance | Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF
1.0 kHz ± 10% for cap > 1000 pF
Voltage: 1.0Vrms ± .2V |
| Q |  <30 pF: Q≥400+20 x Cap Value ≥30 pF: Q≥1000 | Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity |
| **Insulation Resistance** | 100,000MΩ or 1000MΩ - μF, whichever is less | Charge device with 250% of rated voltage for 1-5 seconds, with charge and discharge current limited to 50 mA (max) |
| **Dielectric Strength** | No breakdown or visual defects | Deflection: 2mm
Test Time: 30 seconds |

<table>
<thead>
<tr>
<th>Resistance to Flexure Stresses</th>
<th>Appearance</th>
<th>No defects</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Variation</td>
<td>±5% or ±.5 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
</tbody>
</table>

| Solderability | ≥ 85% of each terminal should be covered with fresh solder |

<table>
<thead>
<tr>
<th>Resistance to Solder Heat</th>
<th>Appearance</th>
<th>No defects, &lt;25% leaching of either end terminal</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±2.5% or ±.25 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Thermal Shock</th>
<th>Appearance</th>
<th>No visual defects</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±3.0% or ±.3 pF, whichever is greater</td>
<td></td>
</tr>
<tr>
<td>Q</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Load Life</th>
<th>Appearance</th>
<th>No visual defects</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±3.0% or ±.3 pF, whichever is greater</td>
<td></td>
</tr>
</tbody>
</table>
| Q | ≥ 30 pF: Q≥350
≥10 pF, <30 pF: Q≥275 +5°C/2
<10 pF: Q≥200 +10°C |
| Insulation Resistance | ≥ Initial Value x 0.3 (See Above) |
| Dielectric Strength | Meets Initial Values (As Above) |

<table>
<thead>
<tr>
<th>Load Humidity</th>
<th>Appearance</th>
<th>No visual defects</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
</tbody>
</table>

---

**MIL-STD-202 / Method 210 / Condition J**

(Reflow Mounting plus 1 Reflow Cycle @ 235°C ± 5°C)
MQ Series – Medical Grade MLCC

X7R/X7S – General Specifications

PREFERRED SIZES ARE SHADED

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1812</th>
<th>1825</th>
<th>2225</th>
</tr>
</thead>
<tbody>
<tr>
<td>Selling</td>
<td>Reflow Only</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow/Wave</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>Packaging</td>
<td>All Paper</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
</tr>
<tr>
<td>(L) Length</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
</tr>
<tr>
<td>(W) Width</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
</tr>
<tr>
<td>(T) Terminal</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
</tr>
<tr>
<td>Maximum Thickness</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
<td>mm</td>
<td>(in.)</td>
</tr>
<tr>
<td>WVDC</td>
<td>10</td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>10</td>
<td>16</td>
<td>25</td>
<td>50</td>
</tr>
<tr>
<td>Cap (μF)</td>
<td>110</td>
<td>150</td>
<td>200</td>
<td>330</td>
<td>470</td>
<td>680</td>
<td>1000</td>
<td>1500</td>
</tr>
<tr>
<td>Cap (μF)</td>
<td>0.010</td>
<td>0.015</td>
<td>0.022</td>
<td>0.033</td>
<td>0.047</td>
<td>0.068</td>
<td>0.10</td>
<td>0.15</td>
</tr>
<tr>
<td>WVDC</td>
<td>30</td>
<td>50</td>
<td>75</td>
<td>100</td>
<td>30</td>
<td>50</td>
<td>75</td>
<td>100</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1812</th>
<th>1825</th>
<th>2225</th>
</tr>
</thead>
<tbody>
<tr>
<td>WVDC</td>
<td>30</td>
<td>50</td>
<td>75</td>
<td>100</td>
<td>30</td>
<td>50</td>
<td>75</td>
<td>100</td>
</tr>
</tbody>
</table>

081718
MQ Series – Medical Grade MLCC
X7R/X7S – General Specifications

TYPICAL ELECTRICAL CHARACTERISTICS

X7R Dielectric
Typical Temperature Coefficient

Δ Capacitance vs. Frequency

Variation of Impedance with Cap Value
Impedance vs. Frequency
1,000 pF vs. 10,000 pF - X7R
0805

Insulation Resistance vs. Temperature

Variation of Impedance with Chip Size
Impedance vs. Frequency
10,000 pF - X7R

Variation of Impedance with Chip Size
Impedance vs. Frequency
100,000 pF - X7R
# MQ Series – Medical Grade MLCC

## X7R/X7S – Specifications & Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>NP0 Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55ºC to +125ºC</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td></td>
</tr>
<tr>
<td><strong>Dissipation Factor</strong></td>
<td>≤ 2.5% for ≥ 50V DC rating</td>
<td></td>
</tr>
<tr>
<td></td>
<td>≤ 3.0% for 25V &amp; 35V DC rating</td>
<td></td>
</tr>
<tr>
<td></td>
<td>≤ 12.5% for 16V DC rating and lower</td>
<td></td>
</tr>
<tr>
<td>Contact factory for DF by PN</td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Insulation Resistance</strong></td>
<td>100,000MΩ or 1000MΩ - μF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>No breakdown or visual defects</td>
<td>Charge device with 250% of rated voltage for 1-5 seconds, with charge and discharge current limited to 50 mA (max)</td>
</tr>
</tbody>
</table>

### Resistance to Flexure Stresses

| Appearance | No defects |
| Capcitance Variation | ≤ ±12% |
| Dissipation Factor | Meets Initial Values (As Above) |
| Insulation Resistance | ≥ Initial Value x 0.3 |

### Solderability

| Appearance | ≥ 85% of each terminal should be covered with fresh solder |
| Capacitance Variation | ≤ ±7.5% |
| Dissipation Factor | Meets Initial Values (As Above) |
| Insulation Resistance | Meets Initial Values (As Above) |

### Resistance to Solder Heat

| Appearance | No defects, <25% leaching of either end terminal |
| Capacitance Variation | ≤ ±7.5% |
| Dissipation Factor | Meets Initial Values (As Above) |
| Insulation Resistance | Meets Initial Values (As Above) |

### Thermal Shock

| Appearance | No visual defects |
| Capacitance Variation | ≤ ±7.5% |
| Dissipation Factor | Meets Initial Values (As Above) |
| Insulation Resistance | Meets Initial Values (As Above) |
| Dielectric Strength | Meets Initial Values (As Above) |

### Load Life

| Appearance | No visual defects |
| Capacitance Variation | ≤ ±12.5% |
| Dissipation Factor | ≤ initial value x 2.0 (see above) |
| Insulation Resistance | ≥ Initial Value x 0.3 (See Above) |
| Dielectric Strength | Meets Initial Values (As Above) |

### Load Humidity

| Appearance | No visual defects |
| Insulation Resistance | ≥ Initial Value x 0.3 (See Above) |

---

**MIL-STD-202 / Method 210 / Condition J** (Reflow Mounting plus 1 Reflow Cycle @ 235°C ± 5°C)

- **Load in test chamber set at 125°C ± 2°C** for 1000 hours (+48, -0) with twice rated voltage applied.
- Remove from test chamber and stabilize at room temperature before measuring.

- **Load in a test chamber set at 85°C ± 2°C/85% ± 5% relative humidity** for 1000 hours (+48, -0) with rated voltage applied.
- Remove from chamber and stabilize at room temperature before measuring.
## PREFERRED SIZES ARE SHAPED

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1812</th>
</tr>
</thead>
<tbody>
<tr>
<td>Soldering</td>
<td>Reflow Only</td>
<td>Reflow/ Wave</td>
<td>Reflow/ Wave</td>
<td>Reflow/ Wave</td>
<td>Reflow Only</td>
<td>Reflow Only</td>
</tr>
<tr>
<td>Packaging</td>
<td>All Paper</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
<td>All Embossed</td>
</tr>
<tr>
<td>(L) Length</td>
<td>mm</td>
<td>1.00 ± 0.10</td>
<td>1.60 ± 0.15</td>
<td>2.01 ± 0.20</td>
<td>3.20 ± 0.20</td>
<td>3.20 ± 0.20</td>
</tr>
<tr>
<td>(W) Width</td>
<td>mm</td>
<td>0.50 ± 0.10</td>
<td>0.81 ± 0.15</td>
<td>1.25 ± 0.20</td>
<td>1.60 ± 0.20</td>
<td>2.50 ± 0.20</td>
</tr>
<tr>
<td>(t) Terminal</td>
<td>mm</td>
<td>0.25 ± 0.15</td>
<td>0.35 ± 0.15</td>
<td>0.50 ± 0.25</td>
<td>0.50 ± 0.25</td>
<td>0.50 ± 0.25</td>
</tr>
<tr>
<td>Maximum Thickness</td>
<td>mm</td>
<td>0.56 (0.022)</td>
<td>0.94 (0.014 ± 0.006)</td>
<td>1.52 (0.060)</td>
<td>1.78 (0.070)</td>
<td>1.78 (0.070)</td>
</tr>
<tr>
<td>WVDC</td>
<td>4</td>
<td>6.3</td>
<td>10</td>
<td>16</td>
<td>25</td>
<td>50</td>
</tr>
<tr>
<td>Cap (μF)</td>
<td>0.01</td>
<td>0.015</td>
<td>0.015</td>
<td>0.007</td>
<td>0.015</td>
<td>0.015</td>
</tr>
<tr>
<td>WVDC</td>
<td>4</td>
<td>6.3</td>
<td>10</td>
<td>16</td>
<td>25</td>
<td>50</td>
</tr>
</tbody>
</table>

### TYPICAL ELECTRICAL CHARACTERISTICS

- **Temperature Coefficient**
- **Insulation Resistance vs. Temperature**
MQ Series – Medical Grade MLCC

X5R – Specifications & Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X5R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating Temperature Range</td>
<td>-55°C to +85°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td></td>
</tr>
<tr>
<td></td>
<td>≤ 2.5% for ≥ 50V DC rating</td>
<td></td>
</tr>
<tr>
<td></td>
<td>≤ 3.0% for 25V, 35V DC rating</td>
<td></td>
</tr>
<tr>
<td></td>
<td>≤ 12.5% Max. for 16V DC rating and lower Contact Factory for DF by PN</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Freq.: 1.0 kHz ± 10% Voltage: 1.0Vrms ± .2V</td>
<td></td>
</tr>
<tr>
<td></td>
<td>For Cap &gt; 10 μF, 0.5Vrms @ 120Hz</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>10,000MΩ or 500MΩ - μF, whichever is less</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>No breakdown or visual defects</td>
<td>Charge device with 150% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max)</td>
</tr>
<tr>
<td>Resistance to Flexure Stresses</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects</td>
<td>Deflection: 2mm</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12%</td>
<td>Test Time: 30 seconds</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>No breakdown or visual defects</td>
<td></td>
</tr>
<tr>
<td>Solderability</td>
<td>≥ 85% of each terminal should be covered with fresh solder</td>
<td>Dip device in eutectic solder at 245 ± 5°C for 5.0 ± 0.5 seconds</td>
</tr>
<tr>
<td>Resistance to Solder Heat</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects, ≤25% leaching of either end terminal</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Thermal Shock</td>
<td>No visual defects</td>
<td>Step 1: -55°C ± 2º 30 ± 3 minutes</td>
</tr>
<tr>
<td>Appearance</td>
<td></td>
<td>Step 2: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td></td>
<td>Step 3: +85°C ± 2º 30 ± 3 minutes</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td></td>
<td>Step 4: Room Temp ≤ 3 minutes</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td></td>
<td>Repeat for 5 cycles and measure after 24 ± 2 hours at room temperature</td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Load Life</td>
<td>Load in test chamber set at 85°C ± 2°C for 1000 hours (+48, -0) with twice rated voltage applied</td>
<td>Load in a test chamber set at 85°C ± 2°C w/0% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td></td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td></td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
<tr>
<td>Dielectric Strength</td>
<td>Meets Initial Values (As Above)</td>
<td></td>
</tr>
<tr>
<td>Load Humidity</td>
<td>Load in a test chamber set at 85°C ± 2°C/85% ± 5%</td>
<td></td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td></td>
</tr>
</tbody>
</table>
The AVX MM series is a multi-layer ceramic capacitor designed for use in medical applications other than implantable/life support. These components have the design & change control expected for medical devices and also offer enhanced LAT including reliability testing and 100% inspection.

### APPLICATIONS

#### Implantable, Non-Life Supporting Medical Devices
- e.g. implanted temporary cardiac monitor, insulin pumps

#### External, Life Supporting Medical Devices
- e.g. heart pump external controller

#### External Devices
- e.g. patient monitoring, diagnostic equipment

### HOW TO ORDER

<table>
<thead>
<tr>
<th>MM02</th>
<th>Z</th>
<th>A</th>
<th>100</th>
<th>J</th>
<th>C</th>
<th>T</th>
<th>2</th>
<th>A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>Rate</td>
<td>Dielectric Code</td>
<td>Capacitance Code (In pF)</td>
<td>Capacitance Tolerance</td>
<td>Failure Rate</td>
<td>Termination Finish</td>
<td>Packaging</td>
<td>Special Code</td>
</tr>
<tr>
<td>MM02 = 0402</td>
<td>Z = 10V</td>
<td>A = NP0 (C0G)</td>
<td>B = ±0.1pF</td>
<td>C = Standard Range</td>
<td>T = Plated Ni &amp; Sn (NP0 only)</td>
<td>2 = 7” Reel</td>
<td>A = Standard</td>
<td></td>
</tr>
<tr>
<td>MM03 = 0603</td>
<td>Y = 16V</td>
<td>C = X7R</td>
<td>G = ±2% (≥10pF)</td>
<td>*Contact AVX for others</td>
<td>Z = Flexiterm (X7R only)</td>
<td>4 = 13” Reel</td>
<td>*Contact AVX for others</td>
<td></td>
</tr>
<tr>
<td>MM05 = 0805</td>
<td>3 = 25V</td>
<td></td>
<td>D = ±0.5pF</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MM10 = 1210</td>
<td>5 = 50V</td>
<td></td>
<td>F = ±1% (≥10pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MM08 = 1808</td>
<td>1 = 100V</td>
<td></td>
<td>G = ±2% (≥10pF)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MM12 = 1812</td>
<td>2 = 200V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MM20 = 2220</td>
<td>V = 250V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MM02 = 0402</td>
<td>7 = 500V</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### COMMERCIAL VS MM SERIES PROCESS COMPARISON

<table>
<thead>
<tr>
<th>Commercial</th>
<th>MM Series</th>
</tr>
</thead>
<tbody>
<tr>
<td>Administrative</td>
<td>Standard part numbers; no restriction on who purchases these parts</td>
</tr>
<tr>
<td>Design</td>
<td>Minimum ceramic thickness of 0.020&quot; on all X7R product</td>
</tr>
<tr>
<td>Dicing</td>
<td>Side &amp; end margins = 0.003&quot; min</td>
</tr>
<tr>
<td>Lot Qualification</td>
<td>As per EIA RS469</td>
</tr>
<tr>
<td>Destructive Physical Analysis (DPA)</td>
<td>Increased sample plan – stricter criteria</td>
</tr>
<tr>
<td>Visual/Cosmetic Quality</td>
<td>Standard process and inspection</td>
</tr>
<tr>
<td>Application Robustness</td>
<td>Standard sampling for accelerated wave solder on X7R dielectrics</td>
</tr>
<tr>
<td>Design/Change Control</td>
<td>Required to inform customer of changes in: form, fit, function</td>
</tr>
<tr>
<td>AVX will quality and notify customers before making any change to the following materials or processes:</td>
<td>AVX will quality and notify customers before making any change to the following materials or processes:</td>
</tr>
<tr>
<td>Dielectric formulation, type, or supplier</td>
<td>Dielectric formulation, type, or supplier</td>
</tr>
<tr>
<td>Metal formulation, type, or supplier</td>
<td>Metal formulation, type, or supplier</td>
</tr>
<tr>
<td>Termination material formulation, type, or supplier</td>
<td>Termination material formulation, type, or supplier</td>
</tr>
<tr>
<td>Manufacturing equipment type</td>
<td>Manufacturing equipment type</td>
</tr>
<tr>
<td>Quality testing regime including sample size and accept/ reject criteria</td>
<td>Quality testing regime including sample size and accept/ reject criteria</td>
</tr>
</tbody>
</table>
**MM Series – MLCC for Medical Applications**

**NP0 (C0G) – Specifications & Test Methods**

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>NP0 Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55°C to +125°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td><strong>Capacitance</strong></td>
<td>Within specified tolerance</td>
<td>Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF 1.0 kHz ± 10% for cap &gt; 1000 pF Voltage: 1.0 Vrms ± 0.2 V</td>
</tr>
</tbody>
</table>
| **Q** | <30 pF: Q ≥ 400 x 20 x Cap Value  
>30 pF: Q ≥ 1000 | Charge device with rated voltage for 60 ± 5 secs @ room temp/humidity |
| **Insulation Resistance** | 100,000 MΩ or 1000 MΩ - μF, whichever is less | |
| **Dielectric Strength** | No breakdown or visual defects | |
| **Resistance to Flexure Stresses** | | |
| Appearance | No defects | |
| Capacitance Variation | ±5% or ±5 pF, whichever is greater | |
| Q | Meets Initial Values (As Above) | |
| Insulation Resistance | ≥ Initial Value x 0.3 | |
| **Solderability** | ≥ 95% of each terminal should be covered with fresh solder | Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds |
| **Resistance to Solder Heat** | | |
| Appearance | No defects, <25% leaching of either end terminal | |
| Capacitance Variation | ≤ ±2.5% or ±25 pF, whichever is greater | |
| Q | Meets Initial Values (As Above) | |
| Insulation Resistance | Meets Initial Values (As Above) | |
| Dielectric Strength | Meets Initial Values (As Above) | |
| **Thermal Shock** | | |
| Appearance | No visual defects | Step 1: -55°C ± 2°C  
30 ± 3 minutes |
| Capacitance Variation | ≤ ±2.5% or ±25 pF, whichever is greater | Step 2: Room Temp  ≤ 3 minutes |
| Q | Meets Initial Values (As Above) | Step 3: +125°C ± 2°C  
30 ± 3 minutes |
| Insulation Resistance | Meets Initial Values (As Above) | Step 4: Room Temp  ≤ 3 minutes |
| Dielectric Strength | Meets Initial Values (As Above) | Repeat for 5 cycles and measure after 24 hours at room temperature |
| **Load Life** | | |
| Appearance | No visual defects | Charge device with twice rated voltage in test chamber set at 125°C ± 2°C for 1000 hours (+48, -0). Remove from test chamber and stabilize at room temperature for 24 hours before measuring. |
| Capacitance Variation | ≤ ±3.0% or ±30 pF, whichever is greater | |
| Q | ≥ 30 pF: Q ≥ 350  
≥10 pF, <30 pF: Q ≥ 275 +5C/2  
<10 pF: Q ≥ 200 +10C | |
| Insulation Resistance | ≥ Initial Value x 0.3 (See Above) | |
| Dielectric Strength | Meets Initial Values (As Above) | |
| **Load Humidity** | | |
| Appearance | No visual defects | Store in a test chamber set at 85°C ± 2°C/ 85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied. Remove from chamber and stabilize at room temperature for 24 ± 2 hours before measuring. |
| Capacitance Variation | ≤ ±5.0% or ±50 pF, whichever is greater | |
| Q | ≥ 30 pF: Q ≥ 350  
≥10 pF, <30 pF: Q ≥ 275 +5C/2  
<10 pF: Q ≥ 200 +10C | |
| Insulation Resistance | ≥ Initial Value x 0.3 (See Above) | |
| Dielectric Strength | Meets Initial Values (As Above) | |
### MM Series – MLCC for Medical Applications

NP0/C0G Capacitance Range

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
</tr>
</thead>
<tbody>
<tr>
<td>WVDC</td>
<td>16</td>
<td>25</td>
<td>50</td>
</tr>
<tr>
<td>Cap</td>
<td>0.5</td>
<td>0.8</td>
<td>1.0</td>
</tr>
<tr>
<td>(pF)</td>
<td>1.2</td>
<td>1.5</td>
<td>1.8</td>
</tr>
<tr>
<td></td>
<td>2.2</td>
<td>2.7</td>
<td>3.3</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>3.9</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.7</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>6.6</td>
</tr>
<tr>
<td></td>
<td>8.2</td>
<td>10</td>
<td>12</td>
</tr>
<tr>
<td></td>
<td>15</td>
<td>18</td>
<td>22</td>
</tr>
<tr>
<td></td>
<td>27</td>
<td>39</td>
<td>47</td>
</tr>
<tr>
<td></td>
<td>56</td>
<td>82</td>
<td>100</td>
</tr>
<tr>
<td></td>
<td>120</td>
<td>150</td>
<td>180</td>
</tr>
<tr>
<td></td>
<td>220</td>
<td>271</td>
<td>330</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
<tr>
<td></td>
<td>221</td>
<td>271</td>
<td>331</td>
</tr>
<tr>
<td></td>
<td>391</td>
<td>471</td>
<td>561</td>
</tr>
<tr>
<td></td>
<td>681</td>
<td>821</td>
<td>1000</td>
</tr>
<tr>
<td></td>
<td>122</td>
<td>150</td>
<td>182</td>
</tr>
</tbody>
</table>
# MM Series – MLCC for Medical Applications

## X7R Specifications and Test Methods

<table>
<thead>
<tr>
<th>Parameter/Test</th>
<th>X7R Specification Limits</th>
<th>Measuring Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating Temperature Range</strong></td>
<td>-55°C to +125°C</td>
<td>Temperature Cycle Chamber</td>
</tr>
<tr>
<td>Capacitance</td>
<td>Within specified tolerance</td>
<td>Freq.: 1.0 kHz ± 10% Voltage: 1.0Vrms ± .2V</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ 10% for ≥ 50V DC rating ≤ 12.5% for 25V DC rating ≤ 12.5% for 25V and 16V DC rating ≤ 12.5% for ≤ 10V DC rating</td>
<td>Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>100,000MΩ or 1000MΩ - μF, whichever is less</td>
<td>Charge device with 300% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>No breakdown or visual defects</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td><strong>Resistance to Flexure Stresses</strong></td>
<td></td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12%</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td><strong>Solderability</strong></td>
<td>≥ 95% of each terminal should be covered with fresh solder</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Appearance</td>
<td>No defects, ≤25% leaching of either end terminal</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td><strong>Resistance to Solder Heat</strong></td>
<td></td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±7.5%</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>Meets Initial Values (As Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>Meets Initial Values (As Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td><strong>Thermal Shock</strong></td>
<td></td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td><strong>Load Life</strong></td>
<td></td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td><strong>Load Humidity</strong></td>
<td></td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Appearance</td>
<td>No visual defects</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Capacitance Variation</td>
<td>≤ ±12.5%</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Dissipation Factor</td>
<td>≤ Initial Value x 2.0 (See Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td>Insulation Resistance</td>
<td>≥ Initial Value x 0.3 (See Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
<tr>
<td><strong>Dielectric Strength</strong></td>
<td>Meets Initial Values (As Above)</td>
<td>Deflection: 2mm Test Time: 30 seconds</td>
</tr>
</tbody>
</table>

**Note:** Charge device with 150% of rated voltage for 500V devices.

**Load Life:** Charge device with 1.5 rated voltage (≤ 10V) in test chamber set at 125°C ± 2°C for 1000 hours (+48, -0)

**Remove from test chamber and stabilize at room temperature for 24 ± 2 hours before measuring.**

**Load Humidity:** Store in a test chamber set at 85°C ± 2°C/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.

**Remove from chamber and stabilize at room temperature and humidity for 24 ± 2 hours before measuring.**
### MM Series – MLCC for Medical Applications

**X7R Capacitance Range**

**PREFERRED SIZES ARE SHADED**

<table>
<thead>
<tr>
<th>SIZE</th>
<th>0402</th>
<th>0603</th>
<th>0805</th>
<th>1206</th>
<th>1210</th>
<th>1808</th>
<th>1812</th>
<th>2220</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>W/Dc</td>
<td>16</td>
<td>25</td>
<td>50</td>
<td>10</td>
<td>16</td>
<td>25</td>
<td>50</td>
</tr>
<tr>
<td>Cap</td>
<td>220</td>
<td>221</td>
<td>222</td>
<td>223</td>
<td>224</td>
<td>225</td>
<td>226</td>
<td>227</td>
</tr>
<tr>
<td></td>
<td>270</td>
<td>271</td>
<td>272</td>
<td>273</td>
<td>274</td>
<td>275</td>
<td>276</td>
<td>277</td>
</tr>
<tr>
<td></td>
<td>330</td>
<td>331</td>
<td>332</td>
<td>333</td>
<td>334</td>
<td>335</td>
<td>336</td>
<td>337</td>
</tr>
<tr>
<td></td>
<td>390</td>
<td>391</td>
<td>392</td>
<td>393</td>
<td>394</td>
<td>395</td>
<td>396</td>
<td>397</td>
</tr>
<tr>
<td></td>
<td>470</td>
<td>471</td>
<td>472</td>
<td>473</td>
<td>474</td>
<td>475</td>
<td>476</td>
<td>477</td>
</tr>
<tr>
<td></td>
<td>680</td>
<td>681</td>
<td>682</td>
<td>683</td>
<td>684</td>
<td>685</td>
<td>686</td>
<td>687</td>
</tr>
<tr>
<td></td>
<td>820</td>
<td>821</td>
<td>822</td>
<td>823</td>
<td>824</td>
<td>825</td>
<td>826</td>
<td>827</td>
</tr>
<tr>
<td>PREFERRED SIZES ARE SHADED</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Packaging of Chip Components

Automatic Insertion Packaging

TAPE & REEL QUANTITIES

All tape and reel specifications are in compliance with RS481.

<table>
<thead>
<tr>
<th></th>
<th>4mm</th>
<th>8mm</th>
<th>12mm</th>
</tr>
</thead>
<tbody>
<tr>
<td>Paper or Embossed Carrier</td>
<td>0612, 0508, 0805, 1206, 1210</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Embossed Only</td>
<td>0101</td>
<td></td>
<td>1808</td>
</tr>
<tr>
<td>Paper Only</td>
<td>0101, 0201, 0306, 0402, 0603</td>
<td></td>
<td>2220, 2225</td>
</tr>
<tr>
<td>Qty. per Reel/7&quot; Reel</td>
<td>4,000</td>
<td>1,000, 2,000, 3,000 or 4,000, 10,000, 15,000, 20,000 Contact factory for exact quantity</td>
<td>3,000</td>
</tr>
<tr>
<td>Qty. per Reel/13&quot; Reel</td>
<td>5,000, 10,000, 50,000 Contact factory for exact quantity</td>
<td>10,000</td>
<td>4,000</td>
</tr>
</tbody>
</table>

REEL DIMENSIONS

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>4mm</td>
<td>1.80</td>
<td>1.5</td>
<td>13.0±0.5</td>
<td>20.2</td>
<td>60.0</td>
<td>4.35±0.3</td>
<td>7.95</td>
<td>40 (1.575) Min. Access Hole At Slot Location</td>
</tr>
<tr>
<td></td>
<td>(7.087)</td>
<td>(0.59)</td>
<td>(0.522±0.020)</td>
<td>(0.795)</td>
<td>(2.362)</td>
<td>(0.171±0.011)</td>
<td>(0.312)</td>
<td></td>
</tr>
<tr>
<td>8mm</td>
<td>330</td>
<td>1.5</td>
<td>13.0±0.5</td>
<td>20.2</td>
<td>50.0</td>
<td>8.40 (0.331)</td>
<td>14.4</td>
<td>13.0±0.5 (0.512±0.020)</td>
</tr>
<tr>
<td></td>
<td>(12.992)</td>
<td>(0.59)</td>
<td>(0.512±0.020)</td>
<td>(0.795)</td>
<td>(1.969)</td>
<td>(1.969)</td>
<td>(0.567)</td>
<td>(0.429)</td>
</tr>
<tr>
<td>12mm</td>
<td>12.4</td>
<td>1.5</td>
<td>12.4±0.5</td>
<td>20.2</td>
<td>18.4</td>
<td>12.4 (0.488)</td>
<td>18.4</td>
<td>12.4±0.5 (0.488)</td>
</tr>
<tr>
<td></td>
<td>(4.650)</td>
<td>(0.59)</td>
<td>(0.488)</td>
<td>(0.724)</td>
<td>(0.724)</td>
<td>(0.488)</td>
<td>(0.724)</td>
<td>(0.488)</td>
</tr>
</tbody>
</table>

Note: Tape with or without components shall pass around radius "R" without damage.

Metric dimensions will govern.
English measurements rounded and for reference only.
(1) For tape sizes 16mm and 24mm (used with chip size 3640) consult EIA RS-481 latest revision.
Embossed Carrier Configuration
4, 8 & 12mm Tape Only

4, 8 & 12mm Embossed Tape Metric Dimensions Will Govern

### CONSTANT DIMENSIONS

<table>
<thead>
<tr>
<th>Tape Size</th>
<th>D₀</th>
<th>E₁</th>
<th>P₀</th>
<th>P₂</th>
<th>S₁ Min.</th>
<th>T Max.</th>
<th>T₁ Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>4mm</td>
<td>0.80±0.04 (0.031±0.001)</td>
<td>0.90±0.05 (0.035±0.001)</td>
<td>2.0±0.04 (0.078±0.001)</td>
<td>1.00±0.02 (0.039±0.0007)</td>
<td>1.075</td>
<td>0.26 (0.010)</td>
<td>0.06 (0.002)</td>
</tr>
<tr>
<td>8mm &amp; 12mm</td>
<td>1.50 (0.059) (0.044)</td>
<td>1.75 ± 0.10 (0.069 ± 0.004)</td>
<td>4.0 ± 0.10 (0.157 ± 0.004)</td>
<td>2.0 ± 0.05 (0.079 ± 0.002)</td>
<td>0.60 (0.024)</td>
<td>0.10 (0.004)</td>
<td></td>
</tr>
</tbody>
</table>

### VARIABLE DIMENSIONS

<table>
<thead>
<tr>
<th>Tape Size</th>
<th>B₁ Max.</th>
<th>D₁ Min.</th>
<th>E₂ Min.</th>
<th>F</th>
<th>P₁</th>
<th>R Min.</th>
<th>See Note 5</th>
<th>T₂</th>
<th>W Max.</th>
<th>A₀ B₀ K₀</th>
</tr>
</thead>
<tbody>
<tr>
<td>8mm</td>
<td>4.35 (0.171)</td>
<td>1.00 (0.039)</td>
<td>6.25 (0.246)</td>
<td>3.50 ± 0.05 (0.138 ± 0.002)</td>
<td>4.00 ± 0.10 (0.157 ± 0.004)</td>
<td>25.0 (0.984)</td>
<td>2.50 Max. (0.098)</td>
<td>8.30 (0.327)</td>
<td>See Note 1</td>
<td></td>
</tr>
<tr>
<td>12mm</td>
<td>8.20 (0.323)</td>
<td>1.50 (0.059)</td>
<td>10.25 (0.404)</td>
<td>5.50 ± 0.05 (0.217 ± 0.002)</td>
<td>4.00 ± 0.10 (0.157 ± 0.004)</td>
<td>30.0 (1.181)</td>
<td>6.50 Max. (0.256)</td>
<td>12.3 (0.484)</td>
<td>See Note 1</td>
<td></td>
</tr>
<tr>
<td>8mm 1/2 Pitch</td>
<td>4.35 (0.171)</td>
<td>1.00 (0.039)</td>
<td>6.25 (0.246)</td>
<td>3.50 ± 0.05 (0.138 ± 0.002)</td>
<td>2.00 ± 0.10 (0.079 ± 0.004)</td>
<td>25.0 (0.984)</td>
<td>2.50 Max. (0.098)</td>
<td>8.30 (0.327)</td>
<td>See Note 1</td>
<td></td>
</tr>
<tr>
<td>12mm Double Pitch</td>
<td>8.20 (0.323)</td>
<td>1.50 (0.059)</td>
<td>10.25 (0.404)</td>
<td>5.50 ± 0.05 (0.217 ± 0.002)</td>
<td>8.00 ± 0.10 (0.315 ± 0.004)</td>
<td>30.0 (1.181)</td>
<td>6.50 Max. (0.256)</td>
<td>12.3 (0.484)</td>
<td>See Note 1</td>
<td></td>
</tr>
</tbody>
</table>

**NOTES:**
1. The cavity defined by A₀, B₀, and K₀ shall be configured to provide the following:
   a) The component does not protrude beyond the sealing plane of the cover tape.
   b) The component can be removed from the cavity in a vertical direction without mechanical restriction, after the cover tape has been removed.
   c) Rotation of the component is limited to 20° maximum (see Sketches D & E).
   d) Lateral movement of the component is restricted to 0.5mm maximum (see Sketch F).

2. Tape with or without components shall pass around radius “R” without damage.
3. Bar code labeling (if required) shall be on the side of the reel opposite the round sprocket holes. Refer to EIA-556.
4. B₁ dimension is a reference dimension for tape feeder clearance only.
5. If P₁ = 2.0mm, the tape may not properly index in all tape feeders.

**Top View, Sketch “F”**
Component Lateral Movements

Maximum Component Rotation

0.50mm (0.020) Maximum

**User Direction of Feed**

**20° maximum component rotation**

**Typical component cavity center line**

**Typical component center line**

**Chip Orientation**
Paper Carrier Configuration
8 & 12mm Tape Only

8 & 12mm Paper Tape
Metric Dimensions Will Govern

CONSTANT DIMENSIONS

<table>
<thead>
<tr>
<th>Tape Size</th>
<th>D₀</th>
<th>E₀</th>
<th>P₀</th>
<th>P₂</th>
<th>T₁</th>
<th>G. Min.</th>
<th>R Min.</th>
</tr>
</thead>
<tbody>
<tr>
<td>8mm &amp; 12mm</td>
<td>1.50 (+0.00) (-0.00)</td>
<td>1.75 ± 0.10</td>
<td>4.00 ± 0.10</td>
<td>2.00 ± 0.05</td>
<td>0.10</td>
<td>0.75</td>
<td>25.0 ((0.984))</td>
</tr>
</tbody>
</table>

VARIABLE DIMENSIONS

<table>
<thead>
<tr>
<th>Tape Size</th>
<th>P₁</th>
<th>See Note 4</th>
<th>E₂ Min.</th>
<th>F</th>
<th>W</th>
<th>A₀ B₀</th>
<th>T</th>
</tr>
</thead>
<tbody>
<tr>
<td>8mm</td>
<td>4.00 ± 0.10</td>
<td>((0.157 ± 0.004))</td>
<td>6.25 ((0.246))</td>
<td>3.50 ± 0.05</td>
<td>((0.138 ± 0.002))</td>
<td>8.00 ((0.315 ± 0.001))</td>
<td>See Note 1</td>
</tr>
<tr>
<td>12mm</td>
<td>4.00 ± 0.10</td>
<td>((0.157 ± 0.004))</td>
<td>10.25 ((0.404))</td>
<td>5.50 ± 0.05</td>
<td>((0.217 ± 0.002))</td>
<td>12.0 ± 0.30</td>
<td>((0.472 ± 0.012))</td>
</tr>
<tr>
<td>8mm 1/2 Pitch</td>
<td>2.00 ± 0.05</td>
<td>((0.079 ± 0.002))</td>
<td>6.25 ((0.246))</td>
<td>3.50 ± 0.05</td>
<td>((0.138 ± 0.002))</td>
<td>8.00 ((0.315 ± 0.001))</td>
<td></td>
</tr>
<tr>
<td>12mm Double Pitch</td>
<td>8.00 ± 0.10</td>
<td>((0.315 ± 0.004))</td>
<td>10.25 ((0.404))</td>
<td>5.50 ± 0.05</td>
<td>((0.217 ± 0.002))</td>
<td>12.0 ± 0.30</td>
<td>((0.472 ± 0.012))</td>
</tr>
</tbody>
</table>

NOTES:
1. The cavity defined by A₀, B₀, and T shall be configured to provide sufficient clearance surrounding the component so that:
   a) the component does not protrude beyond either surface of the carrier tape;
   b) the component can be removed from the cavity in a vertical direction without mechanical restriction after the top cover tape has been removed;
   c) rotation of the component is limited to 20° maximum (see Sketches A & B);
   d) lateral movement of the component is restricted to 0.5mm maximum (see Sketch C).
2. Tape with or without components shall pass around radius “R” without damage.
3. Bar code labeling (if required) shall be on the side of the reel opposite the sprocket holes. Refer to EIA-556.
4. If P₁ = 2.0mm, the tape may not properly index in all tape feeders.

Bar Code Labeling Standard
AVX bar code labeling is available and follows latest version of EIA-556
Basic Capacitor Formulas

I. Capacitance (farads)
   English: \( C = \frac{0.224 \, K \cdot A}{T_0} \)
   Metric: \( C = \frac{0.0884 \, K \cdot A}{T_0} \)

II. Energy stored in capacitors (Joules, watt - sec)
   \( E = \frac{1}{2} CV^2 \)

III. Linear charge of a capacitor (Amperes)
   \( I = C \frac{dV}{dt} \)

IV. Total Impedance of a capacitor (ohms)
   \( Z = \sqrt{R^2_s + (X_C - X_L)^2} \)

V. Capacitive Reactance (ohms)
   \( X_C = \frac{1}{2 \pi f C} \)

VI. Inductive Reactance (ohms)
   \( X_L = 2 \pi f L \)

VII. Phase Angles:
   Ideal Capacitors: Current leads voltage 90°
   Ideal Inductors: Current lags voltage 90°
   Ideal Resistors: Current in phase with voltage

VIII. Dissipation Factor (%)
   \( D.F. = \tan \delta \) (loss angle) = \( \frac{E.S.R.}{X_C} = (2 \pi fC) \) (E.S.R.)

IX. Power Factor (%)
   P.F. = \( \sin \delta \) (loss angle) = Cos \( \phi \) (phase angle)
   P.F. = (when less than 10%) = DF

X. Quality Factor (dimensionless)
   \( Q = \cotan \delta \) (loss angle) = \( \frac{1}{D.F.} \)

XI. Equivalent Series Resistance (ohms)
   \( E.S.R. = (D.F.) \frac{X_C}{(2 \pi fC)} \)

XII. Power Loss (watts)
   Power Loss = \( (2 \pi fCV^2) \) (D.F.)

XIII. KVA (Kilowatts)
   \( KVA = 2 \pi fCV^2 \times 10^{-3} \)

XIV. Temperature Characteristic (ppm/°C)
   \( T.C. = \frac{C_t - C_{25}}{C_{25} (T_t - 25)} \times 10^6 \)

XV. Cap Drift (%)
   \( C.D. = \frac{C_t - C_{25}}{C_{25}} \times 100 \)

XVI. Reliability of Ceramic Capacitors
   \( L_o = \left( \frac{V_t}{V_o} \right) X \left( \frac{T_t}{T_0} \right) Y \)

XVII. Capacitors in Series (current the same)
   Any Number: \( \frac{1}{C_T} = \frac{1}{C_1} + \frac{1}{C_2} + \cdots + \frac{1}{C_N} \)
   Two: \( C_T = \frac{C_1 \cdot C_2}{C_1 + C_2} \)

XVIII. Capacitors in Parallel (voltage the same)
   \( C_T = C_1 + C_2 + \cdots + C_N \)

XIX. Aging Rate
   A.R. = \% \( \Delta C \) decade of time

XX. Decibels
   \( \text{db} = 20 \log \frac{V_1}{V_2} \)

<table>
<thead>
<tr>
<th>METRIC PREFIXES</th>
<th>SYMBOLS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pico ( X 10^{-12} )</td>
<td>K = Dielectric Constant</td>
</tr>
<tr>
<td>Nano ( X 10^{-9} )</td>
<td>f = frequency</td>
</tr>
<tr>
<td>Micro ( X 10^{-6} )</td>
<td>A = Area</td>
</tr>
<tr>
<td>Milli ( X 10^{-3} )</td>
<td>L = Inductance</td>
</tr>
<tr>
<td>Deci ( X 10^{-1} )</td>
<td>T_D = Dielectric thickness</td>
</tr>
<tr>
<td>Deca ( X 10^{1} )</td>
<td>( \delta = ) Loss angle</td>
</tr>
<tr>
<td>Kilo ( X 10^{3} )</td>
<td>V = Voltage</td>
</tr>
<tr>
<td>Mega ( X 10^{6} )</td>
<td>( \phi = ) Phase angle</td>
</tr>
<tr>
<td>Giga ( X 10^{9} )</td>
<td>t = time</td>
</tr>
<tr>
<td>Tera ( X 10^{12} )</td>
<td>X &amp; Y = exponent effect of voltage and temp.</td>
</tr>
<tr>
<td>Series Resistance ( L_s )</td>
<td>R_s = Series Resistance</td>
</tr>
<tr>
<td>Test Life ( T_0 )</td>
<td>L_o = Operating life</td>
</tr>
</tbody>
</table>
General Description

Basic Construction – A multilayer ceramic (MLC) capacitor is a monolithic block of ceramic containing two sets of offset, interleaved planar electrodes that extend to two opposite surfaces of the ceramic dielectric. This simple structure requires a considerable amount of sophistication, both in material and manufacture, to produce it in the quality and quantities needed in today’s electronic equipment.

Formulations – Multilayer ceramic capacitors are available in both Class 1 and Class 2 formulations. Temperature compensating formulation are Class 1 and temperature stable and general application formulations are classified as Class 2.

Class 1 – Class 1 capacitors or temperature compensating capacitors are usually made from mixtures of titanates where barium titanate is normally not a major part of the mix. They have predictable temperature coefficients and in general, do not have an aging characteristic. Thus they are the most stable capacitor available. The most popular Class 1 multilayer ceramic capacitors are C0G (NP0) temperature compensating capacitors (negative-positive 0 ppm/°C).

Class 2 – EIA Class 2 capacitors typically are based on the chemistry of barium titanate and provide a wide range of capacitance values and temperature stability. The most commonly used Class 2 dielectrics are X7R and Y5V. The X7R provides intermediate capacitance values which vary only ±15% over the temperature range of -55°C to 125°C. It finds applications where stability over a wide temperature range is required.

The Y5V provides the highest capacitance values and is used in applications where limited temperature changes are expected. The capacitance value for Y5V can vary from 22% to -82% over the -30°C to 85°C temperature range.

All Class 2 capacitors vary in capacitance value under the influence of temperature, operating voltage (both AC and DC), and frequency. For additional information on performance changes with operating conditions, consult AVX's software, SpiCap.
In specifying capacitance change with temperature for Class 2 materials, EIA expresses the capacitance change over an operating temperature range by a 3 symbol code. The first symbol represents the cold temperature end of the temperature range, the second represents the upper limit of the operating temperature range and the third symbol represents the capacitance change allowed over the operating temperature range. Table 1 provides a detailed explanation of the EIA system.

Table 1: EIA and MIL Temperature Stable and General Application Codes

<table>
<thead>
<tr>
<th><strong>EIA CODE</strong></th>
<th>Percent Capacity Change Over Temperature Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>RS198</td>
<td>Temperature Range</td>
</tr>
<tr>
<td>X7</td>
<td>-55°C to +125°C</td>
</tr>
<tr>
<td>X6</td>
<td>-55°C to +105°C</td>
</tr>
<tr>
<td>X5</td>
<td>-55°C to +85°C</td>
</tr>
<tr>
<td>Y5</td>
<td>-30°C to +85°C</td>
</tr>
<tr>
<td>Z5</td>
<td>+10°C to +85°C</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Code</th>
<th>Percent Capacity Change</th>
</tr>
</thead>
<tbody>
<tr>
<td>D</td>
<td>±3.3%</td>
</tr>
<tr>
<td>E</td>
<td>±4.7%</td>
</tr>
<tr>
<td>F</td>
<td>±7.5%</td>
</tr>
<tr>
<td>P</td>
<td>±10%</td>
</tr>
<tr>
<td>R</td>
<td>±15%</td>
</tr>
<tr>
<td>S</td>
<td>±22%</td>
</tr>
<tr>
<td>T</td>
<td>+22%, -33%</td>
</tr>
<tr>
<td>U</td>
<td>+22%, -56%</td>
</tr>
<tr>
<td>V</td>
<td>+22%, -82%</td>
</tr>
</tbody>
</table>

EXAMPLE – A capacitor is desired with the capacitance value at 25°C to increase no more than 7.5% or decrease no more than 7.5% from -30°C to +85°C. EIA Code will be Y5F.

<table>
<thead>
<tr>
<th><strong>MIL CODE</strong></th>
<th>Symbol</th>
<th>Temperature Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>0</td>
<td>-55°C to +85°C</td>
</tr>
<tr>
<td>B</td>
<td>0</td>
<td>-55°C to +125°C</td>
</tr>
<tr>
<td>C</td>
<td>0</td>
<td>-55°C to +150°C</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>R</td>
<td>+15%, -15%</td>
<td>+15%, -40%</td>
</tr>
<tr>
<td>S</td>
<td>+22%, -22%</td>
<td>+22%, -56%</td>
</tr>
<tr>
<td>W</td>
<td>+22%, -56%</td>
<td>+22%, -66%</td>
</tr>
<tr>
<td>X</td>
<td>+15%, -15%</td>
<td>+15%, -25%</td>
</tr>
<tr>
<td>Y</td>
<td>+30%, -70%</td>
<td>+30%, -80%</td>
</tr>
<tr>
<td>Z</td>
<td>+20%, -20%</td>
<td>+20%, -30%</td>
</tr>
</tbody>
</table>

Effects of Voltage – Variations in voltage have little effect on Class 1 dielectric but does affect the capacitance and dissipation factor of Class 2 dielectrics. The application of DC voltage reduces both the capacitance and dissipation factor while the application of an AC voltage within a reasonable range tends to increase both capacitance and dissipation factor readings. If a high enough AC voltage is applied, eventually it will reduce capacitance just as a DC voltage will. Figure 2 shows the effects of AC voltage.

Capacitor specifications specify the AC voltage at which to measure (normally 0.5 or 1 VAC) and application of the wrong voltage can cause spurious readings. Figure 3 gives the voltage coefficient of dissipation factor for various AC voltages at 1 kilohertz. Applications of different frequencies will affect the percentage changes versus voltages.

In specifying capacitance change with temperature for Class 2 materials, EIA expresses the capacitance change over an operating temperature range by a 3 symbol code. The first symbol represents the cold temperature end of the temperature range, the second represents the upper limit of the operating temperature range and the third symbol represents the capacitance change allowed over the operating temperature range. Table 1 provides a detailed explanation of the EIA system.

Typical effect of the application of DC voltage is shown in Figure 4. The voltage coefficient is more pronounced for higher K dielectrics. These figures are shown for room temperature conditions. The combination characteristic known as voltage temperature limits which shows the effects of rated voltage over the operating temperature range is shown in Figure 5 for the military BX characteristic.
**General Description**

**Effects of Time** – Class 2 ceramic capacitors change capacitance and dissipation factor with time as well as temperature, voltage and frequency. This change with time is known as aging. Aging is caused by a gradual re-alignment of the crystalline structure of the ceramic and produces an exponential loss in capacitance and decrease in dissipation factor versus time. A typical curve of aging rate for semistable ceramics is shown in Figure 6.

If a Class 2 ceramic capacitor that has been sitting on the shelf for a period of time, is heated above its curie point, (125°C for 4 hours or 150°C for ¥¼ hour will suffice) the part will de-age and return to its initial capacitance and dissipation factor readings. Because the capacitance changes rapidly, immediately after de-aging, the basic capacitance measurements are normally referred to a time period sometime after the de-aging process. Various manufacturers use different time bases but the most popular one is one day or twenty-four hours after “last heat.” Change in the aging curve can be caused by the application of voltage and other stresses. The possible changes in capacitance due to de-aging by heating the unit explain why capacitance changes are allowed after test, such as temperature cycling, moisture resistance, etc., in MIL specs. The application of high voltages such as dielectric withstanding voltages also tends to de-age capacitors and is why re-reading of capacitance after 12 or 24 hours is allowed in military specifications after dielectric strength tests have been performed.

**Effects of Frequency** – Frequency affects capacitance and impedance characteristics of capacitors. This effect is much more pronounced in high dielectric constant ceramic formulation than in low K formulations. AVX’s SpiCap software generates impedance, ESR, series inductance, series resonant frequency and capacitance all as functions of frequency, temperature and DC bias for standard chip sizes and styles. It is available free from AVX and can be downloaded for free from AVX website: www.avx.com.
Effects of Mechanical Stress – High “K” dielectric ceramic capacitors exhibit some low level piezoelectric reactions under mechanical stress. As a general statement, the piezoelectric output is higher, the higher the dielectric constant of the ceramic. It is desirable to investigate this effect before using high “K” dielectrics as coupling capacitors in extremely low level applications.

Reliability – Historically ceramic capacitors have been one of the most reliable types of capacitors in use today. The approximate formula for the reliability of a ceramic capacitor is:

\[
\frac{L_0}{L_t} = \left(\frac{V_t}{V_o}\right)^X \left(\frac{T_t}{T_o}\right)^Y
\]

where

- \(L_0\) = operating life
- \(L_t\) = test life
- \(V_t\) = test voltage
- \(V_o\) = operating voltage
- \(T_t\) = test temperature and
- \(T_o\) = operating temperature

Historically for ceramic capacitors exponent X has been considered as 3. The exponent Y for temperature effects typically tends to run about 8.

A capacitor is a component which is capable of storing electrical energy. It consists of two conductive plates (electrodes) separated by insulating material which is called the dielectric. A typical formula for determining capacitance is:

\[
C = \frac{0.224KA}{t}
\]

where

- \(C\) = capacitance (picofarads)
- \(K\) = dielectric constant (Vacuum = 1)
- \(A\) = area in square inches
- \(t\) = separation between the plates in inches (thickness of dielectric)
- 0.224 = conversion constant (.0884 for metric system in cm)

Capacitance – The standard unit of capacitance is the farad. A capacitor has a capacitance of 1 farad when 1 coulomb charges it to 1 volt. One farad is a very large unit and most capacitors have values in the micro (10^-6), nano (10^-9) or pico (10^-12) farad level.

Dielectric Constant – In the formula for capacitance given above the dielectric constant of a vacuum is arbitrarily chosen as the number 1. Dielectric constants of other materials are then compared to the dielectric constant of a vacuum.

Dielectric Thickness – Capacitance is indirectly proportional to the separation between electrodes. Lower voltage requirements mean thinner dielectrics and greater capacitance per volume.

Area – Capacitance is directly proportional to the area of the electrodes. Since the other variables in the equation are usually set by the performance desired, area is the easiest parameter to modify to obtain a specific capacitance within a material group.

Energy Stored – The energy which can be stored in a capacitor is given by the formula:

\[
E = \frac{1}{2}CV^2
\]

where

- \(E\) = energy in joules (watts-sec)
- \(V\) = applied voltage
- \(C\) = capacitance in farads

Potential Change – A capacitor is a reactive component which reacts against a change in potential across it. This is shown by the equation for the linear charge of a capacitor:

\[
I_{\text{ideal}} = C \frac{dV}{dt}
\]

where

- \(I\) = Current
- \(C\) = Capacitance
- \(dV/dt\) = Slope of voltage transition across capacitor

Thus an infinite current would be required to instantly change the potential across a capacitor. The amount of current a capacitor can “sink” is determined by the above equation.

Equivalent Circuit – A capacitor, as a practical device, exhibits not only capacitance but also resistance and inductance. A simplified schematic for the equivalent circuit is:

\[
C = \frac{0.224KA}{t}
\]

where

- \(C\) = Capacitance
- \(R_s\) = Series Resistance
- \(R_p\) = Parallel Resistance

Reactance – Since the insulation resistance (\(R_p\)) is normally very high, the total impedance of a capacitor is:

\[
Z = \sqrt{R_s^2 + (X_C - X_L)^2}
\]

where

- \(Z\) = Total Impedance
- \(R_s\) = Series Resistance
- \(X_C\) = Capacitive Reactance
- \(X_L\) = Inductive Reactance

The variation of a capacitor’s impedance with frequency determines its effectiveness in many applications.

Phase Angle – Power Factor and Dissipation Factor are often confused since they are both measures of the loss in a capacitor under AC application and are often almost identical in value. In a “perfect” capacitor the current in the capacitor will lead the voltage by 90°.
General Description

In practice the current leads the voltage by some other phase angle due to the series resistance $R_s$. The complement of this angle is called the loss angle and:

\[
\text{Power Factor (P.F.)} = \cos \phi \quad \text{or} \quad \sin \delta \\
\text{Dissipation Factor (D.F.)} = \tan \delta
\]

for small values of $\delta$ the tan and sine are essentially equal which has led to the common interchangeability of the two terms in the industry.

**Equivalent Series Resistance** – The term E.S.R. or Equivalent Series Resistance combines all losses both series and parallel in a capacitor at a given frequency so that the equivalent circuit is reduced to a simple R-C series connection.

**Dissipation Factor** – The DF/PF of a capacitor tells what percent of the apparent power input will turn to heat in the capacitor.

\[
\text{Dissipation Factor} = \frac{\text{E.S.R.}}{X_C} = (2 \pi fC) \times (\text{E.S.R.})
\]

The watts loss are:

\[
\text{Watts loss} = (2 \pi fCV^2) \times \text{(D.F.)}
\]

Very low values of dissipation factor are expressed as their reciprocal for convenience. These are called the “Q” or Quality factor of capacitors.

**Parasitic Inductance** – The parasitic inductance of capacitors is becoming more and more important in the decoupling of today’s high speed digital systems. The relationship between the inductance and the ripple voltage induced on the DC voltage line can be seen from the simple inductance equation:

\[
V = L \frac{di}{dt}
\]

The $\frac{di}{dt}$ seen in current microprocessors can be as high as 0.3 A/ns, and up to 10A/ns. At 0.3 A/ns, 100pH of parasitic inductance can cause a voltage spike of 30mV. While this does not sound very drastic, with the $V_{cc}$ for microprocessors decreasing at the current rate, this can be a fairly large percentage.

Another important, often overlooked, reason for knowing the parasitic inductance is the calculation of the resonant frequency. This can be important for high frequency, bypass capacitors, as the resonant point will give the most signal attenuation. The resonant frequency is calculated from the simple equation:

\[
f_{res} = \frac{1}{2\pi L C}
\]

**Insulation Resistance** – Insulation Resistance is the resistance measured across the terminals of a capacitor and consists principally of the parallel resistance $R_P$ shown in the equivalent circuit. As capacitance values and hence the area of dielectric increases, the I.R. decreases and hence the product $(C \times IR$ or $RC)$ is often specified in ohm farads or more commonly megohm-microfarads. Leakage current is determined by dividing the rated voltage by IR (Ohm’s Law).

**Dielectric Strength** – Dielectric Strength is an expression of the ability of a material to withstand an electrical stress. Although dielectric strength is ordinarily expressed in volts, it is actually dependent on the thickness of the dielectric and thus is also more generically a function of volts/mil.

**Dielectric Absorption** – A capacitor does not discharge instantaneously upon application of a short circuit, but drains gradually after the capacitance proper has been discharged. It is common practice to measure the dielectric absorption by determining the “reappearing voltage” which appears across a capacitor at some point in time after it has been fully discharged under short circuit conditions.

**Corona** – Corona is the ionization of air or other vapors which causes them to conduct current. It is especially prevalent in high voltage units but can occur with low voltages as well where high voltage gradients occur. The energy discharged degrades the performance of the capacitor and can in time cause catastrophic failures.
REFLOW SOLDERING

Component Pad Design
Component pads should be designed to achieve good solder fillets and minimize component movement during reflow soldering. Pad designs are given below for the most common sizes of multilayer ceramic capacitors for both wave and reflow soldering. The basis of these designs is:

- Pad width equal to component width. It is permissible to decrease this to as low as 85% of component width but it is not advisable to go below this.
- Pad overlap 0.5mm beneath component.
- Pad extension 0.5mm beyond components for reflow and 1.0mm for wave soldering.

<table>
<thead>
<tr>
<th>Case Size</th>
<th>D1</th>
<th>D2</th>
<th>D3</th>
<th>D4</th>
<th>D5</th>
</tr>
</thead>
<tbody>
<tr>
<td>0201</td>
<td>0.85 (0.033)</td>
<td>0.30 (0.012)</td>
<td>0.25 (0.010)</td>
<td>0.30 (0.012)</td>
<td>0.35 (0.014)</td>
</tr>
<tr>
<td>0402</td>
<td>1.70 (0.067)</td>
<td>0.60 (0.024)</td>
<td>0.50 (0.020)</td>
<td>0.60 (0.024)</td>
<td>0.50 (0.020)</td>
</tr>
<tr>
<td>0603</td>
<td>2.30 (0.091)</td>
<td>0.80 (0.031)</td>
<td>0.70 (0.028)</td>
<td>0.80 (0.031)</td>
<td>0.75 (0.030)</td>
</tr>
<tr>
<td>0805</td>
<td>3.00 (0.118)</td>
<td>1.00 (0.039)</td>
<td>1.00 (0.039)</td>
<td>1.00 (0.039)</td>
<td>1.25 (0.049)</td>
</tr>
<tr>
<td>1206</td>
<td>4.00 (0.157)</td>
<td>1.00 (0.039)</td>
<td>2.00 (0.079)</td>
<td>1.00 (0.039)</td>
<td>1.60 (0.063)</td>
</tr>
<tr>
<td>1210</td>
<td>4.00 (0.157)</td>
<td>1.00 (0.039)</td>
<td>2.00 (0.079)</td>
<td>1.00 (0.039)</td>
<td>2.50 (0.098)</td>
</tr>
<tr>
<td>1808</td>
<td>5.60 (0.220)</td>
<td>1.00 (0.039)</td>
<td>3.60 (0.142)</td>
<td>1.00 (0.039)</td>
<td>2.00 (0.079)</td>
</tr>
<tr>
<td>1812</td>
<td>5.60 (0.220)</td>
<td>1.00 (0.039)</td>
<td>3.60 (0.142)</td>
<td>1.00 (0.039)</td>
<td>3.00 (0.118)</td>
</tr>
<tr>
<td>1825</td>
<td>5.60 (0.220)</td>
<td>1.00 (0.039)</td>
<td>3.60 (0.142)</td>
<td>1.00 (0.039)</td>
<td>6.35 (0.250)</td>
</tr>
<tr>
<td>2220</td>
<td>6.60 (0.260)</td>
<td>1.00 (0.039)</td>
<td>4.60 (0.181)</td>
<td>1.00 (0.039)</td>
<td>5.00 (0.197)</td>
</tr>
<tr>
<td>2225</td>
<td>6.60 (0.260)</td>
<td>1.00 (0.039)</td>
<td>4.60 (0.181)</td>
<td>1.00 (0.039)</td>
<td>6.35 (0.250)</td>
</tr>
</tbody>
</table>

WAVE SOLDERING

Component Spacing
For wave soldering components, must be spaced sufficiently far apart to avoid bridging or shadowing (inability of solder to penetrate properly into small spaces). This is less important for reflow soldering but sufficient space must be allowed to enable rework should it be required.

Preheat & Soldering
The rate of preheat should not exceed 4°C/second to prevent thermal shock. A better maximum figure is about 2°C/second.

For capacitors size 1206 and below, with a maximum thickness of 1.25mm, it is generally permissible to allow a temperature differential from preheat to soldering of 150°C. In all other cases this differential should not exceed 100°C.

For further specific application or process advice, please consult AVX.

Cleaning
Care should be taken to ensure that the capacitors are thoroughly cleaned of flux residues especially the space beneath the capacitor. Such residues may otherwise become conductive and effectively offer a low resistance bypass to the capacitor.

Ultrasonic cleaning is permissible, the recommended conditions being 8 Watts/litre at 20-45 kHz, with a process cycle of 2 minutes vapor rinse, 2 minutes immersion in the ultrasonic solvent bath and finally 2 minutes vapor rinse.
Surface Mounting Guide

Recommended Soldering Profiles

REFLOW SOLDER PROFILES

AVX RoHS compliant products utilize termination finishes (e.g., Sn or SnAg) that are compatible with all Pb-Free soldering systems and are fully reverse compatible with SnPb soldering systems. A recommended SnPb profile is shown for comparison; for Pb-Free soldering, IPC/JEDEC J-STD-020C may be referenced. The upper line in the chart shows the maximum envelope to which products are qualified (typically 3x reflow cycles at 260°C max). The center line gives the recommended profile for optimum wettability and soldering in Pb-Free Systems.

Preheat:
The pre-heat stabilizes the part and reduces the temperature differential prior to reflow. The initial ramp to 125°C may be rapid, but from that point (2-3°C/sec is recommended to allow ceramic parts to heat uniformly and plastic encapsulated parts to stabilize through the glass transition temperature of the body (~180°C).

Reflow:
In the reflow phase, the maximum recommended time > 230°C is 40secs. Time at peak reflow is 10secs max.; optimum reflow is achieved at 250°C, (see wetting balance chart opposite) but products are qualified to 260°C max. Please reference individual product datasheets for maximum limits.

Cool Down:
Cool down should not be forced and 6°C/sec is recommended. A slow cool down will result in a finer grain structure of the reflow solder in the solder fillet.

WAVE SOLDER PROFILES

For wave solder, there is no change in the recommended wave profile; all standard Pb-Free (SnCu/SnCuAg) systems operate at the same 260°C max recommended for SnPb systems.

Preheat:
This is more important for wave solder; a higher temperature preheat will reduce the thermal shock to SMD parts that are immersed (please consult individual product data sheets for SMD parts that are suited to wave solder). SMD parts should ideally be heated from the bottom-Side prior to wave. PTH (Pin through hole) parts on the topside should not be separately heated.

Wave:
250°C – 260°C recommended for optimum solderability.

Cool Down:
As with reflow solder, cool down should not be forced and 6°C/sec is recommended. Any air knives at the end of the 2nd wave should be heated.

IMPORTANT NOTE: Typical Pb-Free reflow solders have a more dull and grainy appearance compared to traditional SnPb. Elevating the reflow temperature will not change this, but extending the cool down can help improve the visual appearance of the joint.
Storage
The components should be stored in their “as received packaging” where possible. If the components are removed from their original packaging then they should be stored in an airtight container (e.g. a heat sealed plastic bag) with desiccant (e.g. silica gel). Storage area temperature should be kept between +5 degrees C and +30 degrees C with humidity < 70% RH. Storage atmosphere must be free of gas containing sulfur and chlorine. Avoid exposing the product to saline moisture or to temperature changes that might result in the formation of condensation. To assure good solderability performance we recommend that the product be used within 6 months from our shipping date, but can be used for up to 12 months. Chip capacitors may crack if exposed to hydrogen (H2) gas while sealed or if coated with silicon, which generates hydrogen gas.

Solderability
Terminations to be well soldered after immersion in a 60/40 tin/lead solder bath at 245°C +/- 5°C for 5 +0/-0.5 seconds.

Leaching
Terminations will resist leaching for at least the immersion times and conditions shown below.

<table>
<thead>
<tr>
<th>Termination Type</th>
<th>Solder Tin/Lead/Silver</th>
<th>Solder Temp °C</th>
<th>Immersion Time Seconds</th>
</tr>
</thead>
<tbody>
<tr>
<td>Nickel Barrier</td>
<td>60/40/0</td>
<td>260 ± 5</td>
<td>30 ± 1</td>
</tr>
</tbody>
</table>

Lead-Free Wave Soldering
The recommended peak temperature for lead-free wave soldering is 250°C-260°C for 3-5 seconds. The other parameters of the profile remains the same as above.

The following should be noted by customers changing from lead based systems to the new lead free pastes.

A. The visual standards used for evaluation of solder joints will need to be modified as lead free joints are not as bright as with tin-lead pastes and the fillet may not be as large.

B. Lead-free solder pastes do not allow the same self alignment as lead containing systems. Standard mounting pads are acceptable, but machine set up may need to be modified.

General
Surface mounting chip multilayer ceramic capacitors are designed for soldering to printed circuit boards or other substrates. The construction of the components is such that they will withstand the time/temperature profiles used in both wave and reflow soldering methods.

Handling
Chip multilayer ceramic capacitors should be handled with care to avoid damage or contamination from perspiration and skin oils. The use of tweezers or vacuum pick ups is strongly recommended for individual components. Bulk handling should ensure that abrasion and mechanical shock are minimized. Taped and reeled components provides the ideal medium for direct presentation to the placement machine. Any mechanical shock should be minimized during handling chip multilayer ceramic capacitors.

Preheat
It is important to avoid the possibility of thermal shock during soldering and carefully controlled preheat is therefore required. The rate of preheat should not exceed 4°C/second and a target figure 2°C/second is recommended. Although an 80°C to 120°C temperature differential is preferred, recent developments allow a temperature differential between the component surface and the soldering temperature of 150°C (Maximum) for capacitors of 1210 size and below with a maximum thickness of 1.25mm. The user is cautioned that the risk of thermal shock increases as chip size or temperature differential increases.

Soldering
Mildly activated rosin fluxes are preferred. The minimum amount of solder to give a good joint should be used. Excessive solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. AVX terminations are suitable for all wave and reflow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

Cooling
Natural cooling in air is preferred, as this minimizes stresses within the soldered joint. When forced air cooling is used, cooling rate should not exceed 4°C/second. Quenching is not recommended but if used, maximum temperature differentials should be observed according to the preheat conditions above.

Cleaning
Flux residues may be hygroscopic or acidic and must be removed. AVX MLC capacitors are acceptable for use with all of the solvents described in the specifications MIL-STD-202 and EIA-RS-198. Alcohol based solvents are acceptable and properly controlled water cleaning systems are also acceptable. Many other solvents have been proven successful, and most solvents that are acceptable to other components on circuit assemblies are equally acceptable for use with ceramic capacitors.

Prevention of Metallic Migration
Note that when components with Sn plating on the end terminations are to be used in applications that are likely to experience conditions of high humidity under bias voltage, we strongly recommend that the circuit boards be conformally coated to protect the Sn from moisture that might lead to migration and eventual current leakage. When using Capacitor Arrays we recommend that there is no differential in applied voltage between adjacent elements.
Surface Mounting Guide
MLC Chip Capacitors

POST SOLDER HANDLING

Once SMP components are soldered to the board, any bending or flexure of the PCB applies stresses to the soldered joints of the components. For leaded devices, the stresses are absorbed by the compliancy of the metal leads and generally don’t result in problems unless the stress is large enough to fracture the soldered connection.

Ceramic capacitors are more susceptible to such stress because they don’t have compliant leads and are brittle in nature. The most frequent failure mode is low DC resistance or short circuit. The second failure mode is significant loss of capacitance due to severing of contact between sets of the internal electrodes.

Cracks caused by mechanical flexure are very easily identified and generally take one of the following two general forms:

Mechanical cracks are often hidden underneath the termination and are difficult to see externally. However, if one end termination falls off during the removal process from PCB, this is one indication that the cause of failure was excessive mechanical stress due to board warping.

Type A:
Angled crack between bottom of device to top of solder joint.

Type B:
Fracture from top of device to bottom of device.
COMMON CAUSES OF MECHANICAL CRACKING

The most common source for mechanical stress is board depanelization equipment, such as manual breakapart, v-cutters and shear presses. Improperly aligned or dull cutters may cause torqueing of the PCB resulting in flex stresses being transmitted to components near the board edge. Another common source of flexural stress is contact during parametric testing when test points are probed. If the PCB is allowed to flex during the test cycle, nearby ceramic capacitors may be broken.

A third common source is board to board connections at vertical connectors where cables or other PCBs are connected to the PCB. If the board is not supported during the plug/unplug cycle, it may flex and cause damage to nearby components.

Special care should also be taken when handling large (>6" on a side) PCBs since they more easily flex or warp than smaller boards.

REWORKING OF MLCs

Thermal shock is common in MLCs that are manually attached or reworked with a soldering iron. AVX strongly recommends that any reworking of MLCs be done with hot air reflow rather than soldering irons. It is practically impossible to cause any thermal shock in ceramic capacitors when using hot air reflow.

However direct contact by the soldering iron tip often causes thermal cracks that may fail at a later date. If rework by soldering iron is absolutely necessary, it is recommended that the wattage of the iron be less than 30 watts and the tip temperature be <300ºC. Rework should be performed by applying the solder iron tip to the pad and not directly contacting any part of the ceramic capacitor.

PCB BOARD DESIGN

To avoid many of the handling problems, AVX recommends that MLCs be located at least .2" away from nearest edge of board. However when this is not possible, AVX recommends that the panel be routed along the cut line, adjacent to where the MLC is located.